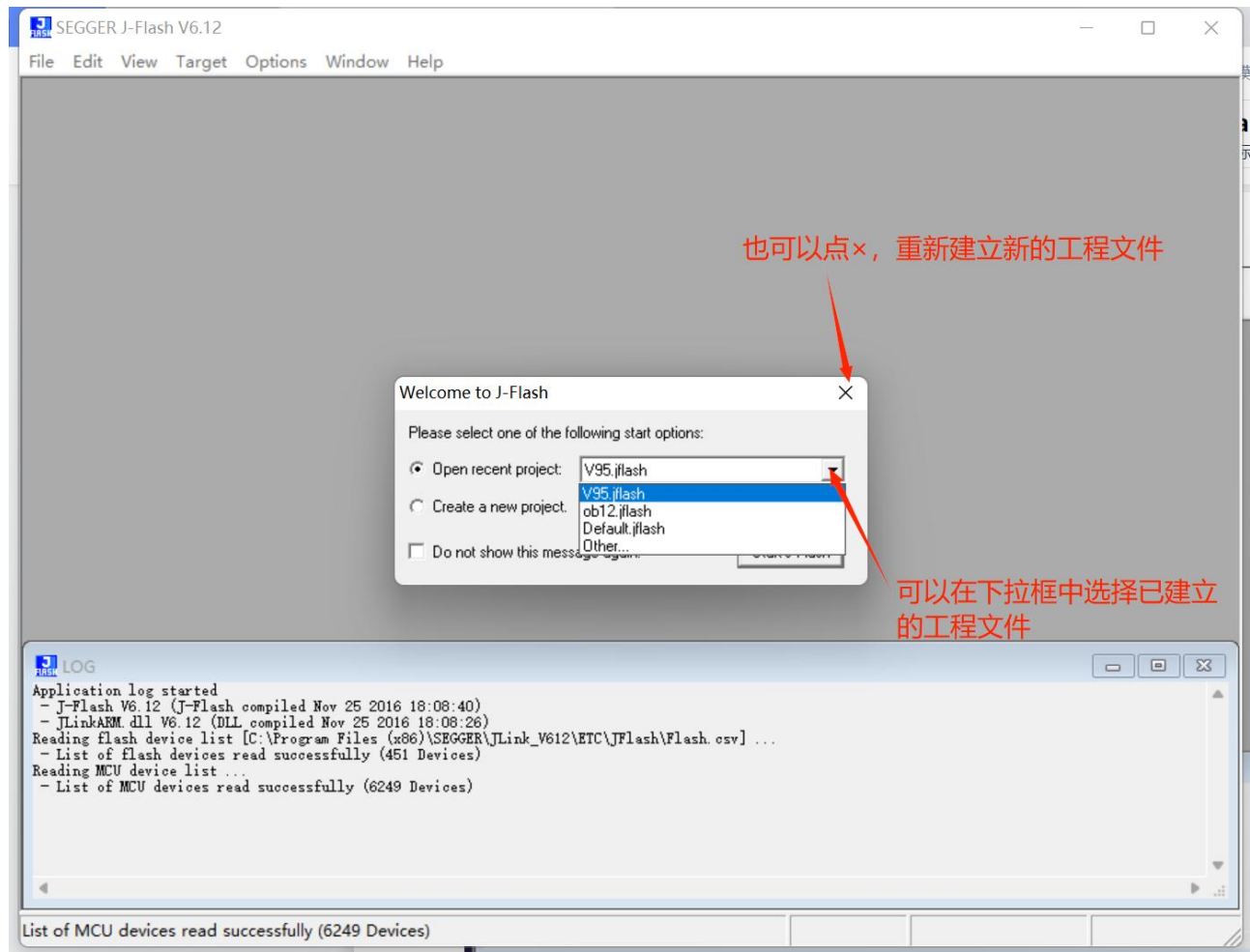
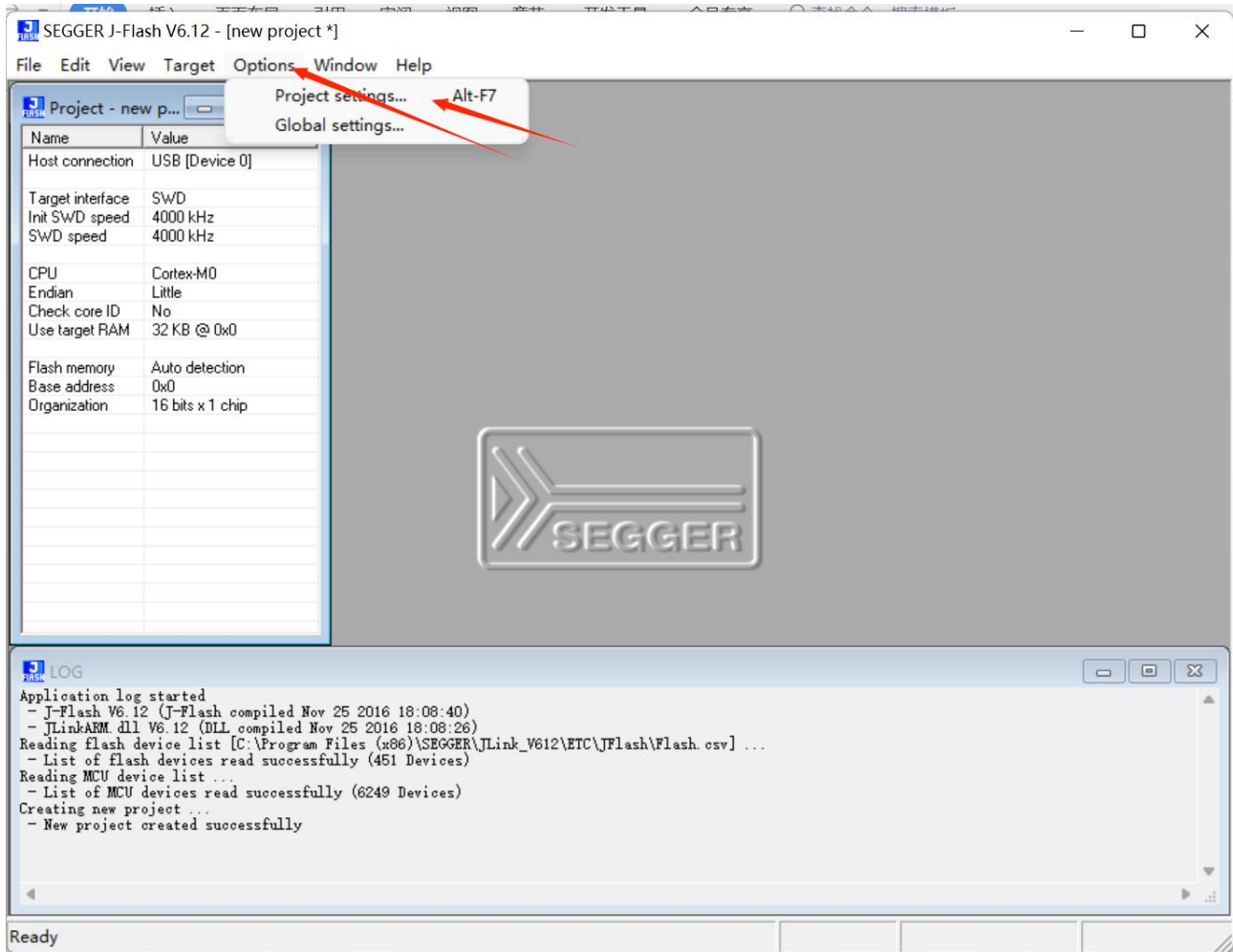
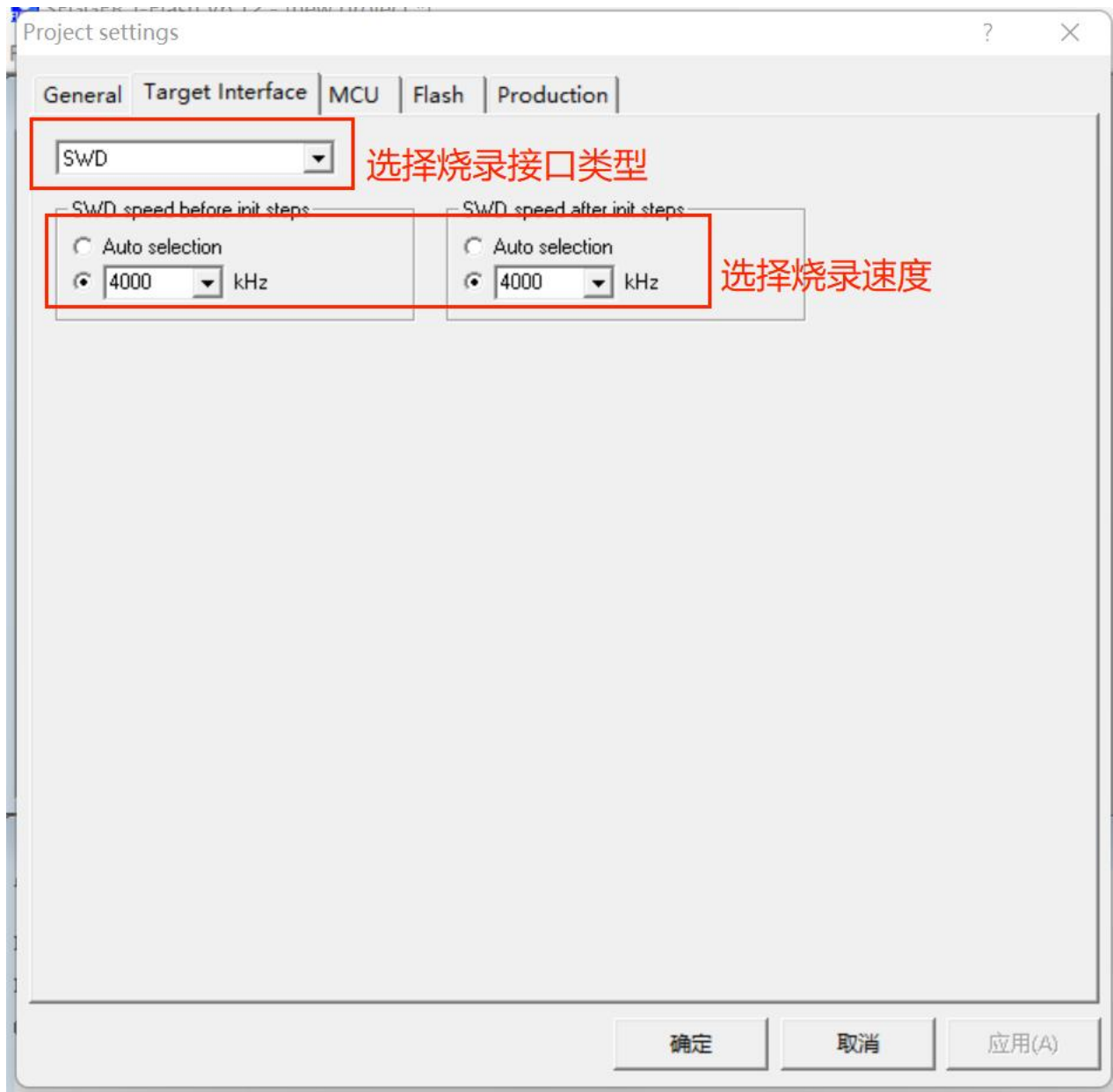


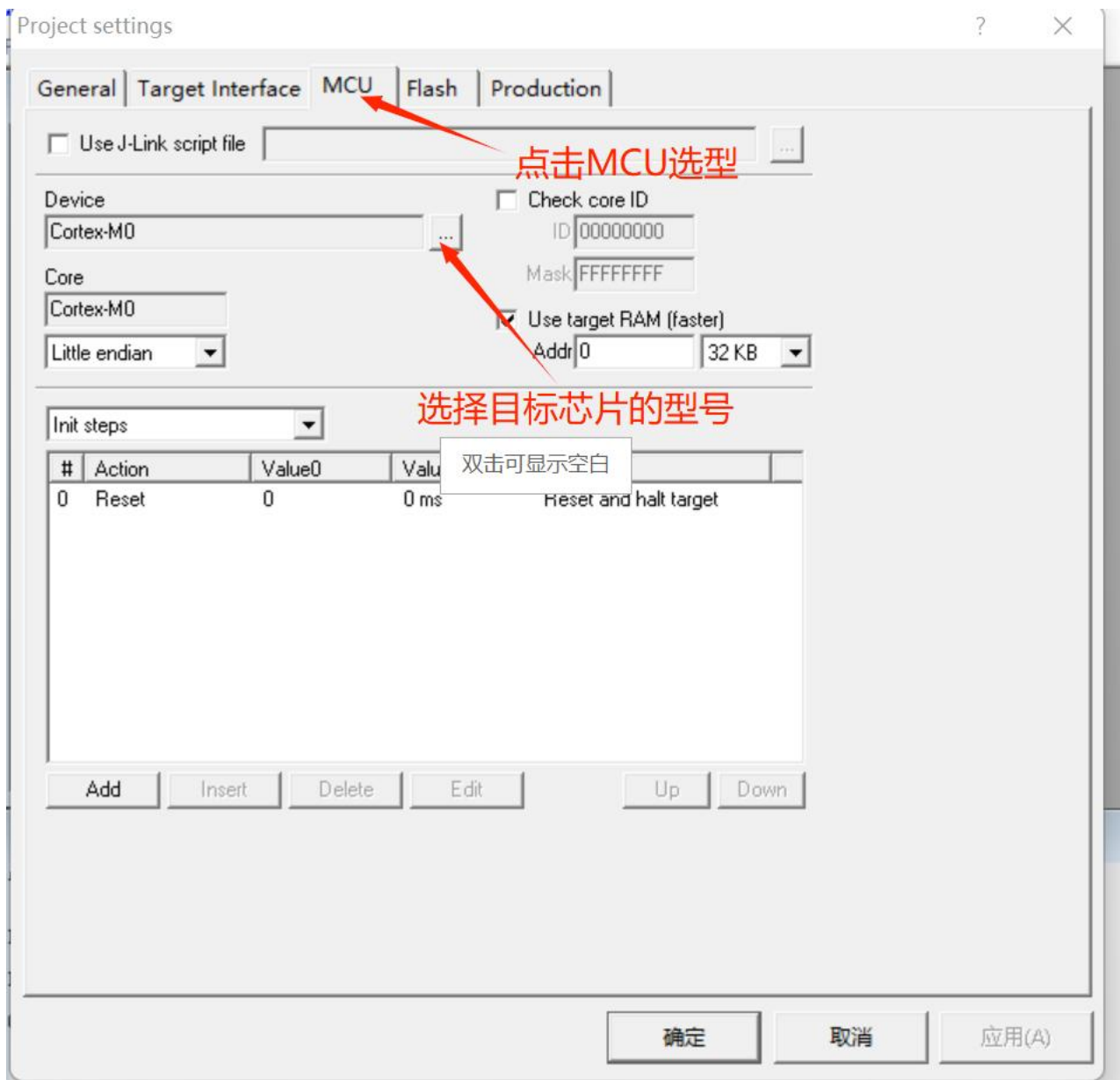
## 1. 双击 Jflash 软件，建立新的工程文件。



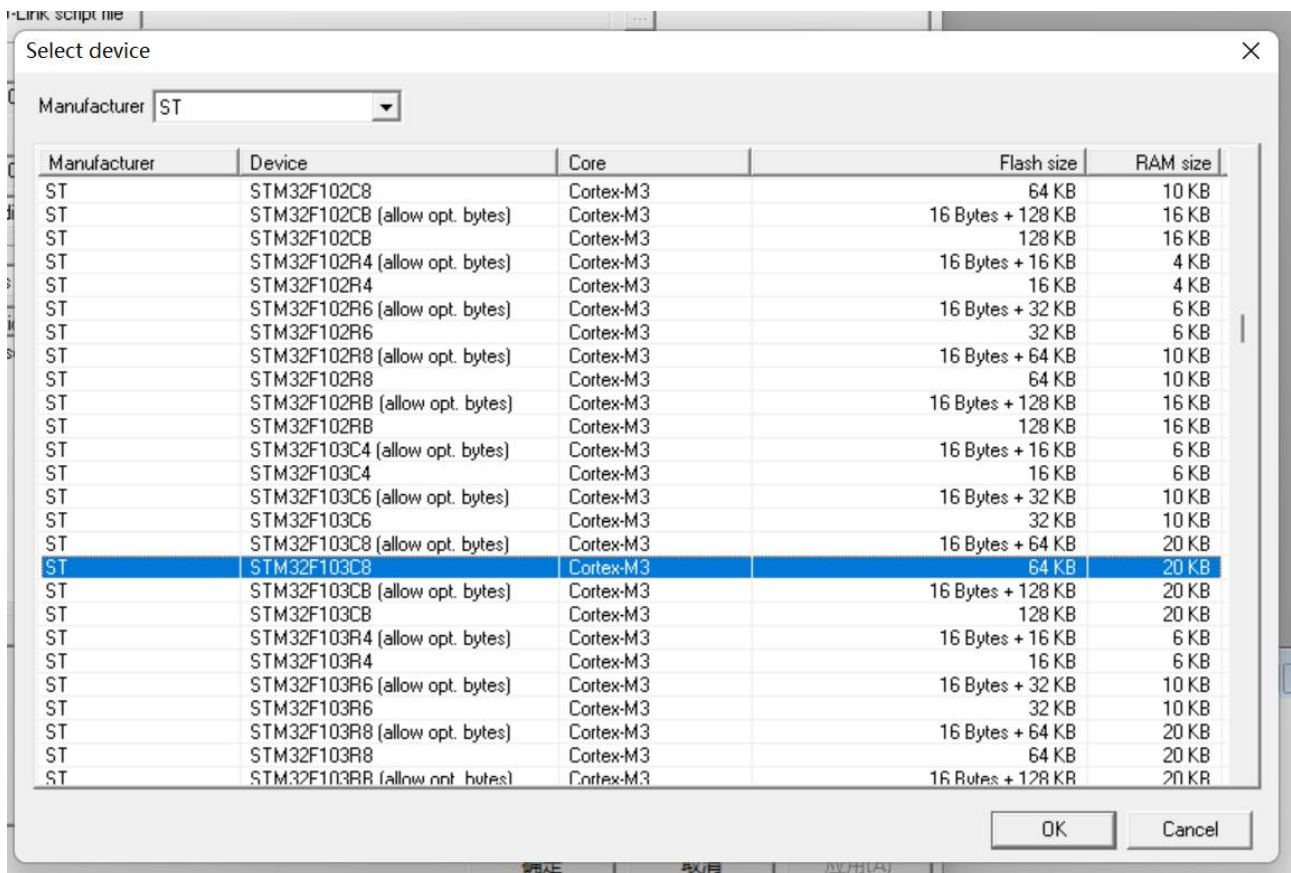
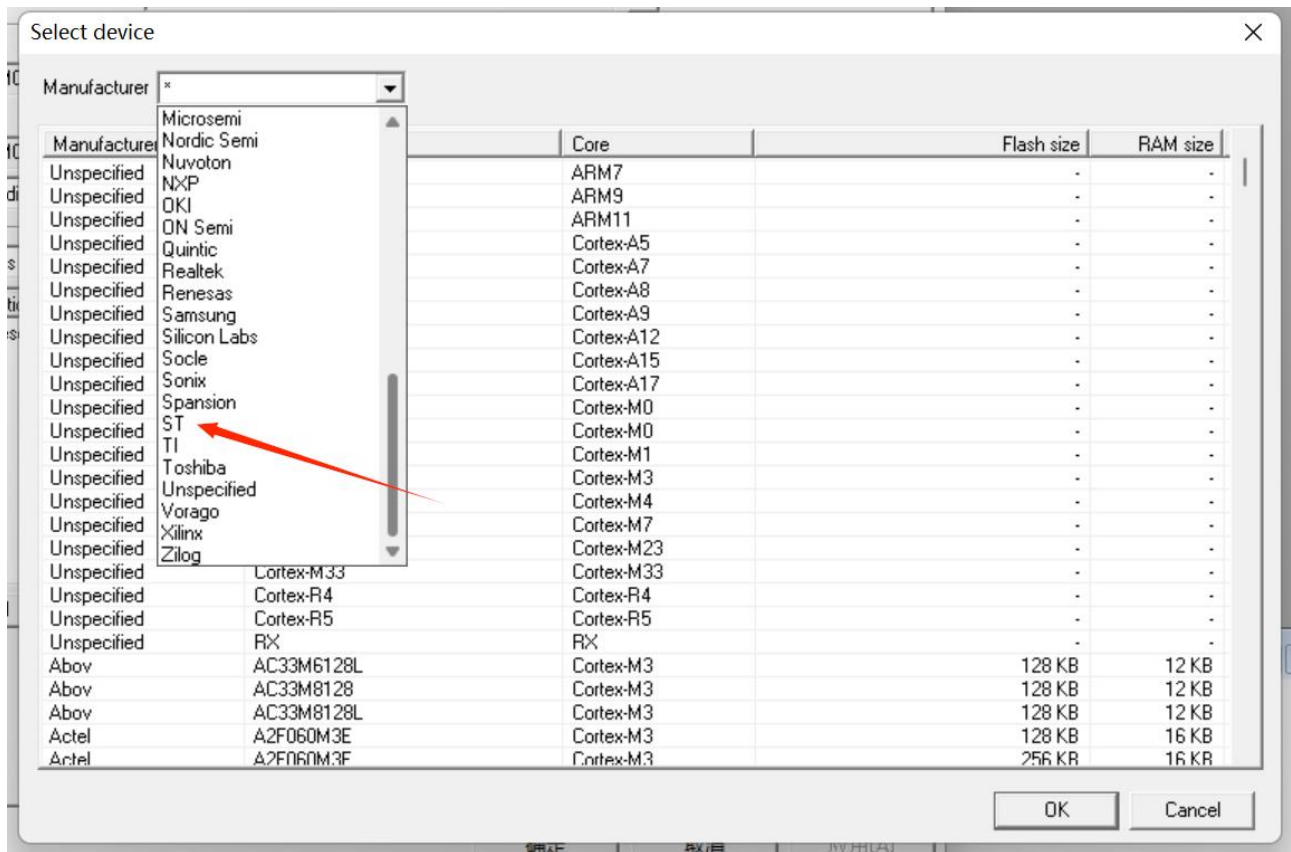
## 2. 工程配置

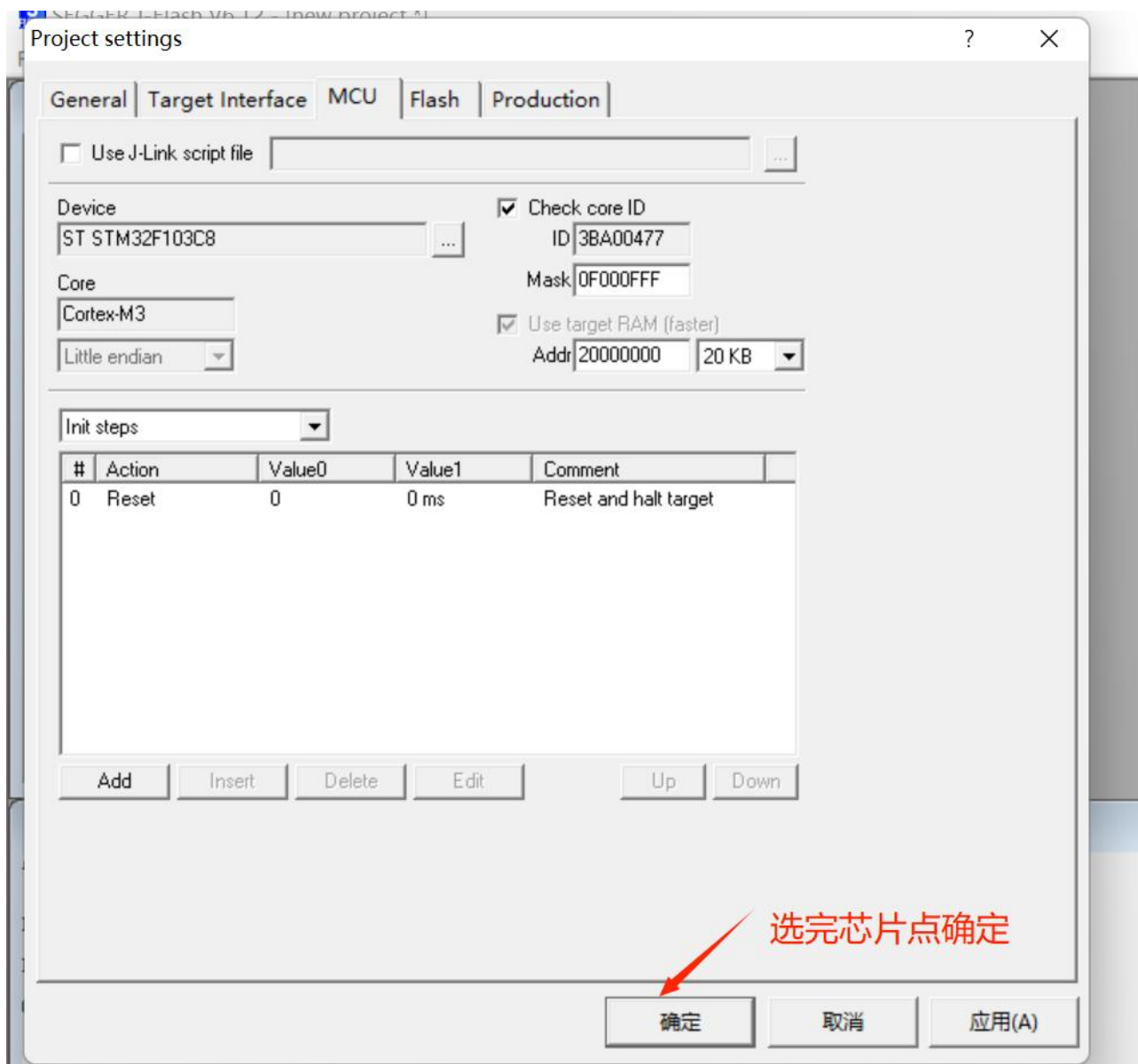


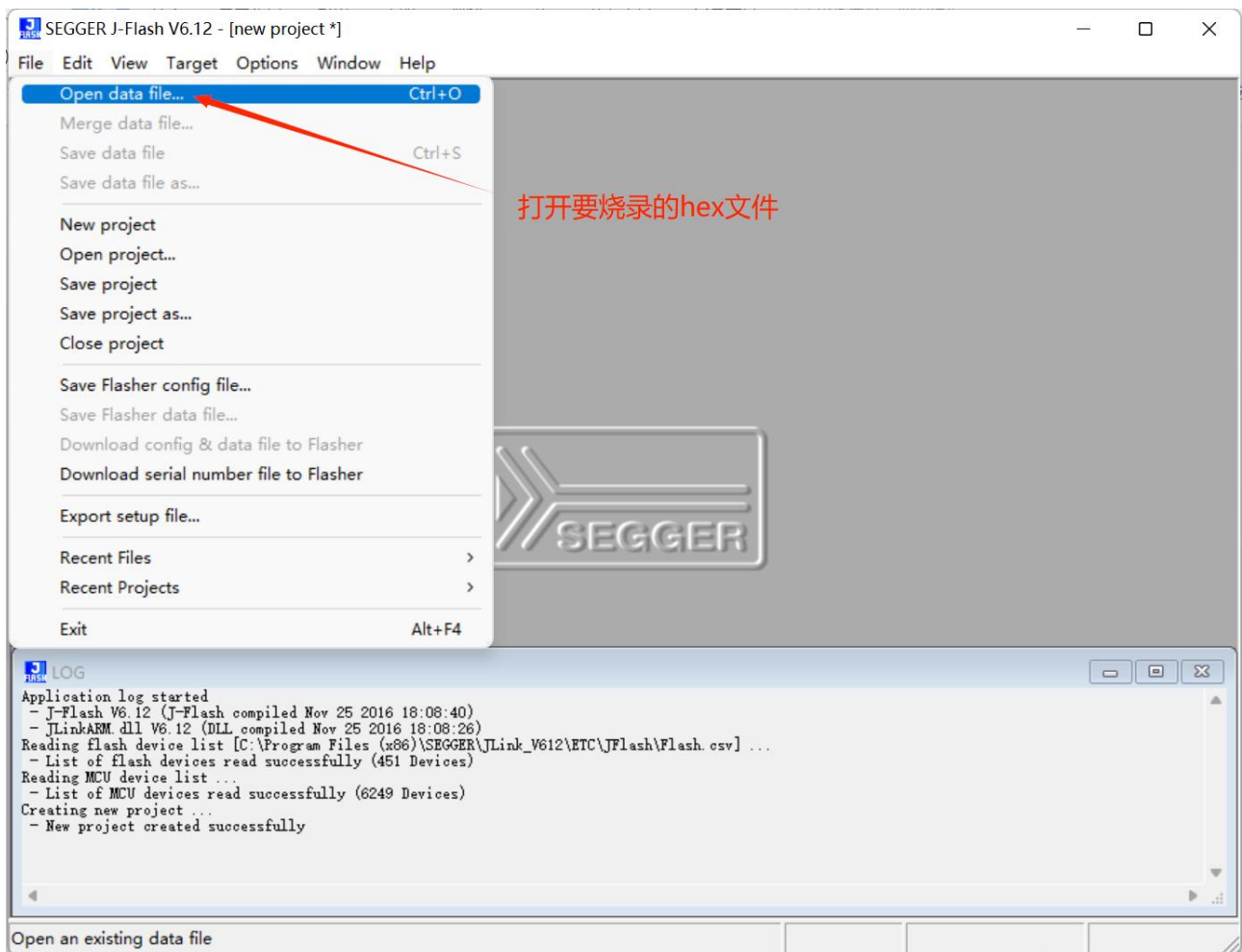




以 STM32F103C8T6 为例。







根据芯片的实际情况填写地址，STM32F103C8T6 是 0X8000000。

SEGGER J-Flash V6.12 - [new project \*]

File Edit View Target Options Window Help

Project - new p...

Name	Value
Host connection	USB [Device 0]
Target interface	SWD
Init SWD speed	4000 kHz
SWD speed	4000 kHz
MCU	ST STM32F103C8
Core	Cortex-M3
Endian	Little
Check core ID	Yes (0x3BA00477)
Use target RAM	20 KB @ 0x20000000
Flash memory	Internal bank 0
Base address	0x8000000
Flash size	64 KB

工程配置信息

Address: 0x8000000 x1 x2 x4

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
80000000	00	04	00	20	45	01	00	08	09	04	00	08	01	04	00	08	... E .....
80000010	05	04	00	08	8D	01	00	08	7D	05	00	08	00	00	00	00	.....>.....
80000020	00	00	00	00	00	00	00	00	00	00	00	00	2D	04	00	08	.....-...
80000030	91	01	00	08	00	00	00	00	0B	04	00	08	19	05	00	08	.....
80000040	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000050	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000060	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000070	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000080	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000090	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000A0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000B0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000C0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000D0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000E0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
800000F0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000100	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000110	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000120	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....

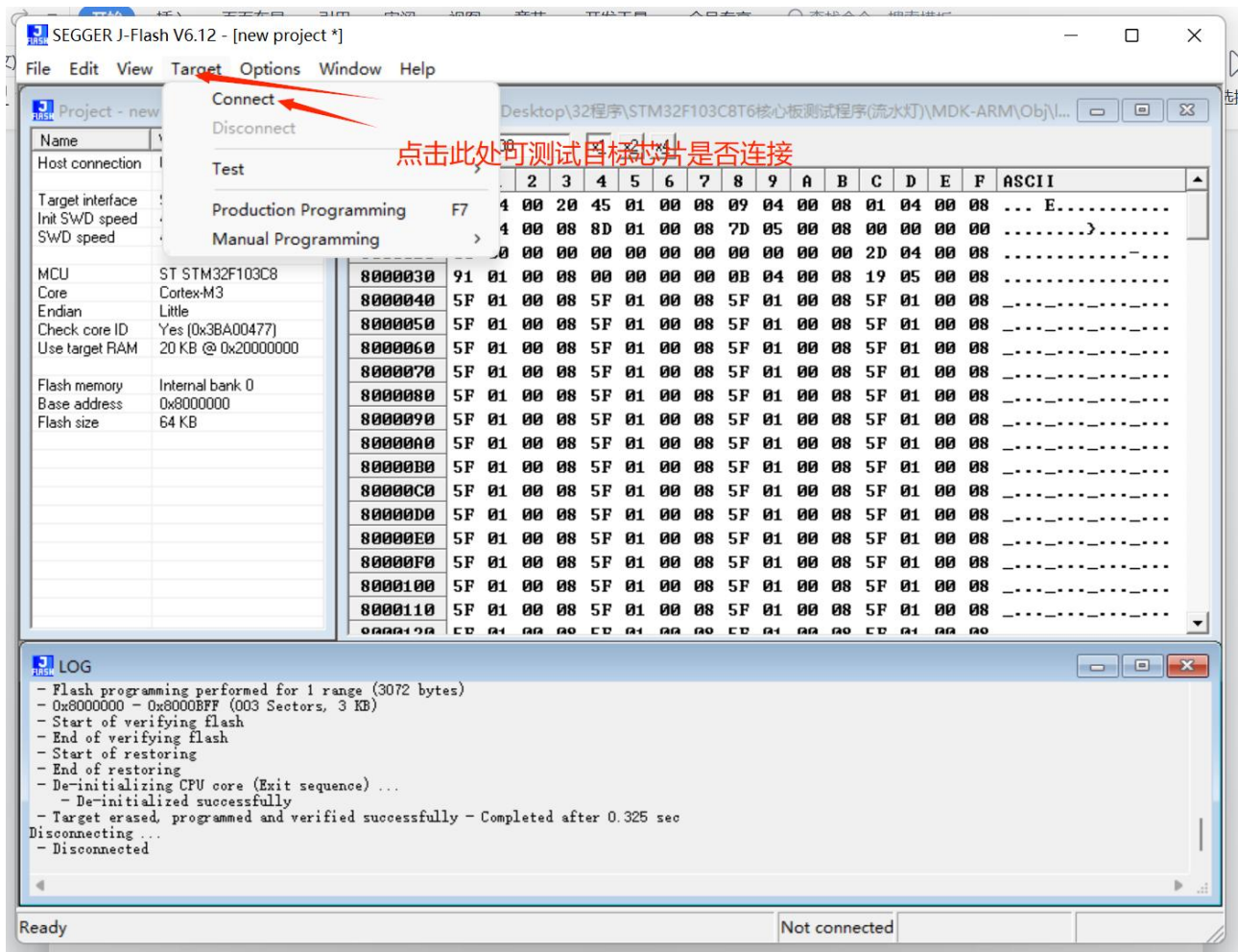
LOG

- Programming range 0x80000000 - 0x80000BFF (003 Sectors, 3 KB)
- End of flash programming
- Flash programming performed for 1 range (3072 bytes)
- 0x80000000 - 0x80000BFF (003 Sectors, 3 KB)
- Start of verifying flash
- End of verifying flash
- Start of restoring
- End of restoring
- De-initializing CPU core (Exit sequence) ...
  - De-initialized successfully
- Target erased, programmed and verified successfully - Completed after 0.221 sec

Ready Connected Core Id: 0x1BA01477 Speed: 4000 kH







SEGGER J-Flash V6.12 - [new project \*]

File Edit View Target Options Window Help

Project - new p...

Name	Value
Host connection	USB [Device 0]
Target interface	SWD
Init SWD speed	4000 kHz
SWD speed	4000 kHz
MCU	ST STM32F103C8
Core	Cortex-M3
Endian	Little
Check core ID	Yes (0x3BA00477)
Use target RAM	20 KB @ 0x20000000
Flash memory	Internal bank 0
Base address	0x8000000
Flash size	64 KB

C:\Users\86153\Desktop\32程序\STM32F103C8T6核心板测试程序(流水灯)\MDK-ARM\Obj\l...

Address: 0x8000000 x1 x2 x4

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
8000000	00	04	00	20	45	01	00	08	09	04	00	08	01	04	00	08	... E.....
8000010	05	04	00	08	8D	01	00	08	7D	05	00	08	00	00	00	00	.....>.....
8000020	00	00	00	00	00	00	00	00	00	00	00	00	2D	04	00	08	.....-....
8000030	91	01	00	08	00	00	00	00	0B	04	00	08	19	05	00	08	.....
8000040	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000050	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000060	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000070	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000080	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000090	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000A0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000B0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000C0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000D0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000E0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
80000F0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000100	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000110	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....
8000120	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....

LOG

- Target erased, programmed and verified successfully - Completed after 0.325 sec

Disconnecting ...

- Disconnected

Connecting ...

- Connecting via USB to J-Link device 0

- Target interface speed: 4000 kHz (Fixed)

- VTarget = 3.275V

- Initializing CPU core (Init sequence) ...

- Initialized successfully

- Target interface speed: 4000 kHz (Fixed)

Connected successfully

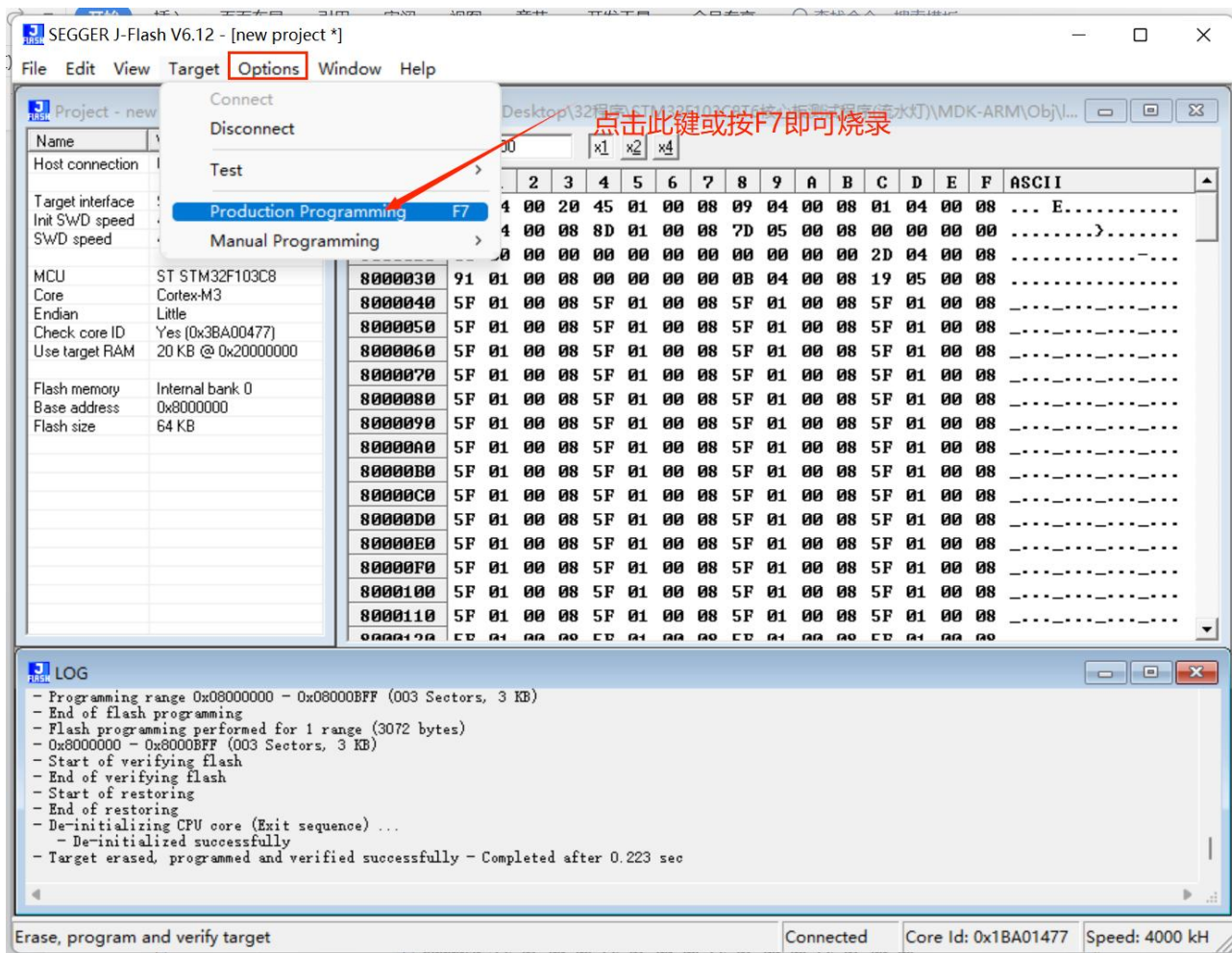
提示连接成功

Ready

Connected

Core Id: 0x1BA01477

Speed: 4000 kHz





SEGGER J-Flash V6.12 - [new project \*]

File Edit View Target Options Window Help

Project - new p... C:\Users\86153\Desktop\32程序\STM32F103C8T6核心板测试程序(流水灯)\MDK-ARM\Obj\...

Name	Value
Host connection	USB [Device 0]
Target interface	SWD
Init SWD speed	4000 kHz
SWD speed	4000 kHz
MCU	ST STM32F103C8
Core	Cortex-M3
Endian	Little
Check core ID	Yes (0x3BA00477)
Use target RAM	20 KB @ 0x20000000
Flash memory	Internal bank 0
Base address	0x8000000
Flash size	64 KB

Address: 0x8000000 x1 x2 x4

Address	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	ASCII
80000000	00	04	00	20	45	01	00	08	09	04	00	08	01	04	00	08	... E.....
80000010	05	04	00	08	8D	01	00	08	7D	05	00	08	00	00	00	00	.....>.....
80000020	00	00	00	00	00	00	00	00	00	00	00	00	2D	04	00	08	.....-....
80000030	91	01	00	08	00	00	00	00	0B	04	00	08	19	05	00	08	.....9.....
80000040	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
80000050	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
80000060	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
80000070	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
80000080	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
80000090	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000A0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000B0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000C0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000D0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000E0	5F	01	00	08	5F	01	00	08	5F	01	00	08	5F	01	00	08	.....F.....
800000F0	5F	01															.....F.....
80001000	5F	01															.....F.....
80001100	5F	01															.....F.....
80001200	5F	01															.....F.....

J-Flash V6.12

Target erased, programmed and verified successfully - Completed after 0.221 sec

烧录成功, 用时0.221秒

确定

LOG

- Programming range 0x08000000 - 0x08000BFF (003 Sectors, 3 KB)
- End of flash programming
- Flash programming performed for 1 range (3072 bytes)
- 0x80000000 - 0x8000BFF (003 Sectors, 3 KB)
- Start of verifying flash
- End of verifying flash
- Start of restoring
- End of restoring
- De-initializing CPU core (Exit sequence) ...
- De-initialized successfully
- Target erased, programmed and verified successfully - Completed after 0.221 sec

Ready Connected Core Id: 0x1BA01477 Speed: 4000 kHz