

BIRLA INSTITUTE OF SCIENCE AND TECHNOLOGY PILANI, HYDERABAD CAMPUS

DIGITAL DESIGN LABORATORY (Session 2021-22)

Workbook

Experiment -3

Full Name of the Student: Shyam N V

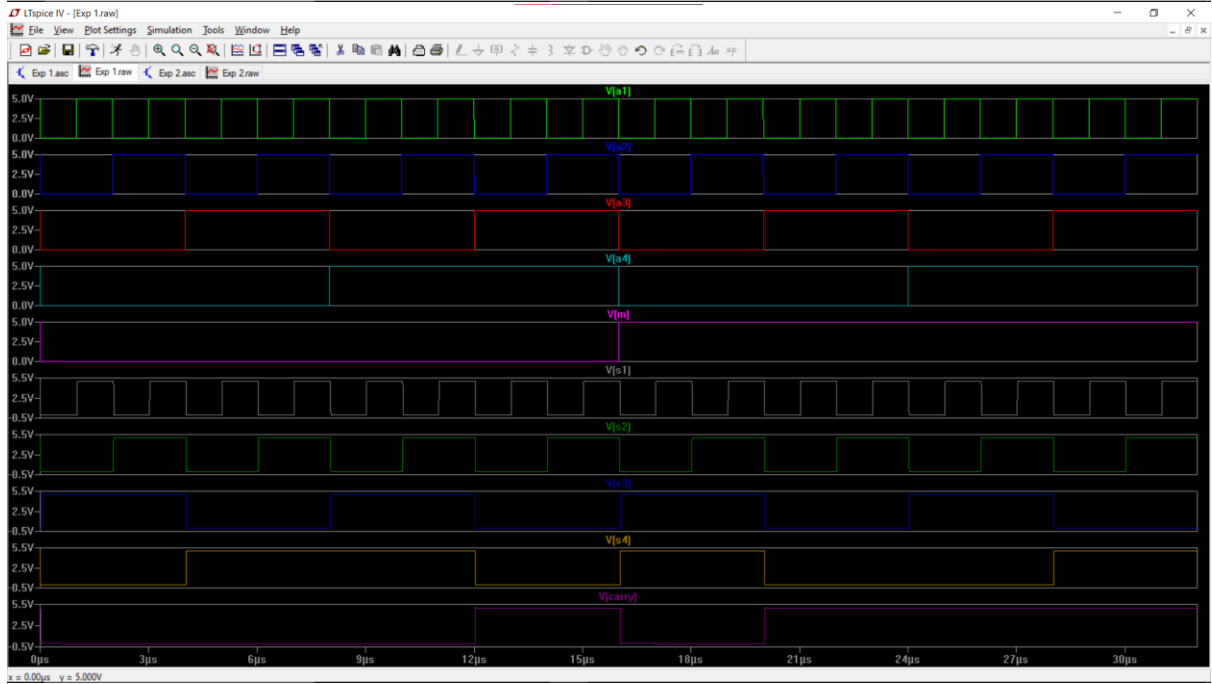
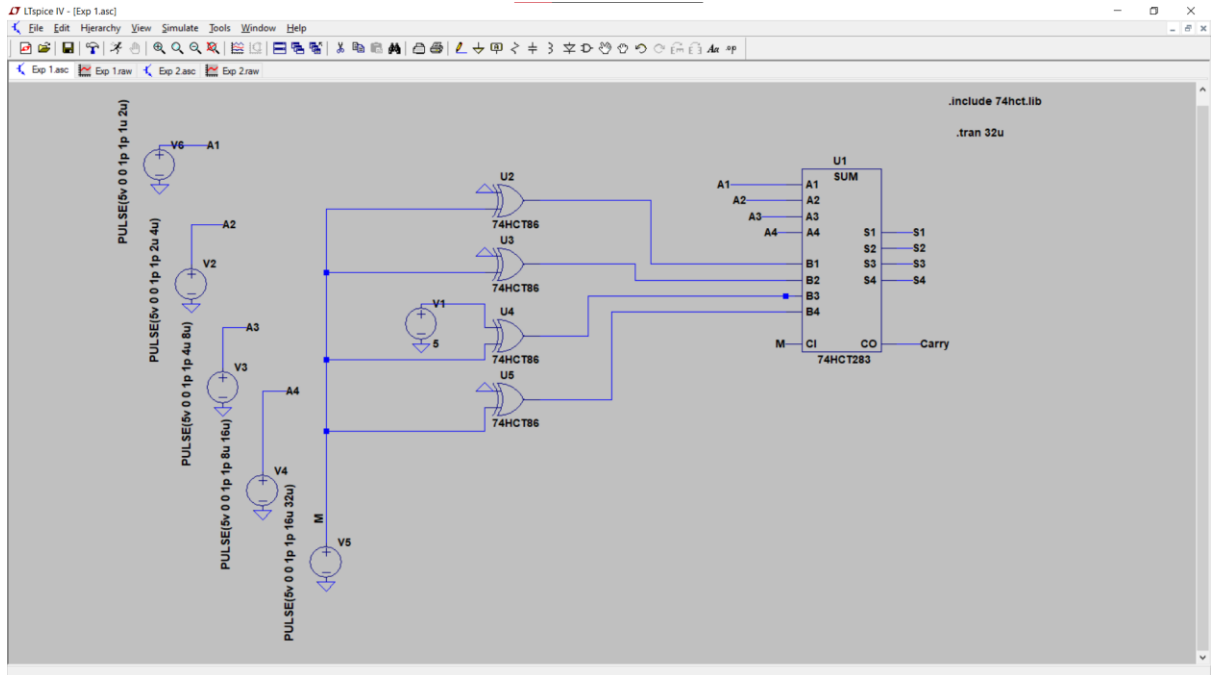
Complete ID of the student: 2020A7PS2081H

Title of Experiment: 4Bit adder/Subtractor and BCD adder

Problem 1:

Design and Implement four-bit Adder & Subtractor operation using IC 74283 in LTSPICE.

(Provide proper LTSPICE model snapshot with neat levelling and show the graphical output)



Problem 2:

Design and implement a BCD Adder operation using two 4 bit - Adder circuits in LTSPICE.

(Provide proper LTSPICE model snapshot with neat levelling and show the graphical output)

