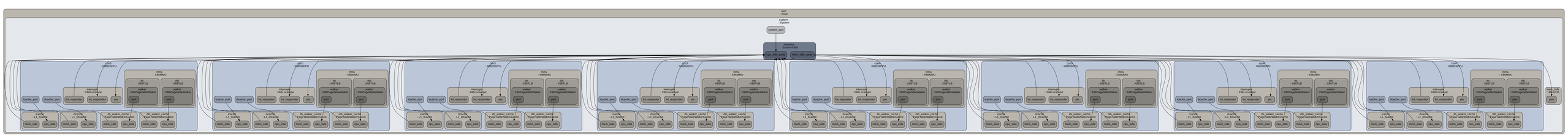

root : Root
system : System
system_port
membus : SystemXBar
cpu_side_ports
mem_side_ports
cpu0 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu1 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu2 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu3 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu4 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu5 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu6 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
cpu7 : X86O3CPU
mmu : X86MMU
itb : X86TLB
walker : X86PagetableWalker
port
dtb : X86TLB
walker : X86PagetableWalker
port
interrupts : X86LocalApic
int_requestor
int_responder
pio
icache_port
dcache_port
icache : L1_ICache
mem_side
cpu_side
dcache : L1_DCache
mem_side
cpu_side
itb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache : PageTableWalkerCache
mem_side
cpu_side
mem_ctrls : MemCtrl
port