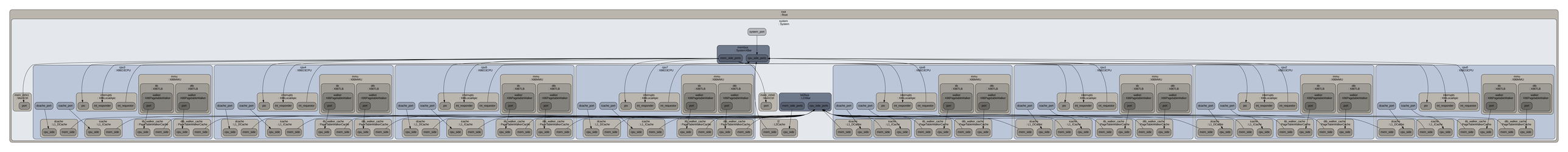

root
: Root
system
: System
system_port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
mem_ctrls1
: MemCtrl
port
cpu3
: X86O3CPU
dcache_port
icache_port
interrupts
: X86LocalApic
pio
int_responder
int_requestor
mmu
: X86MMU
itb
: X86TLB
walker
: X86PagetableWalker
port
dtb
: X86TLB
walker
: X86PagetableWalker
port
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
cpu4
: X86O3CPU
cpu5
: X86O3CPU
cpu7
: X86O3CPU
mem_ctrls0
: MemCtrl
port
tol2bus
: L2XBar
mem_side_ports
cpu_side_ports
l2
: L2Cache
mem_side
cpu_side
cpu6
: X86O3CPU
cpu1
: X86O3CPU
cpu2
: X86O3CPU
cpu0
: X86O3CPU