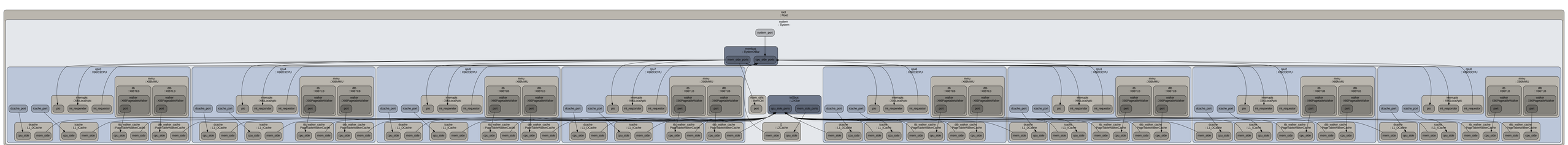

root
: Root
system
: System
system_port
membus
: SystemXBar
mem_side_ports
cpu_side_ports
cpu3
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
cpu4
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
cpu5
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
cpu7
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
mem_ctrls
: MemCtrl
port
tol2bus
: L2XBar
cpu_side_ports
mem_side_ports
l2
: L2Cache
mem_side
cpu_side
cpu6
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
mem_side
cpu_side
icache
: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
cpu1
: X86O3CPU
mmu
: X86MMU
itb
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dtb
: X86TLB
walker
: X86PagetableWalker
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: X86PagetableWalker
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interrupts
: X86LocalApic
dcache_port
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int_responder
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dcache
: L1_DCache
mem_side
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: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
cpu2
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
port
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
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int_responder
int_requestor
dcache
: L1_DCache
mem_side
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icache
: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
cpu0
: X86O3CPU
mmu
: X86MMU
itb
: X86TLB
dtb
: X86TLB
walker
: X86PagetableWalker
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walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
dcache_port
icache_port
pio
int_responder
int_requestor
dcache
: L1_DCache
mem_side
cpu_side
icache
: L1_ICache
mem_side
cpu_side
itb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side
dtb_walker_cache
: PageTableWalkerCache
mem_side
cpu_side