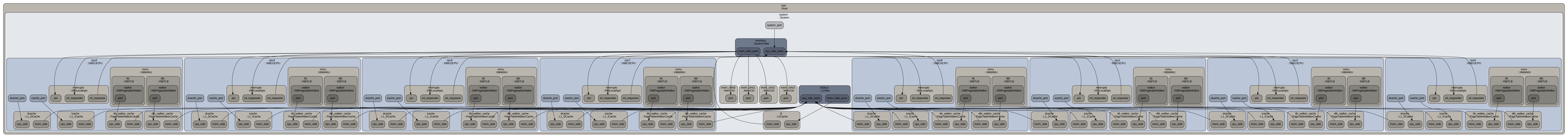
root
Root
system
System
system_port
membus
SystemXBar
mem_side_ports
cpu_side_ports
cpu2
X86O3CPU
cpu3
X86O3CPU
cpu6
X86O3CPU
cpu7
X86O3CPU
cpu8
X86O3CPU
cpu1
X86O3CPU
cpu4
X86O3CPU
cpu5
X86O3CPU
mmu
X86MMU
itb
X86TLB
dtb
X86TLB
walker
X86PagetableWalker
port
interrupts
X86LocalApic
pio
int_responder
int_requestor
int_slave
int_master
dcache_port
icache_port
dcache
L1_DCache
icache
L1_ICache
itb_walker_cache
PageTableWalkerCache
cpu_side
mem_side
mem_ctrls0
MemCtrl
mem_ctrls1
mem_ctrls2
mem_ctrls3
l2xbar
L2XBar
l2
L2Cache