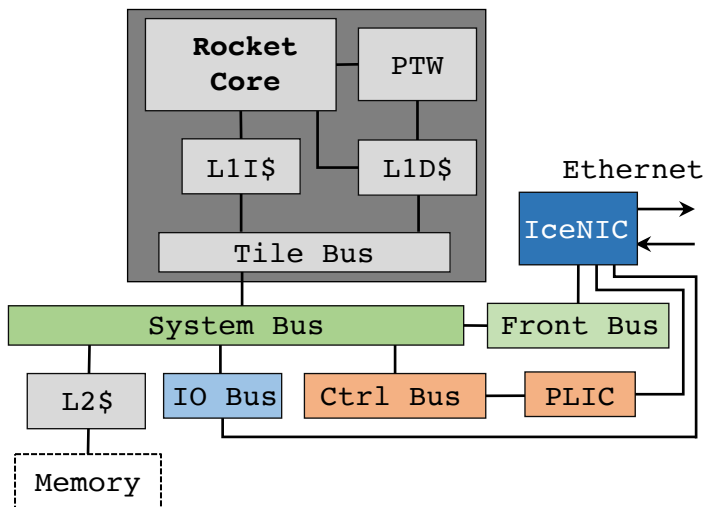


(a) The nanoPU Prototype



(b) Traditional RISC-V with IceNIC