

# **AXI4-Stream VVC** – Quick Reference

For general information see UVVM VVC Framwork Essential Mechanisms located in uvvm\_vvc\_framework/doc. CAUTION: shaded code/description is preliminary

# **AXI4-Stream Master**

In order to use the AXI4-Stream VVC in master mode, it must be instantiated in the test harness by setting the generic constant 'GC\_MASTER\_MODE' to TRUE.

axistream\_transmit (VVCT, vvc\_instance\_idx, data\_array, [user\_array, [strb\_array, id\_array, dest\_array]], msg, [scope])

Example: axistream\_transmit(AXISTREAM\_VVCT, 0, v\_data\_array(0 to v\_numBytes-1), v\_user\_array(0 to v\_numWords-1), "Send a 'v\_numBytes' byte packet to DUT"); axistream\_transmit(AXISTREAM\_VVCT, 0, v\_data\_array(0 to v\_numBytes-1)(31 downto 0), v\_user\_array(0 to v\_numWords-1), "Send a '4 x v\_numBytes' byte packet to DUT");

Note! Use axistream\_transmit\_bytes ( ) when using t\_byte\_array.



## **AXI4-Stream Slave**

In order to use the AXI4-Stream VVC in slave mode, it must be instantiated in the test harness by setting the generic constant 'GC\_MASTER\_MODE' to FALSE.

### axistream\_receive (VVCT, vvc\_instance\_idx, msg, [scope])

**Example**: axistream\_receive (AXISTREAM\_VVCT, 1, "Receive packet, and store it in the VVC. To be fetched later using fetch\_result() "); axistream\_receive (AXISTREAM\_VVCT, 1, "Receive packet, and send it to scoreboard for checking ");

Note! Use axistream\_receive\_bytes ( ) when using t\_byte\_array.

axistream\_expect (VVCT, vvc\_instance\_idx, exp\_data\_array, [exp\_user\_array, [exp\_strb\_array, exp\_id\_array, exp\_dest\_array]], msg, [alert\_level, [scope]])

Example: axistream\_expect(AXISTREAM\_VVCT, 0, v\_data\_array(0 to v\_numBytes-1), v\_user\_array(0 to v\_numWords-1), "Expect a packet, checking the tuser bits"); axistream\_expect(AXISTREAM\_VVCT, 0, v\_data\_array(0 to v\_numBytes-1)(15 downto 0), v\_user\_array(0 to v\_numWords-1), "Expecting a packet, checking the tuser bits");

Note! Use axistream\_expect\_bytes ( ) when using t\_byte\_array





### AXI4-Stream VVC Configuration record 'vvc\_config' -- accessible via shared\_axistream\_vvc\_config

Record element	Туре	C_AXISTREAM_VVC_CONFIG_DEFAULT
inter_bfm_delay	t_inter_bfm_delay	C_AXISTREAM_INTER_BFM_DELAY_DEFAULT
cmd_queue_count_max	natural	C_CMD_QUEUE_COUNT_MAX
cmd_queue_count_threshold	natural	C_CMD_QUEUE_COUNT_THRESHOLD
cmd_queue_count_threshold_severity	t_alert_level	C_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY
bfm_config	t_axistream_bfm_config	C_AXISTREAM_BFM_CONFIG_DEFAULT
msg_id_panel	t_msg_id_panel	C_VVC_MSG_ID_PANEL_DEFAULT

### AXI4-Stream VVC Status record signal 'vvc\_status' -- accessible via shared\_axistream\_vvc\_status

Record element	Type	
current_cmd_idx	natural	
previous_cmd_idx	natural	
pending_cmd_cnt	natural	

## Common VVC procedures applicable for this VVC

- See UVVM Methods QuickRef for details.

enable\_log\_msg()

disable\_log\_msg()

fetch\_result()

flush\_command\_queue()

await\_[any\_]completion()

terminate\_current\_command()

terminate\_all\_commands()

insert\_delay()

get\_last\_received\_cmd\_idx()

# VVC target parameters

Name	Туре	Example(s)	Description
VVCT	t_vvc_target_record	AXISTREAM_VVCT	VVC target type compiled into each VVC in order to differentiate between VVCs.
vvc_instance_idx	integer	0	Instance number of the VVC

# VVC functional parameters

Name	Туре	Example(s)	Description	
data_array	t_slv_array,	x"D0" & x"D1"	A SLV array, byte array or a single SLV containing the packet data to be sent or the data received.	
	std_logic_vector or	(x"D0D1", x"D2D3")	SLV and t_slv_array data has to be a multiple of byte(s), e.g. x"AA", x"BEEF".	
	t_byte_array	x"D0D1"	Note the name change when data_array is t_byte_array.	
user_array	t_user_array	x"1" & x"2"	Sideband data to send or has been received via the tuser signal.	
			t_user_array is defined in axistream_bfm_pkg. Refer to the AXI4-Stream BFM documentation	
strb_array	t_strb_array	x"1" & x"2"	Sideband data to send or has been received via the tstrb signal.	
			t_strb_array is defined in axistream_bfm_pkg. Refer to the AXI4-Stream BFM documentation	
id_array	t_id_array	x"1" & x"2"	Sideband data to send or has been received via the tid signal.	
			t_id_array is defined in axistream_bfm_pkg. Refer to the AXI4-Stream BFM documentation	
dest_array	t_dest_array	x"1" & x"2"	Sideband data to send or has been received via the tdest signal.	
			t_dest_array is defined in axistream_bfm_pkg. Refer to the AXI4-Stream BFM documentation	
msg	string	"Send data"	A custom message to be appended in the log/alert	
alert-level	t_alert_level	ERROR or TB_WARNING	Set the severity for the alert that may be asserted by the method.	
scope	string	"AXISTREAM VVC"	A string describing the scope from which the log/alert originates. In a simple single sequencer typically	
			"AXISTREAM BFM". In a verification component typically "AXISTREAM VVC ".	



# VVC entity signals

Name	Туре	Description
clk	std_logic	VVC Clock signal
axistream_vvc_master_if	t_axistream_if	See AXI4-Stream BFM documentation

# VVC entity generic constants

Name	Туре	Default	Description
GC_VVC_IS_MASTER	boolean	-	Set to true when this VVC instance is an AXI4 Stream master (data is output from BFM).
			Set to false when this VVC is an AXI4 Stream slave (data is input to BFM.)
GC_DATA_WIDTH	integer	-	Width of the AXI4-Stream data bus
GC_USER_WIDTH	integer	-	Width of the AXI4-Stream TUSER signal.
			Note 1: if TUSER is wider than 8, increase the value of the constant C_MAX_TUSER_BITS in
			axistream_bfm_pkg.
			Note 2: If the TUSER signal is not used, refer to description in Section 7
GC_ID_WIDTH	integer	-	Width of the AXI4-Stream TID signal.
			Note 1: if TID is wider than 8, increase the value of the constant C_MAX_TID_BITS in
			axistream_bfm_pkg.
			Note 2: If the TID signal is not used, refer to description in Section 7
GC_DEST_WIDTH	integer	-	Width of the AXI4-Stream TDEST signal.
			Note 1: if TDEST is wider than 4, increase the value of the constant C_MAX_TDEST_BITS in
			axistream_bfm_pkg.
			Note 2: If the TDEST signal is not used, refer to description in Section 7
GC_INSTANCE_IDX	natural	-	Instance number to assign the VVC
GC_AXISTREAM_CONFIG	t_axistream_bfm_config	C_AXISTREAM_BFM_	Configuration for the AXI4-Stream BFM, see AXI4-Stream BFM documentation.
		CONFIG_DEFAULT	
GC_CMD_QUEUE_COUNT_MAX	natural	1000	Absolute maximum number of commands in the VVC command queue
GC_CMD_QUEUE_COUNT_THRESHOLD	natural	950	An alert will be generated when reaching this threshold to indicate that the command queue is
			almost full. The queue will still accept new commands until it reaches
			GC_CMD_QUEUE_COUNT_MAX.
GC_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY	t_alert_level	WARNING	Alert severity which will be used when command queue reaches
			GC_CMD_QUEUE_COUNT_THRESHOLD.
GC_RESULT_QUEUE_COUNT_MAX	natural	1000	Maximum number of unfetched results before result_queue is full.
GC_RESULT_QUEUE_COUNT_THRESHOLD	natural	950	An alert with severity 'result_queue_count_threshold_severity' will be issued if result queue
			exceeds this count. Used for early warning if result queue is almost full. Will be ignored if set to 0.
GC_RESULT_QUEUE_COUNT_THRESHOLD_SEVERITY	t_alert_level	WARNING	Severity of alert to be initiated if exceeding result_queue_count_threshold



# **VVC** details

All VVC procedures are defined in vvc\_methods\_pkg (dedicated this VVC), and uvvm\_vvc\_framework.td\_vvc\_framework\_common\_methods\_pkg (common VVC procedures). It is also possible to send a multicast to all instances of a VVC with ALL\_INSTANCES as parameter for vvc\_instance\_idx.

Note the procedure name change when using t\_byte\_array.

Note: Every procedure here can be called without the optional parameters enclosed in [].

## 1 VVC procedure details

#### **Procedure**

### **Description**

axistream transmit()

axistream\_transmit (VVCT, vvc\_instance\_idx, data\_array, [user\_array, [strb\_array, id\_array, dest\_array]], msg, [scope])

The axistream\_transmit() VVC procedure adds a transmit command to the AXI4-Stream VVC executor queue, which will run as soon as all preceding commands have completed. When the command is scheduled to run, the executor calls the AXI4-Stream BFM axistream\_transmit() procedure, described in the AXI4-Stream BFM QuickRef.

The axistream\_transmit() procedure can only be called when the AXISTREAM VVC is instantiated in master mode, i.e. setting the generic constant 'GC MASTER MODE' to true.

#### Examples:

### axistream\_expect()

axistream\_expect (VVCT, vvc\_instance\_idx, exp\_data\_array, [exp\_user\_array, [exp\_strb\_array, exp\_id\_array, exp\_dest\_array]],
msg, [alert\_level, [scope]])

The axistream\_expect() VVC procedure adds an expect command to the AXI4-Stream VVC executor queue, which will run as soon as all preceding commands have completed. When the command is scheduled to run, the executor calls the AXI4-Stream BFM axistream\_expect() procedure, described in the AXI4-Stream BFM QuickRef.

The axistream\_expect() procedure can only be called when the AXISTREAM VVC is instantiated in slave mode, i.e. setting the generic constant 'GC\_MASTER\_MODE' to false.

#### Examples:

axistream expect (AXISTREAM VVCT, 0, v exp data array(0 to 1),



#### axistream receive()

#### axistream\_receive (VVCT, vvc\_instance\_idx, msg, [scope])

The axistream\_receive() VVC procedure adds a receive command to the AXISTREAM VVC executor queue, which will run as soon as all preceding commands have completed. When the receive command is scheduled to run, the executor calls the AXISTREAM BFM axistream\_receive() procedure, described in the AXISTREAM BFM QuickRef. The axistream\_reveice() procedure can only be called when the AXISTREAM VVC is instantiated in slave mode, i.e. setting the generic constant 'GC\_MASTER\_MODE' to false.

The value receive from DUT will not be returned in this procedure call since it is non-blocking for the sequencer/caller, but the received data and metadata will be stored in the VVC for a potential future fetch (see example with *fetch\_result* below).

Note that the stored received data is t\_byte\_array.

#### Example:

```
axistream receive (AXISTREAM VVCT, 1, "Receive data to VVC", C SCOPE);
```

#### Example with fetch\_result() call: Result is placed in v result

```
variable v_cmd_idx : natural; -- Command index for the last receive
variable v_result : work.vvc_cmd_pkg.t_vvc_result; -- Result from receive (data and metadata)
(...)
axistream_receive(AXISTREAM_VVCT, 1, "Receive data to VVC");
v_cmd_idx := get_last_received_cmd_idx(AXISTREAM_VVCT, 1);
await_completion(AXISTREAM_VVCT, 1, 1 ms, "Wait for receive to finish");
fetch_result(AXISTREAM_VVCT, 1, v_cmd_idx, v_result, "Fetching result from receive operation");
```



### 2 VVC Instantiation

In order to select between the master and slave modes, the VVC must be instantiated using the correct value of the generic constant GC\_VVC\_IS\_MASTER in the testbench or test-harness. Example instantiations of the VVC in both operation supplied for ease of reference.

Mode	Instatiation	Mode	Instatiation
Master	i_axistream_vvc_master: <b>entity</b> work.axistream_vvc	Slave	i_axistream_vvc_slave : entity work.axistream_vvc
	generic map(		generic map(
	<pre>GC_VVC_IS_MASTER =&gt; true,</pre>		<pre>GC_VVC_IS_MASTER =&gt; false,</pre>
	GC_DATA_WIDTH => GC_DATA_WIDTH,		GC_DATA_WIDTH => GC_DATA_WIDTH,
	GC_USER_WIDTH => GC_USER_WIDTH,		GC_USER_WIDTH => GC_USER_WIDTH,
	GC_ID_WIDTH => GC_ID_WIDTH,		GC_ID_WIDTH => GC_ID_WIDTH,
	GC_DEST_WIDTH => GC_DEST_WIDTH,		GC_DEST_WIDTH => GC_DEST_WIDTH,
	GC_INSTANCE_IDX => 2)		GC_INSTANCE_IDX => 3)
	port map(		port map(
	clk => clk,		clk => clk,
	<pre>axistream vvc if =&gt; axistream if);</pre>		<pre>axistream vvc if =&gt; axistream if);</pre>

# 3 VVC Configuration

Record element	Туре	C_AXISTREAM_BFM_CONFIG_DEFAULT	Description
inter_bfm_delay	t_inter_bfm_delay	C_AXISTREAM_INTER_BFM_DELAY_DEFAULT	Delay between any requested BFM accesses towards the DUT.
			- TIME_START2START: Time from a BFM start to the next BFM start
			(A TB_WARNING will be issued if access
			takes longer than TIME_START2START).
			<ul> <li>TIME_FINISH2START: Time from a BFM end to the next BFM start.</li> </ul>
			Any insert_delay() command will add to the above minimum delays, giving for instance
			the ability to skew the BFM starting time.
cmd_queue_count_max	natural	C_CMD_QUEUE_COUNT_MAX	Maximum pending number in command queue before queue is full. Adding additional
			commands will result in an ERROR.
cmd_queue_count_threshold	natural	C_CMD_QUEUE_COUNT_THRESHOLD	An alert with severity "cmd_queue_count_threshold_severity" will be issued if command
			queue exceeds this count. Used for early warning if command queue is almost full. Will
			be ignored if set to 0.
cmd_queue_count_threshold_severity	t_alert_level	C_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY	Severity of alert to be initiated if exceeding cmd_queue_count_threshold
result_queue_count_max	natural	C_RESULT_QUEUE_COUNT_MAX	Maximum number of unfetched results before result_queue is full.
result _queue_count_threshold	natural	C_RESULT_QUEUE_COUNT_THRESHOLD	An alert with severity 'result_queue_count_threshold_severity' will be issued if result
			queue exceeds this count. Used for early warning if result queue is almost full. Will be
			ignored if set to 0.
result _queue_count_threshold_severity	t_alert_level	C_RESULT_QUEUE_COUNT_THRESHOLD_SEVERITY	Severity of alert to be initiated if exceeding result_queue_count_threshold
bfm_config	t_axistream_bfm_config	C_AXISTREAM_BFM_CONFIG_DEFAULT	Configuration for AXI4-Stream BFM. See quick reference for AXI4-Stream BFM
msg_id_panel	t_msg_id_panel	C_VVC_MSG_ID_PANEL_DEFAULT	VVC dedicated message ID panel. See section 16 of
			uvvm_vvc_framework/doc/UVVM_VVC_Framework_Essential_Mechanisms.pdf for how
			to use verbosity control.

Note: cmd/result queue parameters in the VVC Configuration are unused and will be removed in v3.0, use instead the entity generic constants.

The configuration record can be accessed from the Central Testbench Sequencer through the shared variable array, e.g.:

```
\verb|shared_axistream_vvc_config(1).inter_bfm_delay.delay_in_time := 50 ns; \\
```

shared axistream vvc config(1).bfm config.clock period := 10 ns;

### 4 VVC Status

The current status of the VVC can be retrieved during simulation. This is achieved by reading from the shared variable shared\_axistream\_vvc\_status record from the test sequencer. The record contents can be seen below:

Record element	Туре	Description
current_cmd_idx	natural	Command index currently running
previous_cmd_idx	natural	Previous command index to run
pending cmd cnt	natural	Pending number of commands in the command gueue

## 5 Activity watchdog

The VVCs support a centralized VVC activity register which the activity watchdog uses to monitor the VVC activities. The VVCs will register their presence to the VVC activity register at start-up, and report when ACTIVE and INACTIVE, using dedicated VVC activity register methods, and trigger the global\_trigger\_vvc\_activity\_register signal during simulations. The activity watchdog is continuously monitoring the VVC activity register for VVC inactivity and raises an alert if no VVC activity is registered within the specified timeout period.

Include activity\_watchdog(num\_exp\_vvc, timeout, [alert\_level, [msg]]) in the testbench to start using the activity watchdog. Note that setting the exact number of expected VVCs in the VVC activity register can be omitted by setting num\_exp\_vvc = 0.

More information can be found in UVVM Essential Mechanisms PDF in the UVVM VVC Framework doc folder.

# 6 Transaction Info

This VVC supports transaction info, a UVVM concept for distributing transaction information in a controlled manner within the complete testbench environment. The transaction info may be used in many different ways, but the main purpose is to share information directly from the VVC to a DUT model.

See UVVM VVC Framework Essential Mechanisms PDF, section 6, for additional information.

Table 6.1 AXI4-Stream base transaction (BT) record fields. Transaction type: t\_base\_transaction (BT) - accessible via shared axistream vvc transaction info.bt.

Info field	Туре	Default	Description
operation	t_operation	NO_OPERATION	Current VVC operation, e.g. INSERT_DELAY, POLL_UNTIL, READ, WRITE.
data_array	t_slv_array(0 to 16*1024)	(others => (others => '0'))	Packet data to be sent or received.
user_array	t_user_array(0 to 16*1024)	(others => (others => '0'))	Sideband data to send or which has been received via the tuser signal.
strb_array	t_strb_array(0 to 16*1024)	(others => (others => '0'))	Sideband data to send or which has been received via the tstrb signal.
id_array	t_id_array(0 to 16*1024)	(others => (others => '0'))	Sideband data to send or which has been received via the tid signal.
dest_array	t_dest_array(0 to 16*1024)	(others => (others => '0'))	Sideband data to send or which has been received via the tdest signal.
vvc_meta	t_vvc_meta	C_VVC_META_DEFAULT	VVC meta data of the executing VVC command.
→ msg	string	u u	Message of executing VVC command.
→ cmd_idx	integer	-1	Command index of executing VVC command.
transaction_status	t_transaction_status	C_TRANSACTION_STATUS_DEFAULT	Set to INACTIVE, IN_PROGRESS, FAILED or SUCCEEDED during a transaction.



Refer to the the VVC Functional Parameters table in page 3 for more details regarding the VVC specific Transaction Info record fields. See UVVM VVC Framework Essential Mechanisms PDF, section 6, for additional information about transaction types and transaction info usage.

### 7 VVC Interface

In this VVC, the interface has been encapsulated in a signal record of type t\_axistream\_if in order to improve readability of the code. Since the AXI4-Stream interface buses can be of arbitrary size, the interface std\_logic\_vectors have been left unconstrained. These unconstrained SLVs needs to be constrained when the interface signals are instantiated. For this interface, they could look like:

```
signal axistream_if: t_axistream_if(tdata(C_DATA_WIDTH -1 downto 0), tkeep((C_DATA_WIDTH/8)-1 downto 0), tuser(C_USER_WIDTH -1 downto 0), tstrb((C_DATA_WIDTH/8)-1 downto 0), tid(C_ID_WIDTH-1 downto 0), tdest(C_DEST_WIDTH-1 downto 0)
```

The widths of *tuser*, *tstrb*, *tid* and *tdest* are declared even when not used or connected to DUT. Set the widths of unused signals to 1, for example C USER WIDTH = 1.

### 8 Additional Documentation

Additional documentation about UVVM and its features can be found under "/uvvm\_vvc\_framework/doc/".

For additional documentation on the AXI4-Stream standard, refer to "AMBA 4 AXI4-Stream Protocol Specification (ARM IHI 0051)", available from ARM.



## 9 Compilation

AXI4-Stream VVC must be compiled with VHDL 2008. It is dependent on the following libraries

- UVVM Utility Library (UVVM-Util), version 2.14.0 and up
- UVVM VVC Framework, version 2.10.0 and up
- AXI4-Stream BFM

Before compiling the AXI4-Stream VVC, assure that uvvm\_vvc\_framework and uvvm\_util have been compiled.

See UVVM Essential Mechanisms located in uvvm\_vvc\_framework/doc for information about compile scripts.

### Compile order for the AXI4-Stream VVC:

Compile Graer for the 7th of t		
Compile to library	File	Comment
bitvis_vip_axistream	axistream_bfm_pkg.vhd	AXI4-Stream BFM
bitvis_vip_axistream	transaction_pkg.vhd	AXI4-Stream transaction package with DTT types, constants etc.
bitvis_vip_axistream	vvc_cmd_pkg.vhd	AXI4-Stream VVC command types and operations
bitvis_vip_axistream	/uvvm_vvc_framework/src_target_dependent/td_target_support_pkg.vhd	UVVM VVC target support package, compiled into the AXI4-Stream VVC
		library.
bitvis_vip_axistream	/uvvm_vvc_framework/src_target_dependent/td_vvc_framework_common_methods_pkg.vhd	UVVM framework common methods compiled into the AXI4-Stream VVC
		library
bitvis_vip_axistream	vvc_methods_pkg.vhd	AXI4-Stream VVC methods
bitvis_vip_axistream	/uvvm_vvc_framework/src_target_dependent/td_queue_pkg.vhd	UVVM queue package for the VVC
bitvis_vip_axistream	/uvvm_vvc_framework/src_target_dependent/td_vvc_entity_support_pkg.vhd	UVVM VVC entity support compiled into the AXI4-Stream VVC library
bitvis_vip_axistream	axistream_vvc.vhd	AXI4-Stream VVC

# 10 Simulator compatibility and setup

See README.md for a list of supported simulators.

For required simulator setup see UVVM-Util Quick reference.

#### **IMPORTANT**

This is a simplified Verification IP (VIP) for AXI4-Stream. The given VIP complies with the basic AXI4-Stream protocol and thus allows a normal access towards an AXI4-Stream interface. This VIP is not AXI4-Stream protocol checker. For a more advanced VIP please contact Bitvis AS at <a href="mailto:support@bitvis.no">support@bitvis.no</a>



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