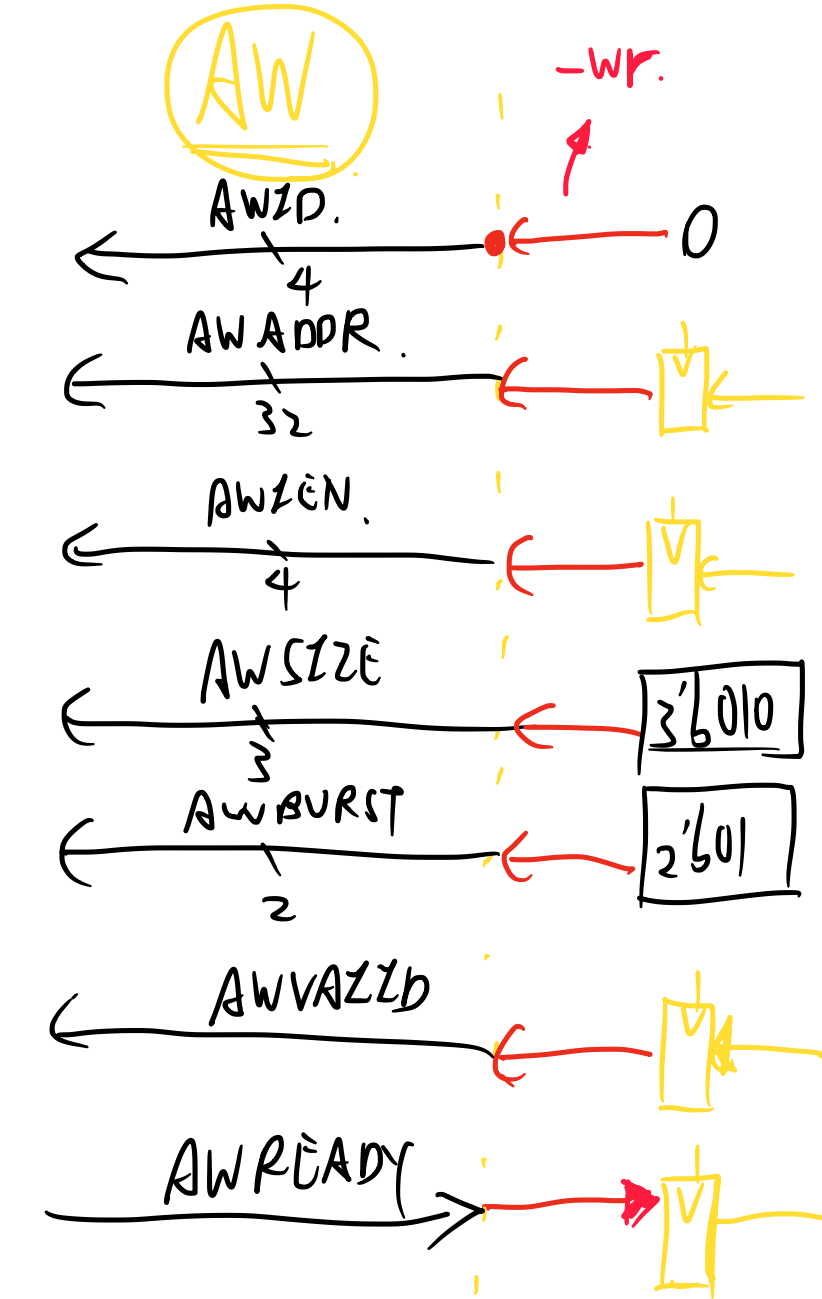
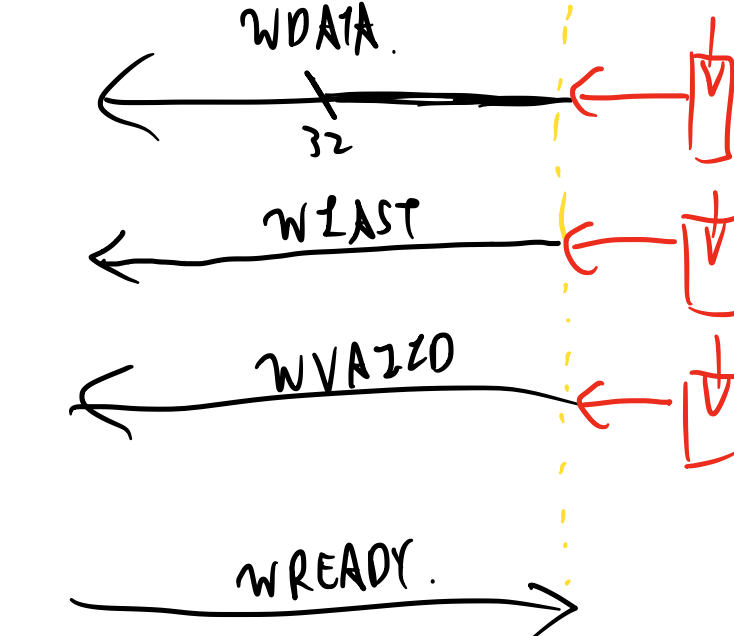


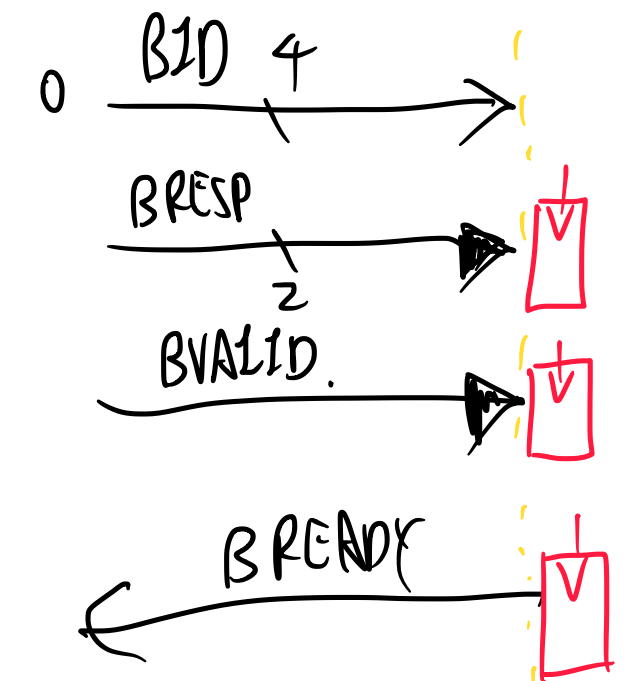
Write channel



W

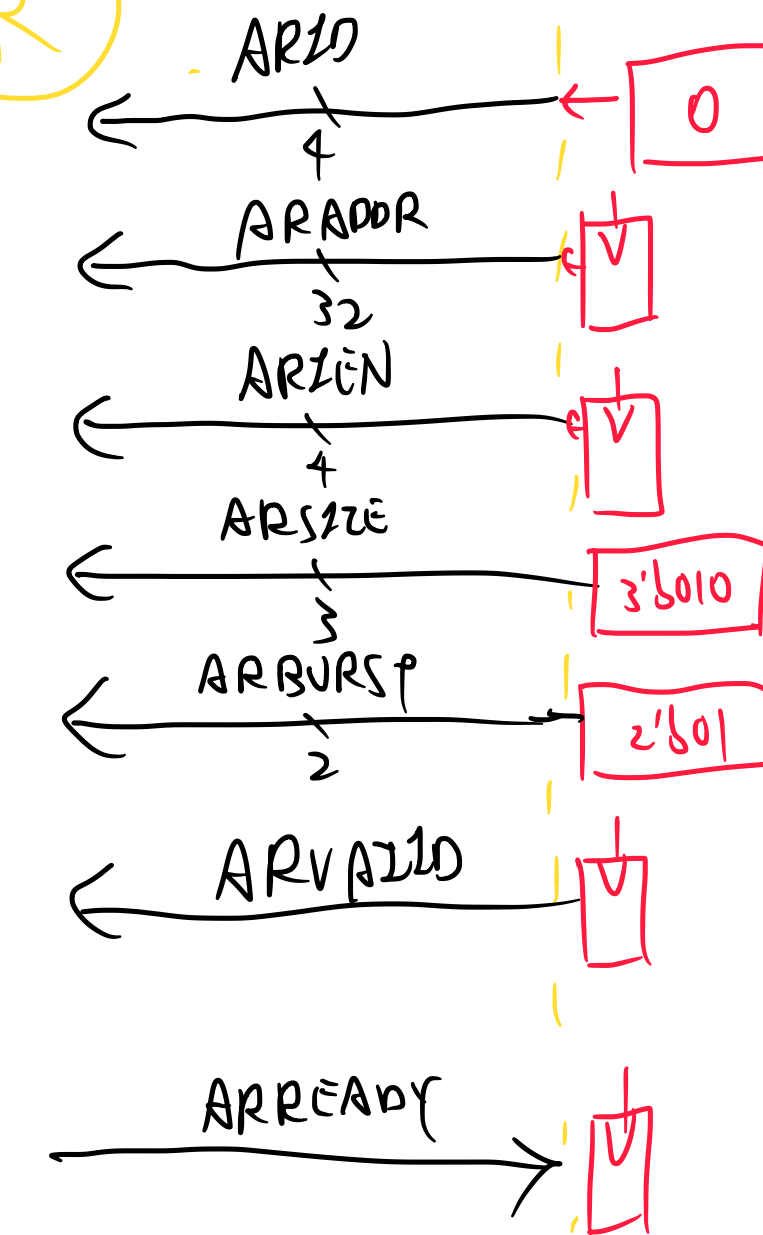


B

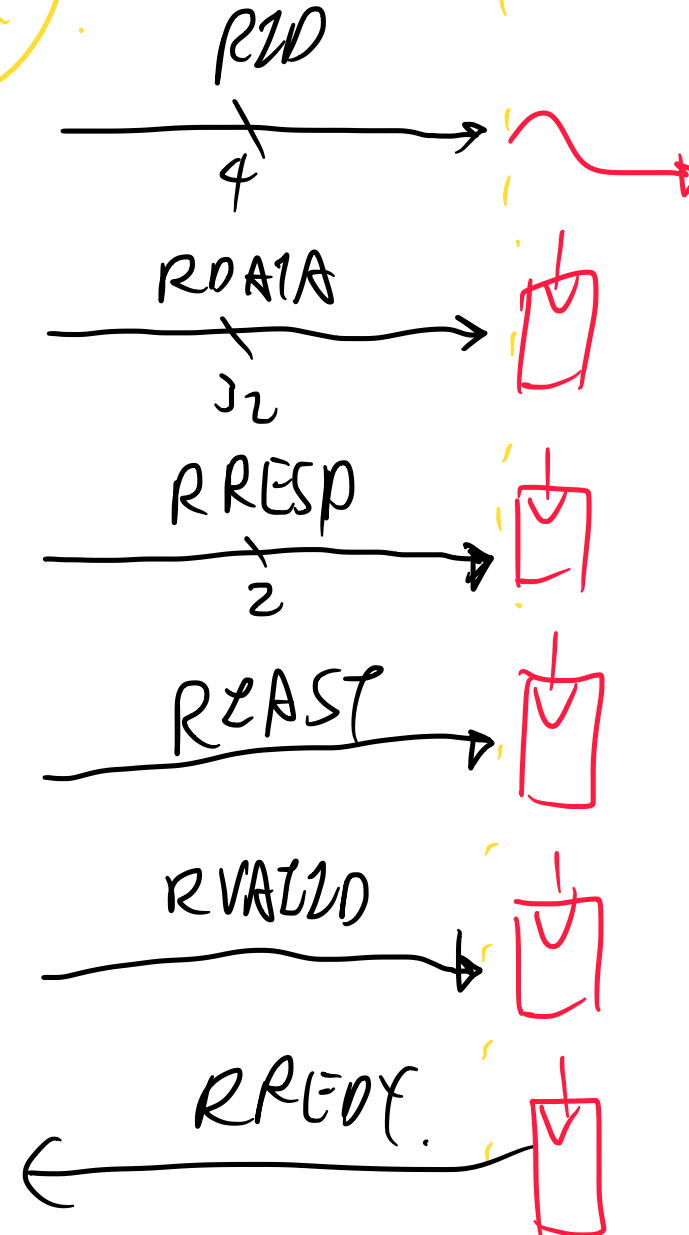


RD_channel

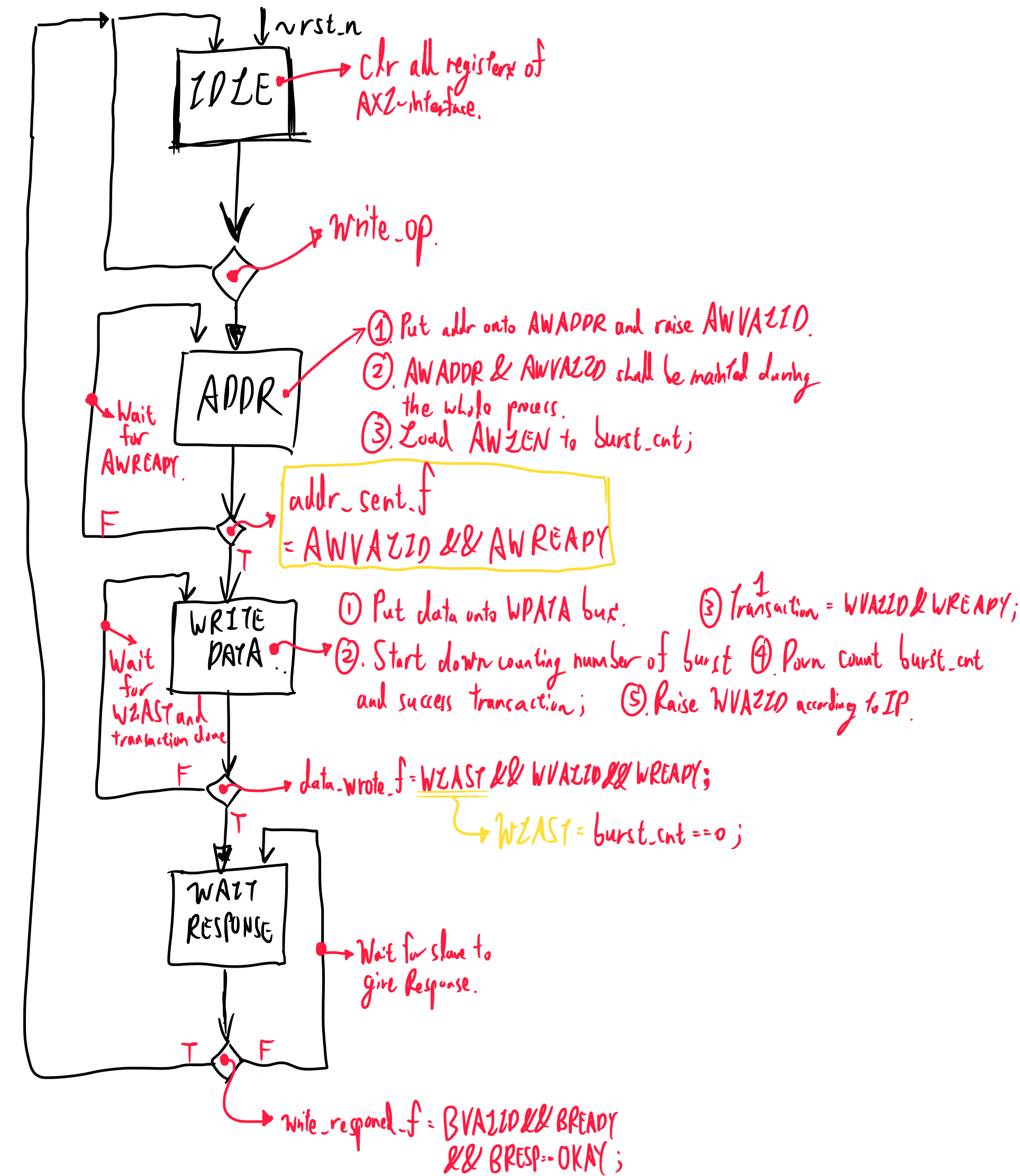
AR



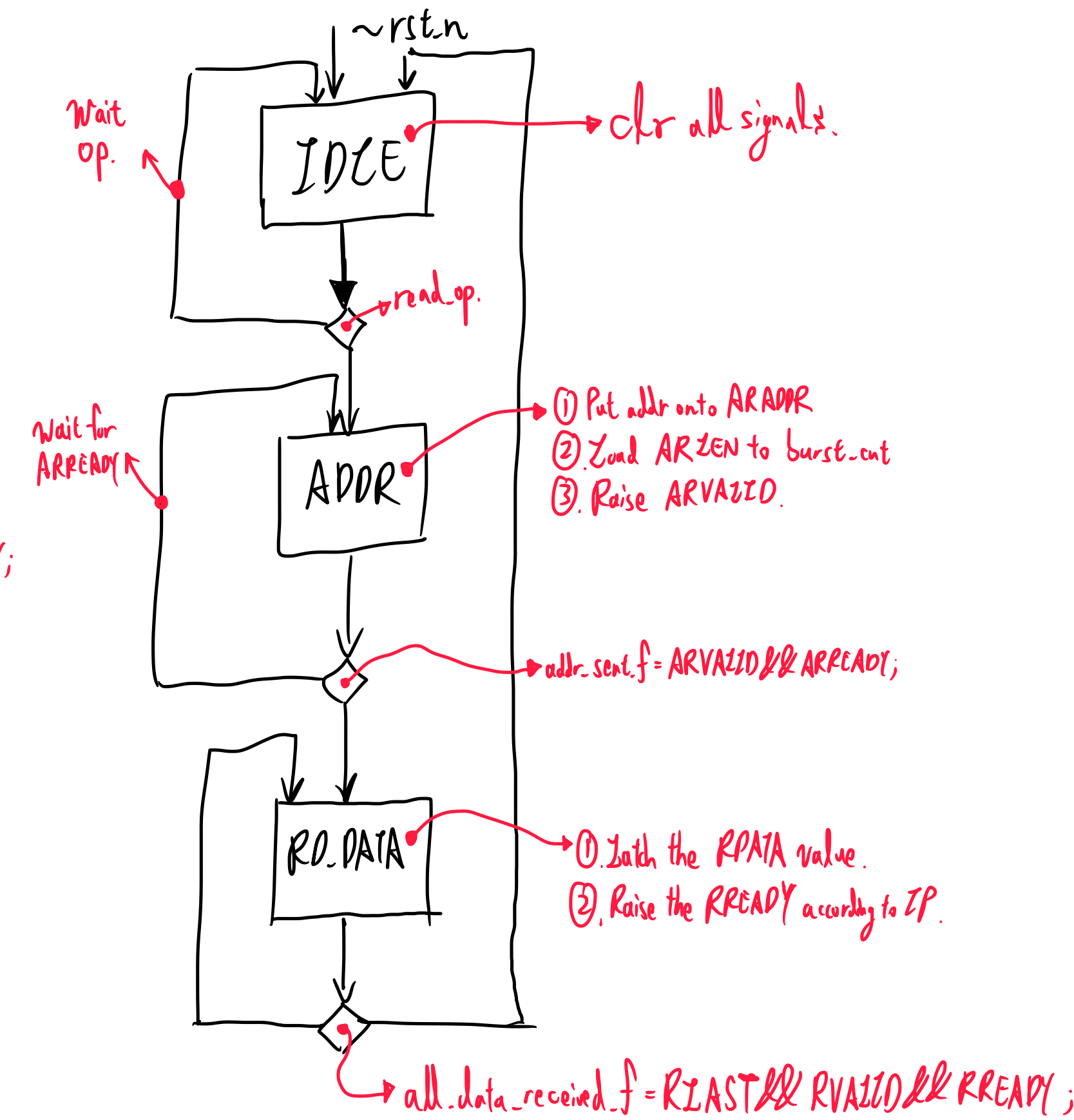
R



Write transaction.



Read transaction



*Use register for all axi signals.