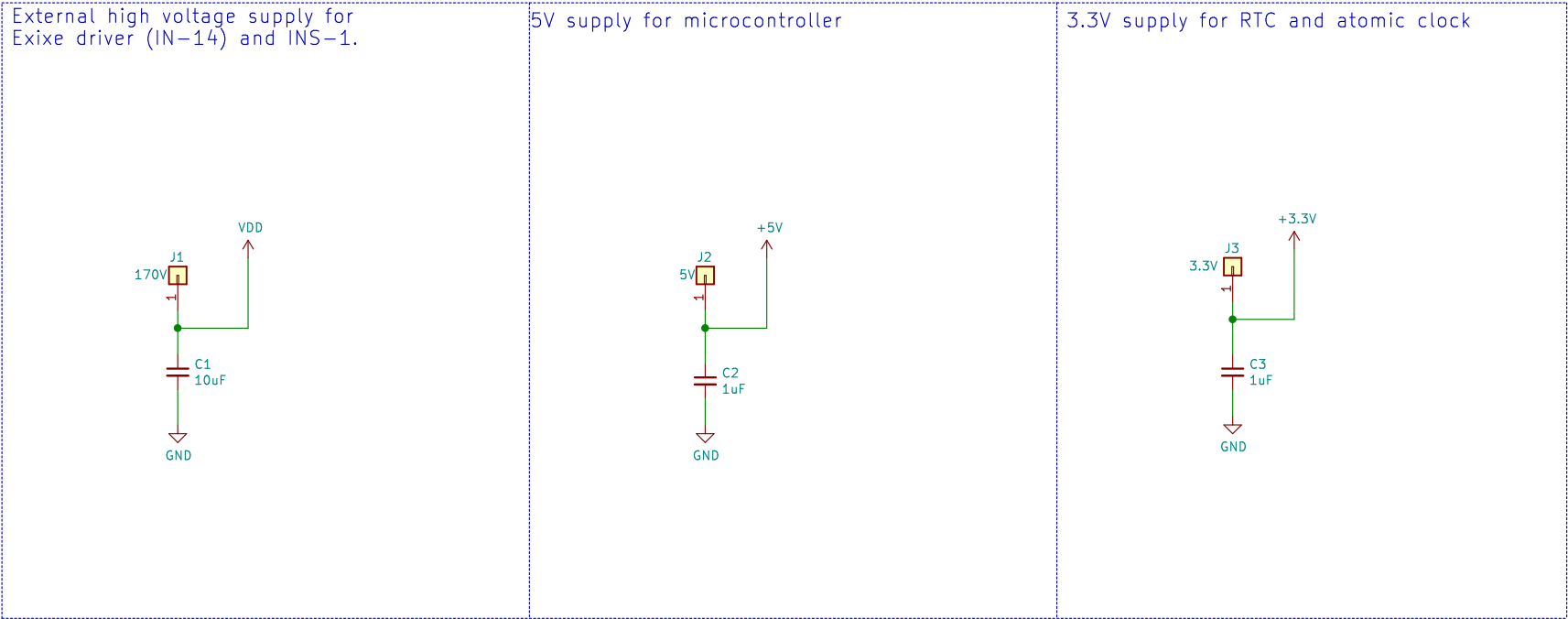


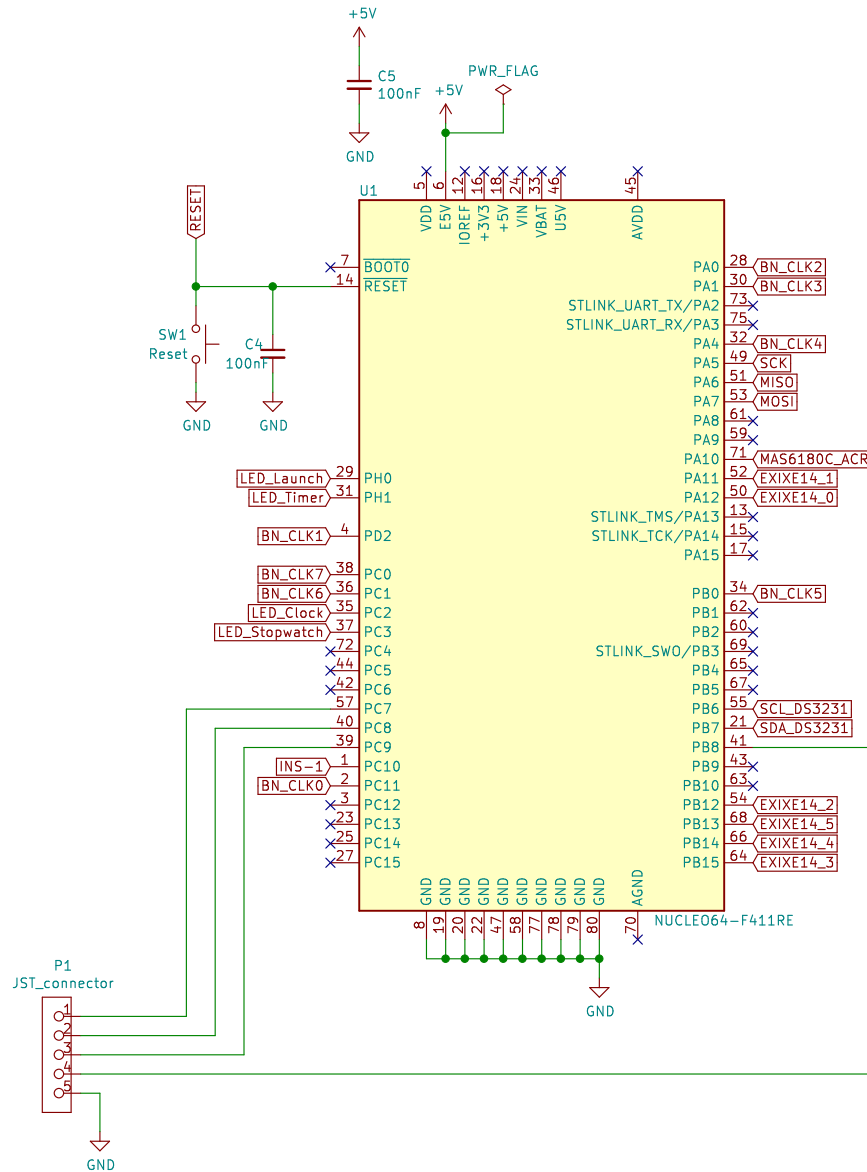
	1	2	3	4	5	6																																																
A	<div>Sheet: Power</div> <div>File: Power.sch</div>		<div>Sheet: Indicators_BinaryClock</div> <div>File: Indicators_BinaryClock.sch</div>																																																			
B	<div>Sheet: CPU_Time</div> <div>File: CPU_Time.sch</div>																																																					
C	<div>Sheet: Exixe14</div> <div>File: Exixe14.sch</div>																																																					
D	<table><tr><td colspan="2">Author: Pundeep Hundal</td><td colspan="3">Co-author: Sichun Xu</td></tr><tr><td colspan="5">This schematic was designed with the assistance of Steven Knudsen (ECE dept., Faculty of Engineering, University of Alberta).</td></tr><tr><td colspan="5">License: CC BY 4.0 (https://creativecommons.org/licenses/by/4.0/)</td></tr><tr><td colspan="5">University of Alberta (AlbertaSat)</td></tr><tr><td colspan="5">Sheet: /</td></tr><tr><td colspan="5">File: Electrical.sch</td></tr><tr><td colspan="5">Title: Nixie Tube Clock Host Board</td></tr><tr><td colspan="2">Size: A4</td><td colspan="3">Date: 2021-04-20</td><td>Rev: v01.1</td></tr><tr><td colspan="2">KiCad E.D.A.</td><td colspan="3">eeschema (5.1.9)-1</td><td>Id: 1/5</td></tr></table>					Author: Pundeep Hundal		Co-author: Sichun Xu			This schematic was designed with the assistance of Steven Knudsen (ECE dept., Faculty of Engineering, University of Alberta).					License: CC BY 4.0 (https://creativecommons.org/licenses/by/4.0/)					University of Alberta (AlbertaSat)					Sheet: /					File: Electrical.sch					Title: Nixie Tube Clock Host Board					Size: A4		Date: 2021-04-20			Rev: v01.1	KiCad E.D.A.		eeschema (5.1.9)-1			Id: 1/5		
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All three power inputs are derived from High Voltage Power Supply Unit.

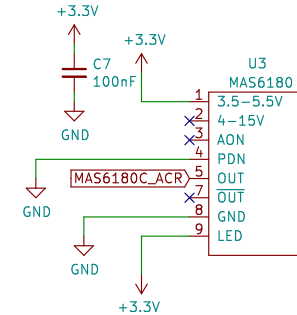


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Sheet: /Power/			
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Title: Nixie Tube Clock Host Board			
Size: A4		Date: 2021-04-20	Rev: v01.1
KiCad E.D.A. eeschema (5.1.9)-1			Id: 2/5

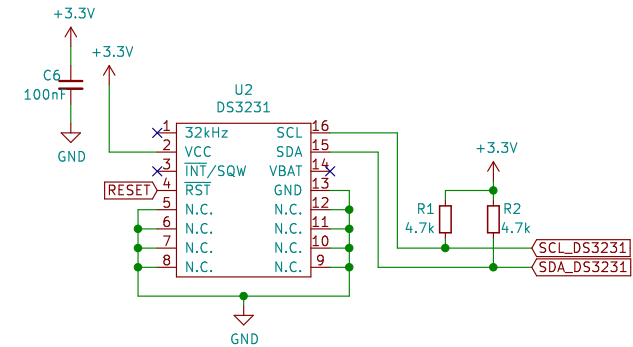
STM64-F401RE Nucleo Board



MAS6180 AM-Receiver with Antenna (or WWVB atomic clock receiver)



DS3231 RTC (with 32k EEPROM)



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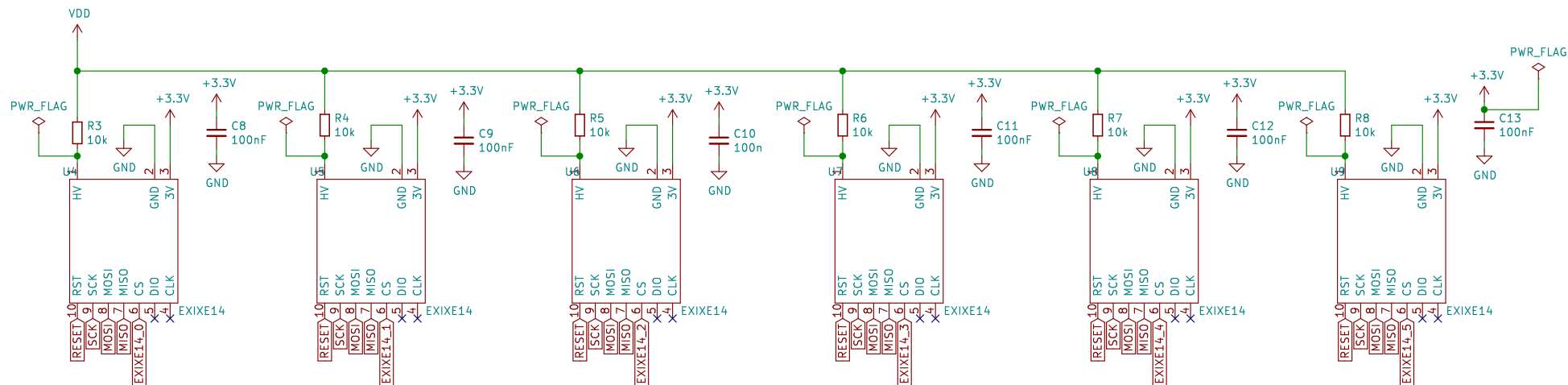
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 File: CPU_Time.sch

Title: Nixie Tube Clock Host Board

Size: A4 Date: 2021-04-20
 KiCad E.D.A. eeschema (5.1.9)-1

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 Id: 3/5

Exixe-14 drivers for IN-14 tubes



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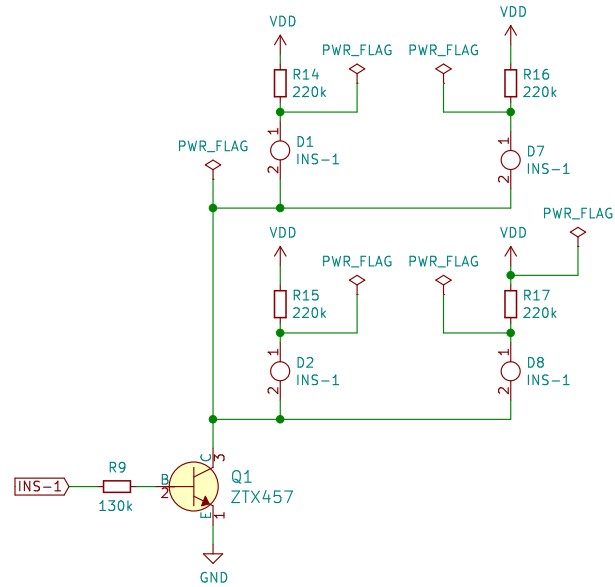
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Title: Nixie Tube Clock Host Board

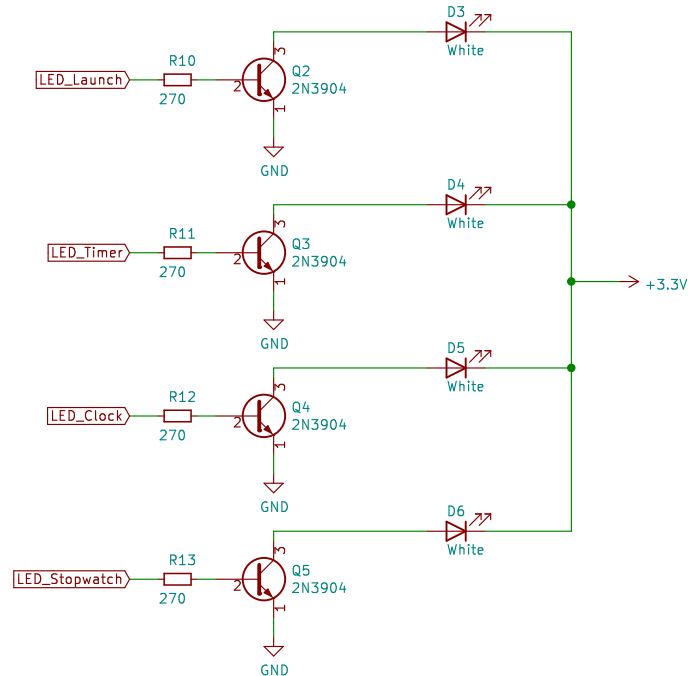
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 KiCad E.D.A. eeschema (5.1.9)-1

Rev: v01.1
 Id: 4/5

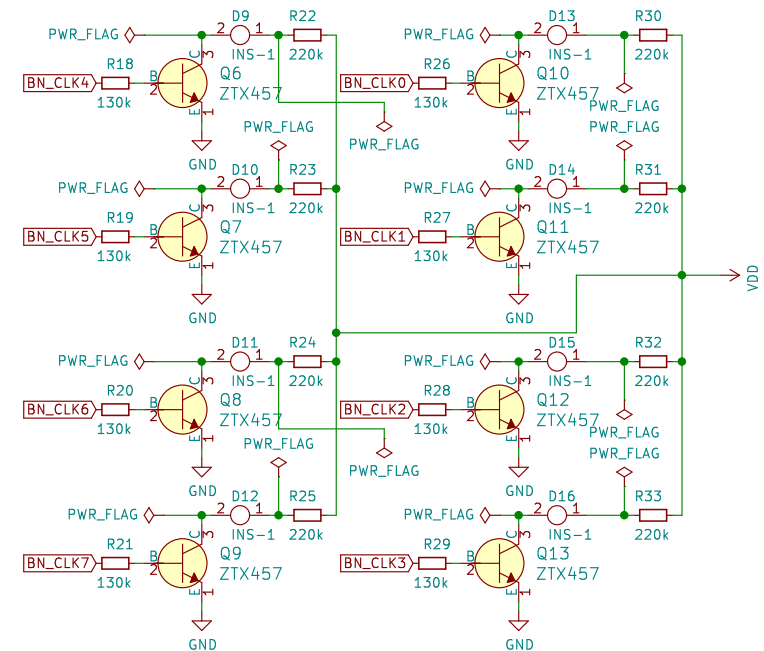
INS-1 Colon Indicators



Panel Mounting LEDs (Model: 5586403027F)



INS-1 Binary Clock



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Sheet: /Indicators_BinaryClock/
File: Indicators_BinaryClock.sch

Title: Nixie Tube Clock Host Board

Size: A4 Date: 2021-04-20

KiCad E.D.A. eeschema (5.1.9)-1

Rev: v01.1

Id: 5/5