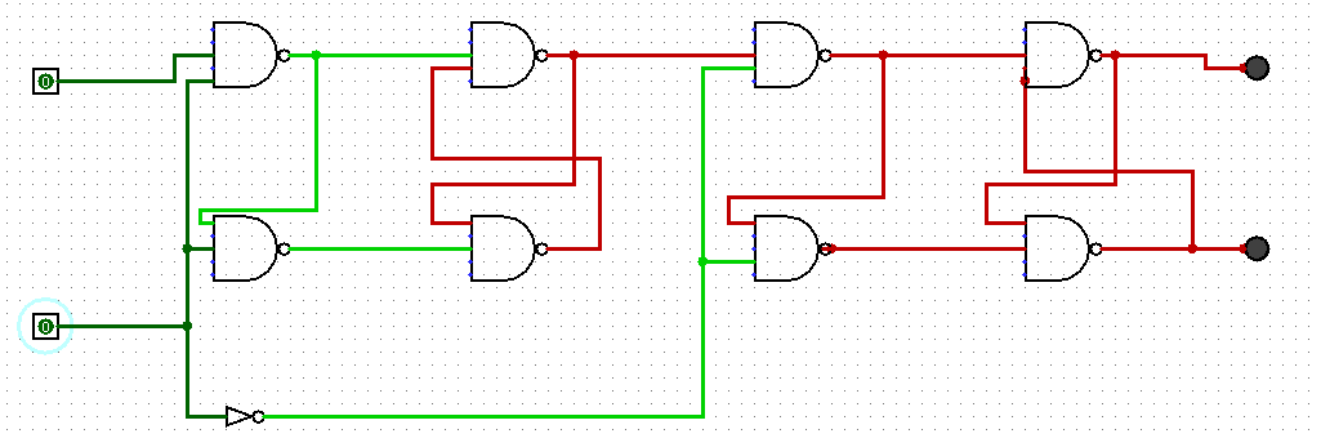CS1026

Lab #6

9/3/2017

Jakub Slowinski

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Part 1:

Negatively edge triggered D Flip flops.

Part 2:

Present-State/Next-State (PS/NS) Table for a JK Flip Flop

J	K	Q	Q'
0	0	Q	Q'
0	1	J	K
1	0	J	K
1	1	Q'	Q

Part 3:

Algorithmic State Machine (ASM) for a JK Flip Flop