

Sid Agrawal

SOFTWARE ENGINEER · GRADUATE STUDENT

Vancouver, Canada

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Summary

Graduate Student pursuing operating systems research. Prior experience as a software engineer in kernel development, and DevOps roles.

Education

University of British Columbia

PHD. IN COMPUTER SCIENCE

- Operating Systems Research at the Systopia Lab

Vancouver, BC, Canada

Jan. 2021 - present

University of Florida

MS. IN ELECTRICAL AND COMPUTER ENGINEERING

Gainesville, Florida, USA

Aug. 2010 - Dec. 2011

BITS(Birla Institute of Technology and Science) Pilani

B.E. IN ELECTRICAL AND ELECTRONICS ENGINEERING

Goa, India

Aug. 2005 - Aug. 2009

Continuing Education

Stanford

NON-DEGREE COURSEWORK

- Operating Systems undergraduate course (CS140)

Stanford, CA, USA

Jan. 2014 - Apr. 2014

University of California Santa Cruz, Extention Campus

NON-DEGREE COURSE WORKS

- Two courses on Linux and device driver programming

Santa Clara, CA, USA

Jan. 2012 - May. 2013

Research Experience

ARM

INTERN, RESEARCH - SYSTEMS SECURITY

- Combining seL4 and CHERI/Morello capabilities

Vancouver, BC, Canada

May 2022 - Aug 2022

Systopia Lab (Advisor Prof. Margo Seltzer)

RESEARCH ASSISTANT

- Investigating the common building blocks for different isolation mechanisms like processes, containers, virtual machines. Implement a mechanism that lets the user select the desired level of isolation for a given resource without depending on the mechanisms.

Vancouver, BC, Canada

Jan 2021 - present

Work Experience

Arista Networks

SOFTWARE ENGINEER

- Co-developed services to store build artifacts generated in the build process
- Co-Developed a glue layer in GoLang for a NoSQL backend for a distributed build system
- Independently developed services to detect and automatically triage faulty testbeds

Vancouver, BC, Canada

Sep. 2016 - Dec. 2020

Panzuara

SOFTWARE ENGINEER

- Independently designed and implemented support to transactionally update file metadata for Panzura's Global Distributed File System. This heavily simplified recovery after crashes.

Campbell, CA, USA

Apr. 2015 - Aug. 2016

Oracle(Solaris Group)

KERNEL ENGINEER

- Enhanced the virtual memory predictor in Solaris by developing an algorithm to determine which segments in the address space can be upgraded to large pages
- Improved performance of multiple system calls by making their $O()$ page size independent

Santa Clara, CA, USA

Apr. 2013 - Apr. 2015

- Developed C and assembly level kernels to stress test cache interconnects and database co-processor of the SPARC microprocessor

Honors & Awards

2021 **Tuition Award**, President's Academic Excellence Initiative Ph.D. Award

*University of British
Columbia*

2021 **Tuition Award**, Faculty of Science Ph.D. Tuition Award

*University of British
Columbia*

2010 **Tuition Award**, Achievement Award, College of Engineering

University of Florida

2006 **Tuition Award**, Need-based merit scholarship, BITS Pilani, India. 2005-2009

BITS Pilani