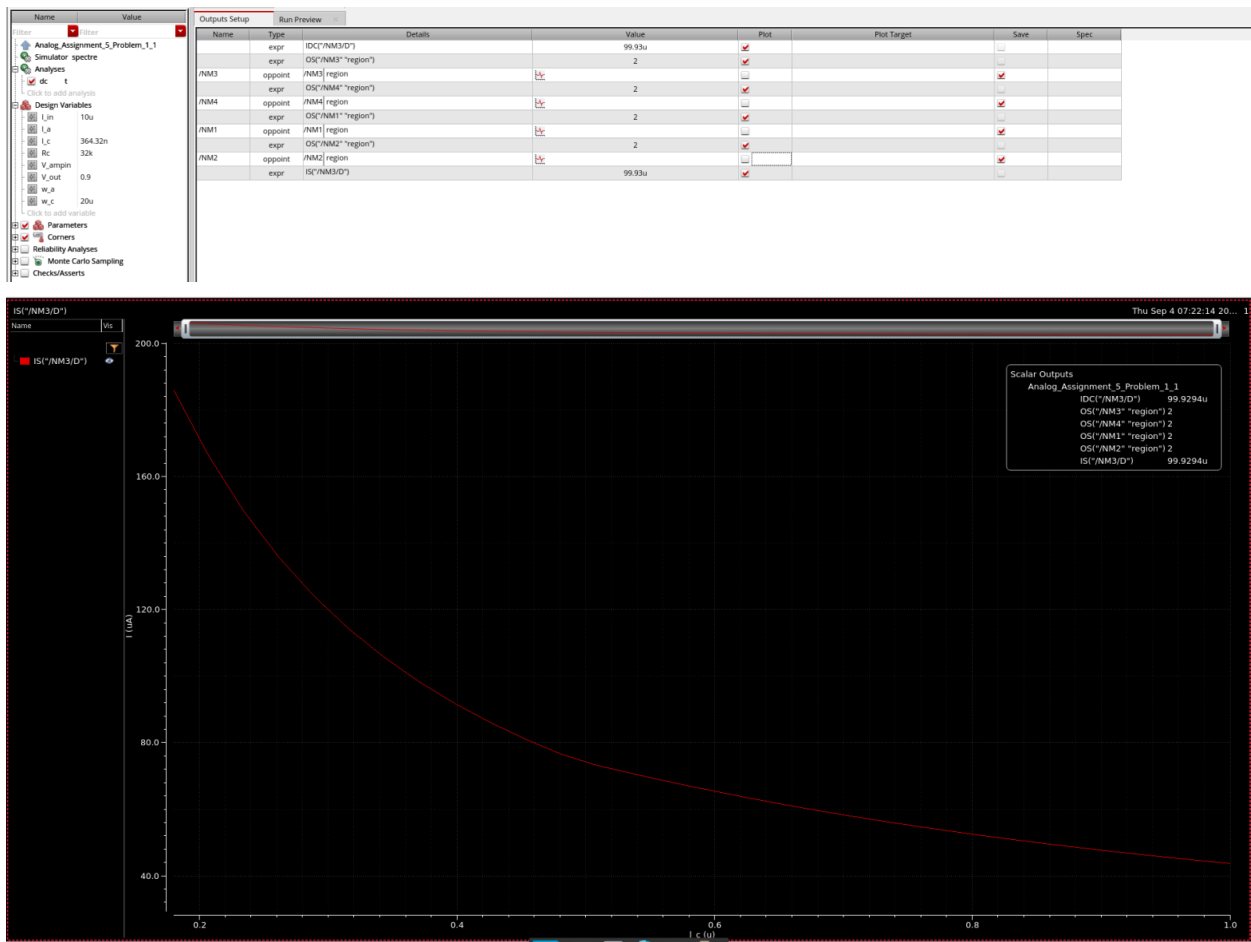
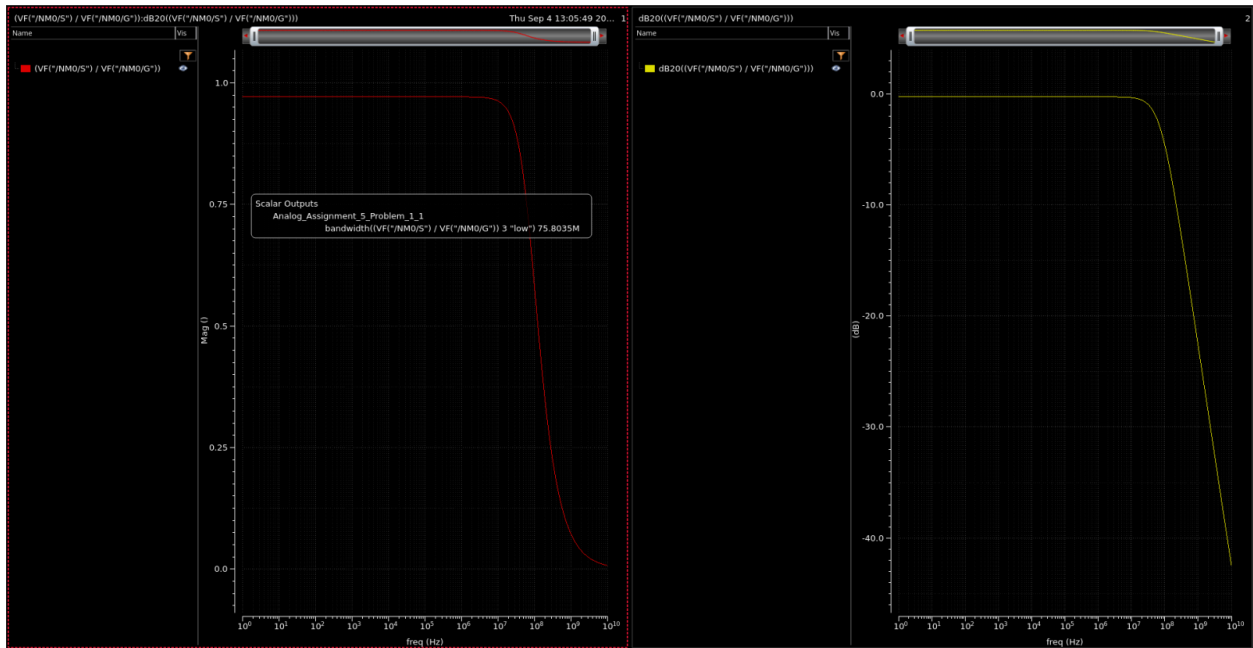


Analog Assignment-5

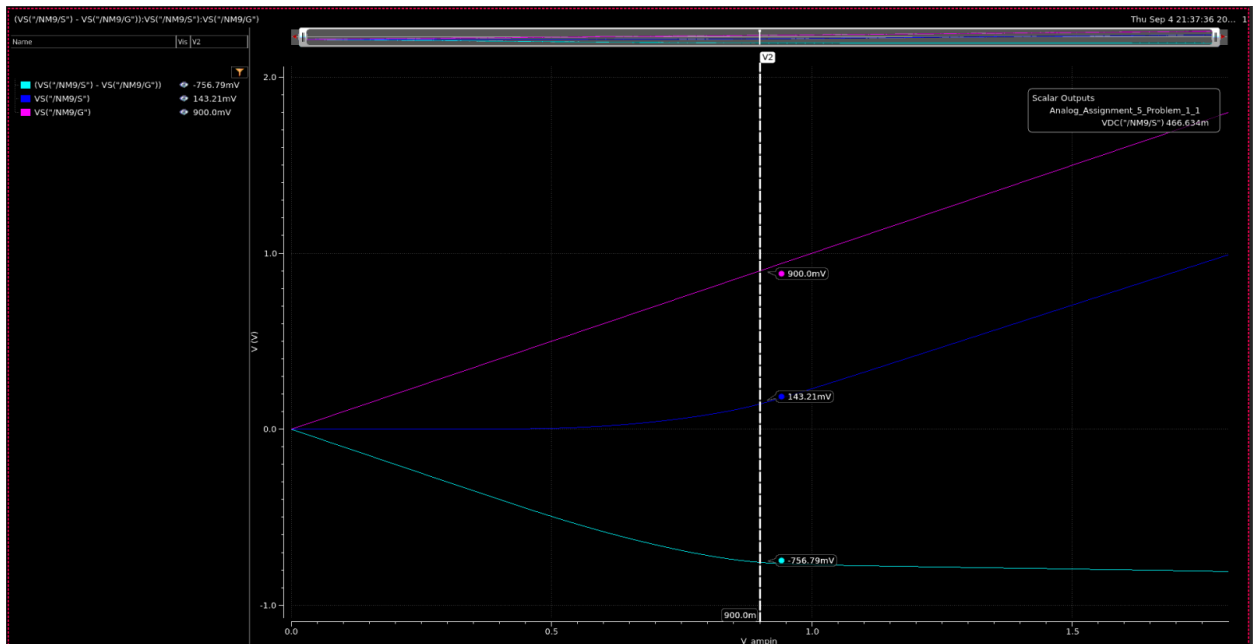
1.



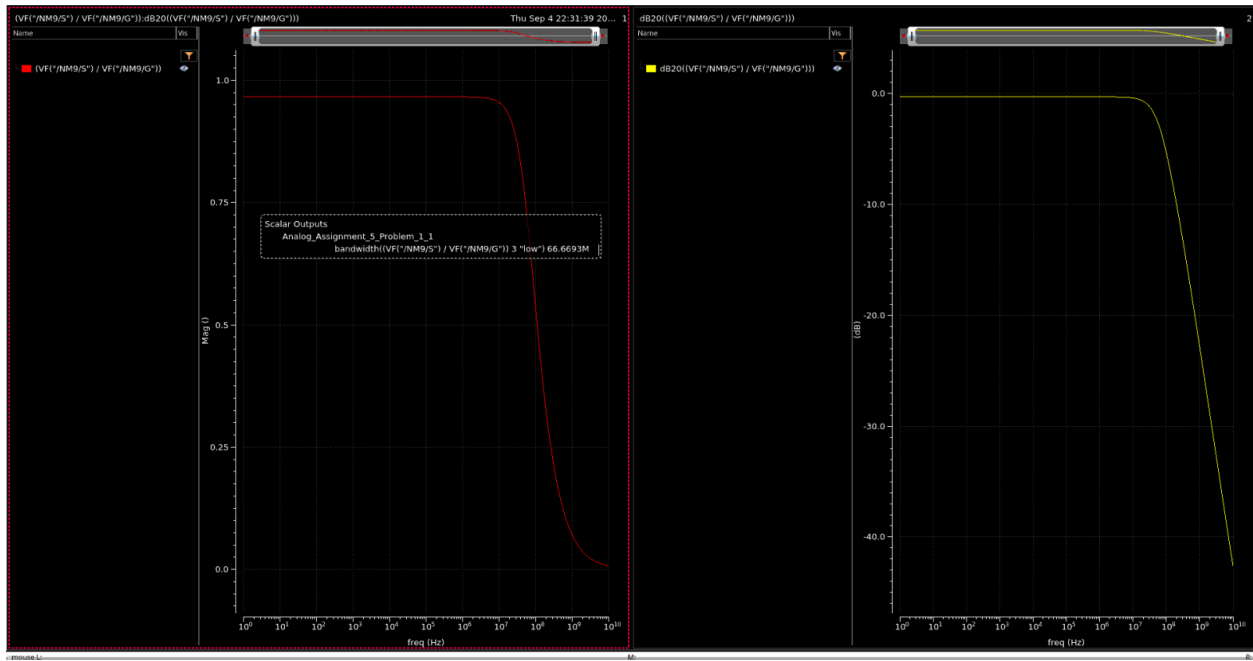


Name	Value
Filter	Other
Analogue_Assignment_5_Problem_1_1	
Simulator: spectre	
Analyses	
ac	1 10G 30 Logarithmic Points Per Decade
dc	t
Click to add analysis	
Design Variables	
Lin	100u
La	180m
Lc	
Rc	
V_amin	1.25
V_out	
w_a	1u
w_c	
Click to add variable	
Parameters	
Comers	
Reliability Analysis	
Monte Carlo Sampling	
Checks/Asserts	

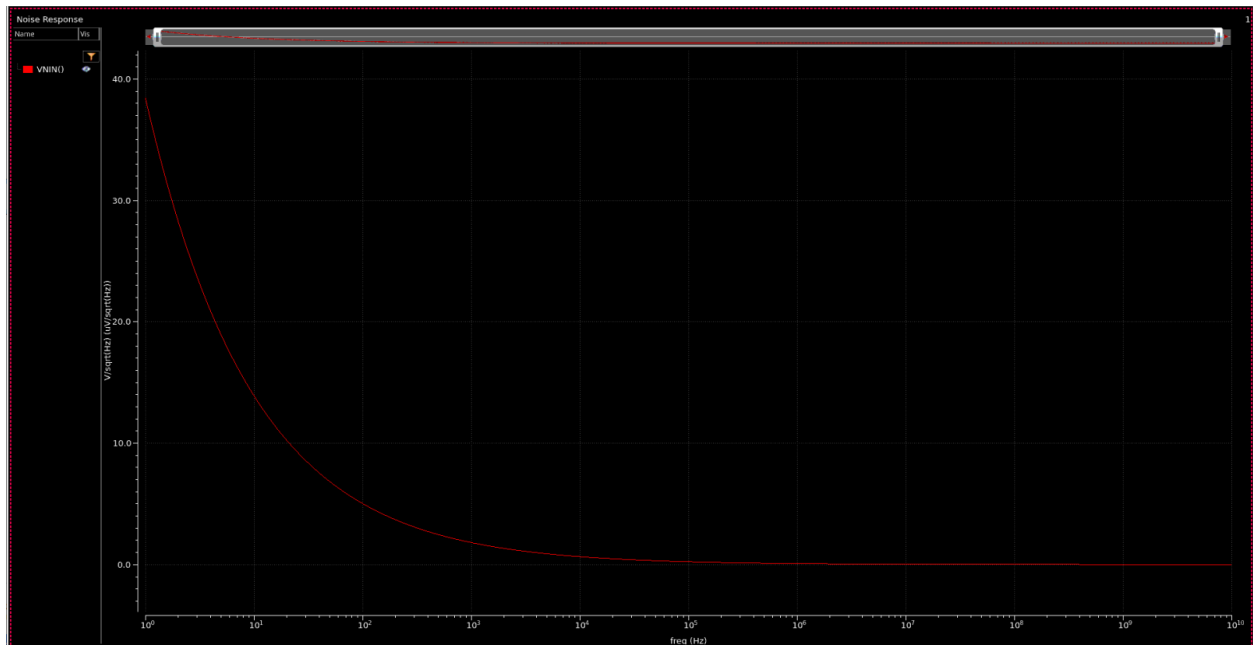
Name	Type	Details	Value	Plot	Plot Target	Save	Spec
VDC1(NM0/S)	expr	$(V_F^{(*)}/NM0/S^{(*)}) / V_F^{(*)}/NM0/G^{(*)}$	1.466	<input checked="" type="checkbox"/>		<input type="checkbox"/>	
dB20((V_F^{(*)}/NM0/S^{(*)}) / V_F^{(*)}/NM0/G^{(*)})	expr	$\text{dB20}((V_F^{(*)}/NM0/S^{(*)}) / V_F^{(*)}/NM0/G^{(*)})$	1.466	<input checked="" type="checkbox"/>		<input type="checkbox"/>	
bandwidth((V_F^{(*)}/NM0/S^{(*)}) / V_F^{(*)}/NM0/G^{(*)}) 3 "low"	expr	$\text{bandwidth}((V_F^{(*)}/NM0/S^{(*)}) / V_F^{(*)}/NM0/G^{(*)}) 3 \text{ "low"}$	75.8M	<input checked="" type="checkbox"/>		<input type="checkbox"/>	



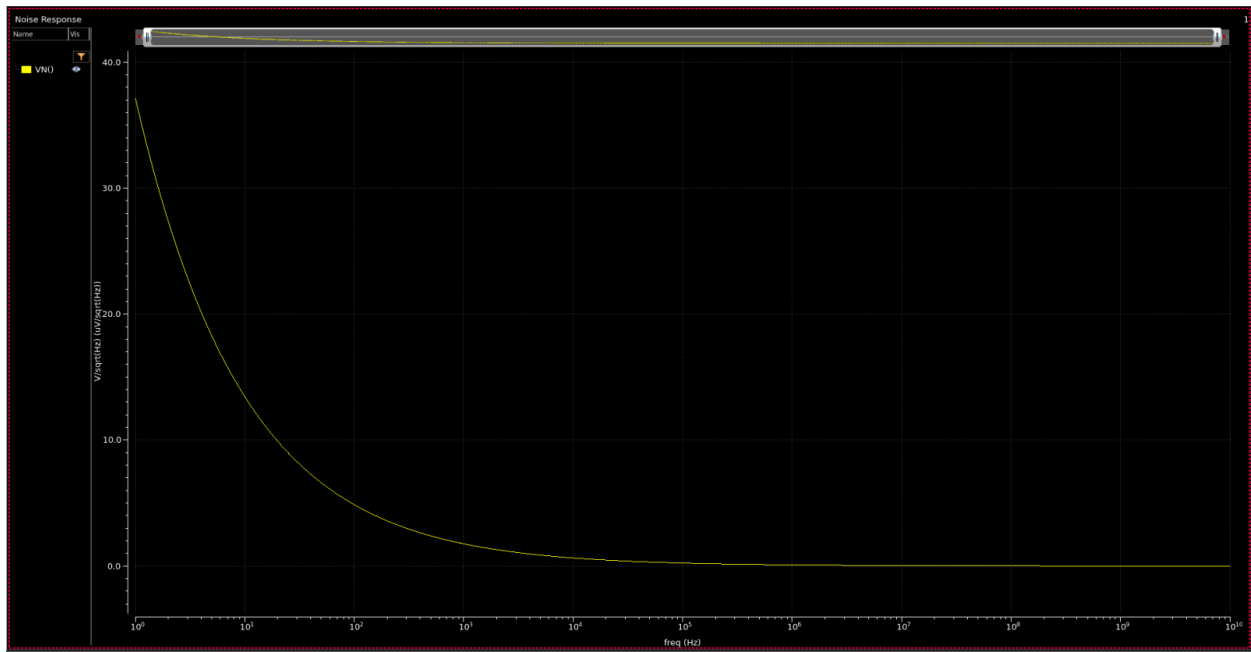
C.



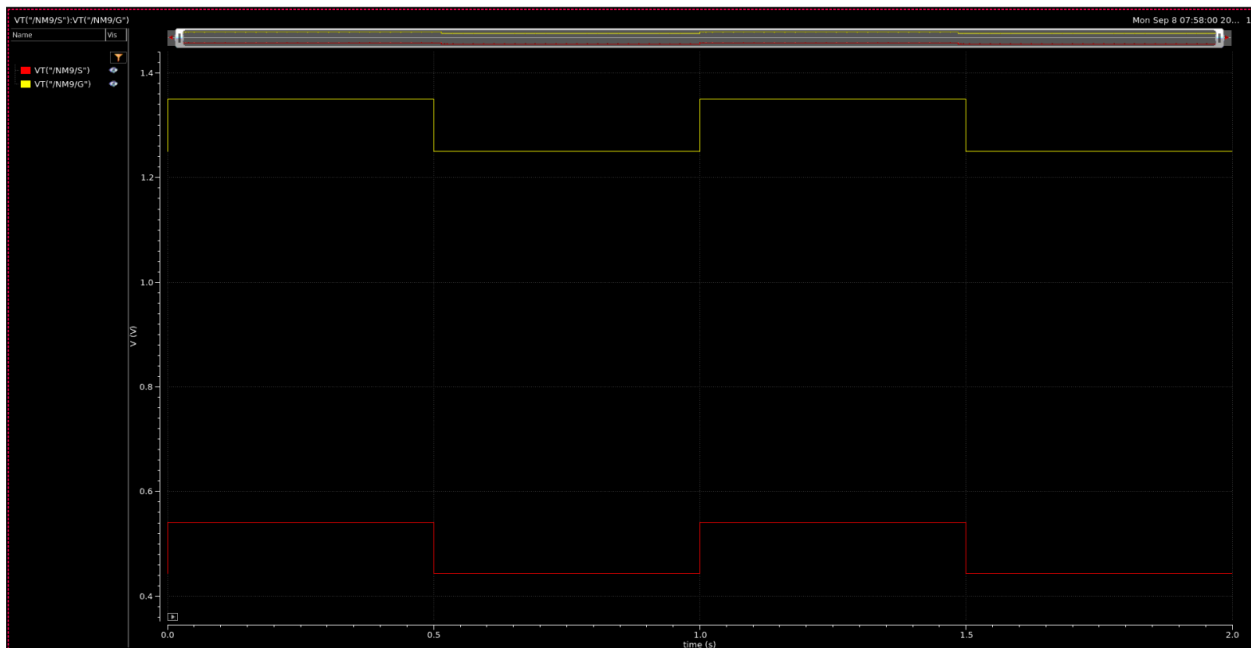
d. Input referred voltage noise.



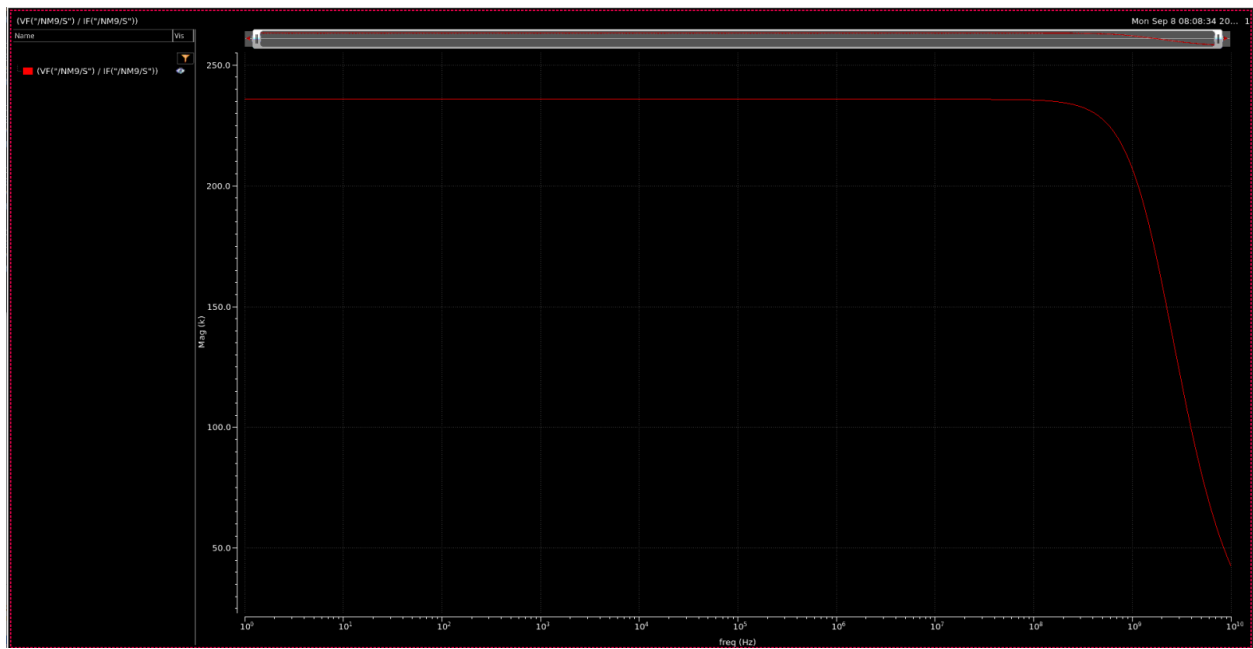
Output noise



e.



f.



Problem_2

Current mirror design with nmos in saturation

The screenshot shows the LTSpice simulation environment. The left sidebar contains the 'Design Variables' list, and the right pane shows the 'Outputs Setup' table.

Design Variables:

- dc: 1
- L_{in}: 10u
- L_c: 4.416u
- R_c: 32k
- R_{d1}: 1
- R_{d2}: 1
- V_{ampin}: 0.9
- V_{out}: 0.9
- w_a: 15u
- w_c: 15u

Outputs Setup Table:

Name	Type	Details	Value	Plot	Plot Target	Save	Spec
	expr	VDC(/NM3/D)					
	expr	IDC(/NM3/D)					
	expr	OSC(/NM12"/region")	2				
/NM12	oppoint	/NM12"/region					
	expr	OSC(/NM4"/region")	2				
/NM4	oppoint	/NM4"/region					
	expr	OSC(/NM5"/region")	2				
/NM5	oppoint	/NM5"/region					
	expr	OSC(/NM3"/region")	2				
/NM3	oppoint	/NM3"/region					
	expr	IDC(/NM3/D)	9.994u				

Top circuit design parameters with nmos in saturation

The screenshot shows the LTSpice simulation environment. The left sidebar contains the 'Design Variables' list, and the right pane shows the 'Outputs Setup' table.

Design Variables:

- dc: 1
- L_{in}: 10u
- L_c: 400n
- R_c: 159.15K
- R_{d1}: 159.15K
- R_{d2}: 159.15K
- V_{ampin}: 1.25
- V_{out}: 2u
- w_a: 2u
- w_c: 2u

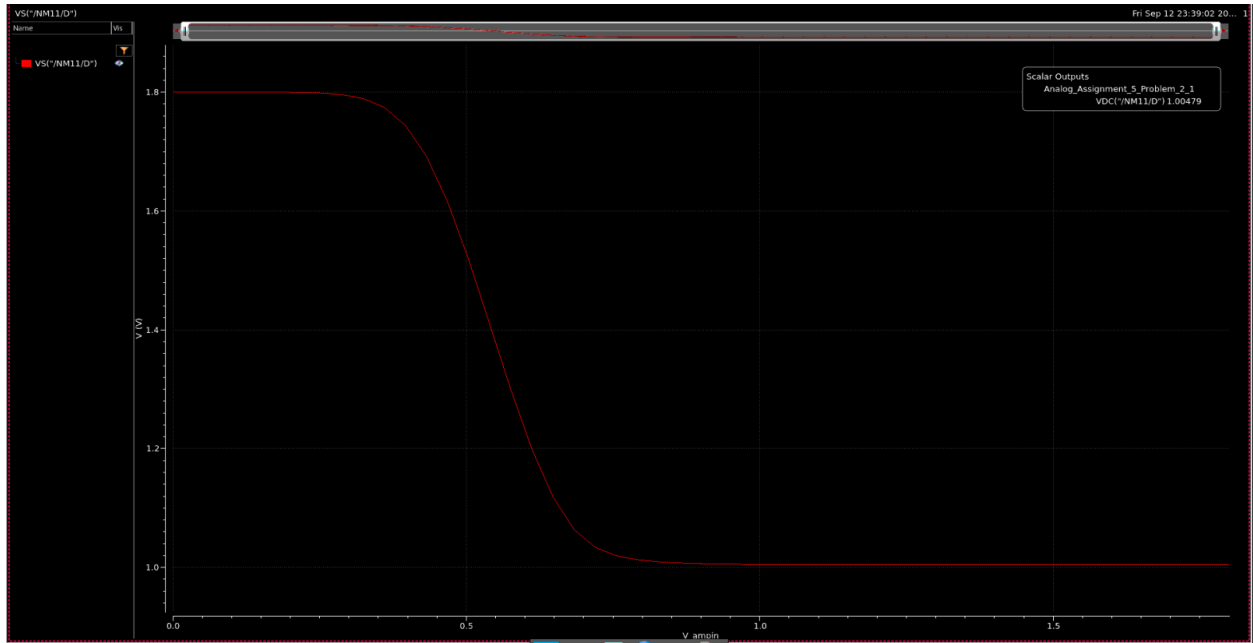
Outputs Setup Table:

Name	Type	Details	Value	Plot	Plot Target	Save	Spec
	expr	VDC(/NM1/D)	1.004				
	expr	OSC(/NM1"/region")	2				
/NM1	oppoint	/NM1"/region					
	expr	OSC(/NM0"/region")	2				
/NM0	oppoint	/NM0"/region					

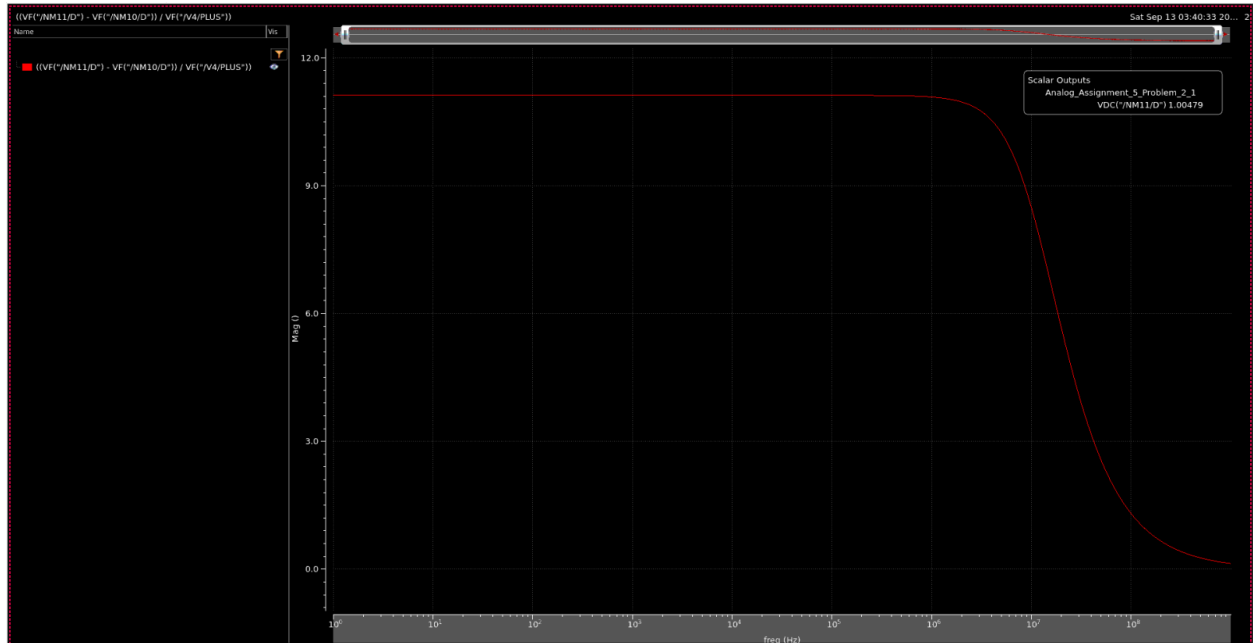
b.

The **input common-mode range (ICMR)** is the range of **DC input voltage (V_{dc})** for which an amplifier (like a MOS differential amplifier) operates properly and maintains input-output linearity.

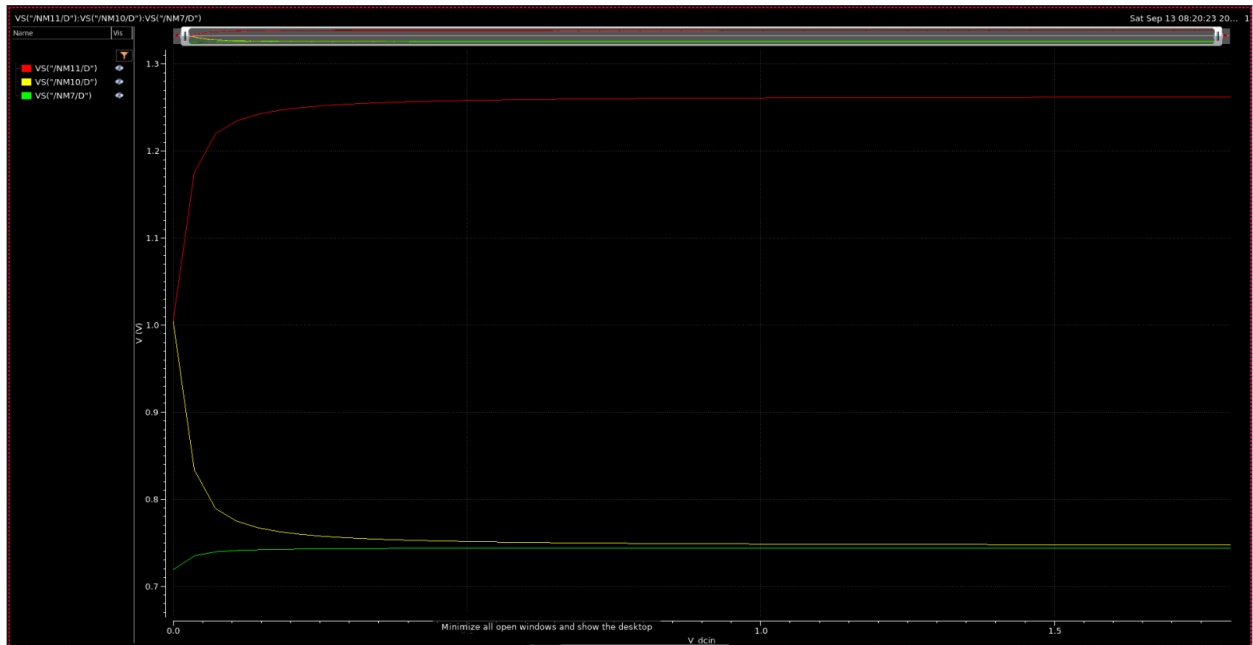
The range of V_{dc} for which amplifier operates in input-output linearity is 380 mV to 680 mV.



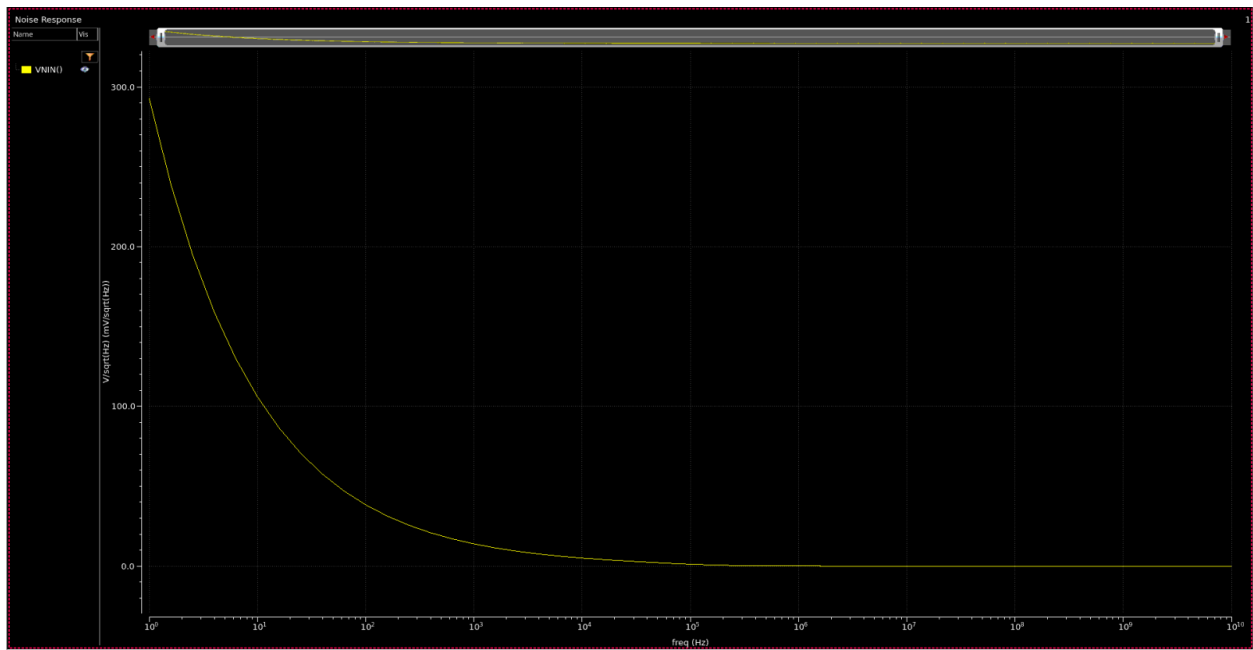
c.

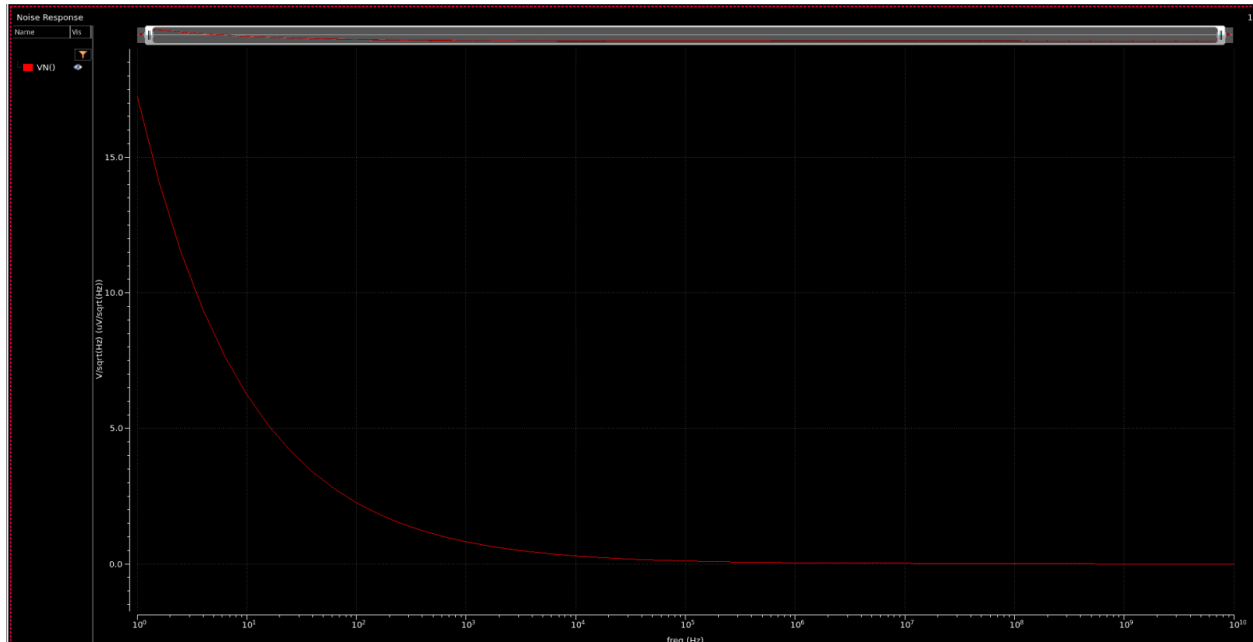


d.

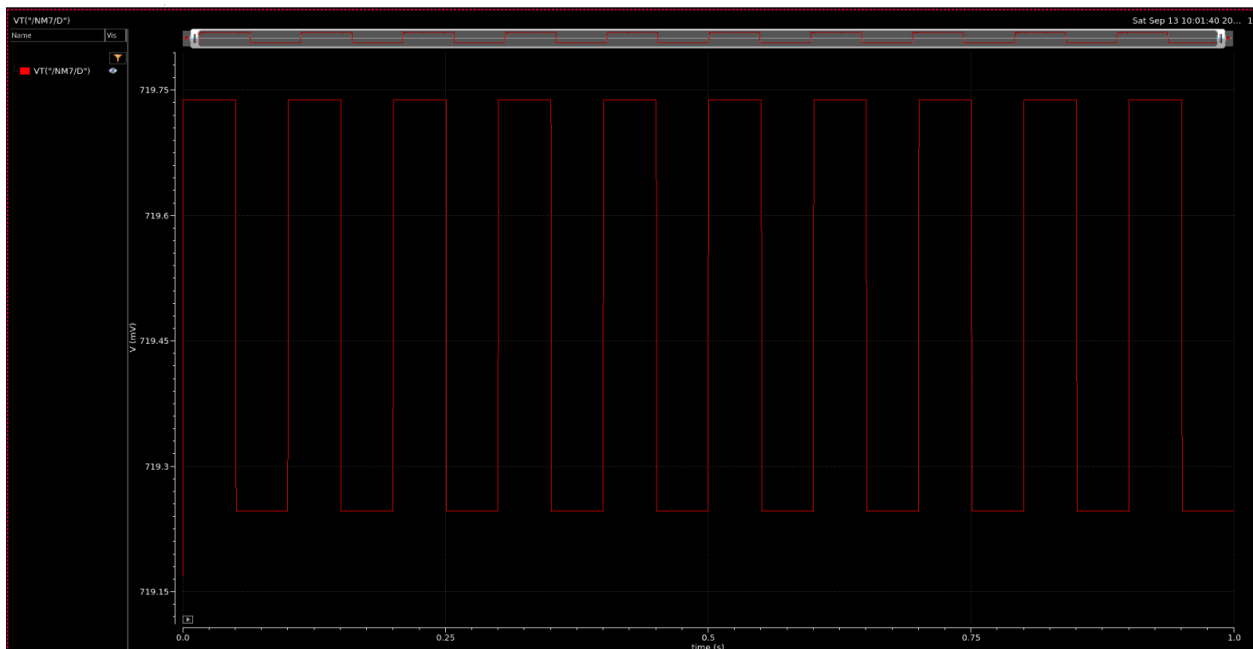


e.



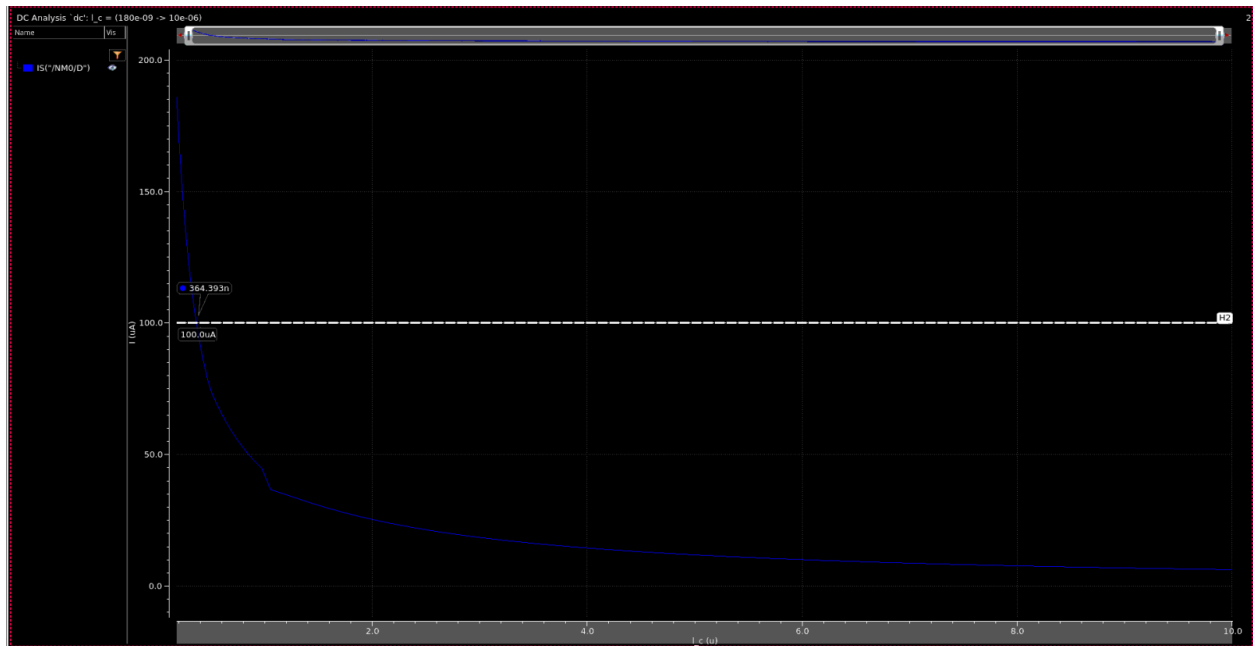


f.



4.

a. current mirror design



Name	Type	Details	Value	Plot	Plot Target	Save	Spec
ISCT(NM0(D7))	expr	ISCT(NM0(D7))	99.91u	<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
OSCT(NM0("region"))	expr	OSCT(NM0("region"))		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
INM0	oppoint	INM0("region")		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
OSCT(NM1("region"))	expr	OSCT(NM1("region"))		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
INM1	oppoint	INM1("region")		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
OSCT(NM2("region"))	expr	OSCT(NM2("region"))		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
INM2	oppoint	INM2("region")		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
OSCT(NM3("region"))	expr	OSCT(NM3("region"))		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
INM3	oppoint	INM3("region")		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
ISCT(NM0(D7))	expr	ISCT(NM0(D7))		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	

Setup

Analogs Assignment_5_Problem_4_1

Analogs

dc t 180n 10u Automatic Start-Stop

Design Variables

C.L.

Ijn 10u

Lc 364.4n

R.L

Rc 32k

Vampin

Vout 0.9

wc 20u

Parameters

Comers

Reliability Analyses

Monte Carlo Sampling

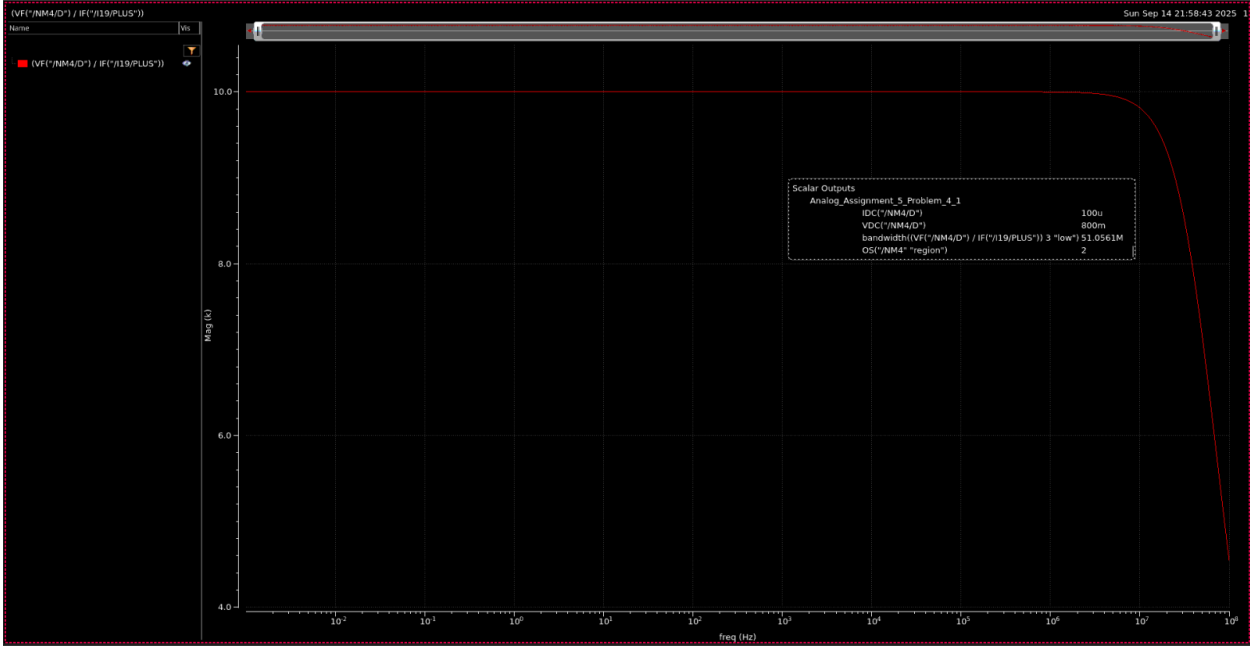
Checks/Rosers

Explore Run Summary

0 Corner

Nominal Corner

Gain is provided as 10k, hence $R_a \approx 10k$ ohm considered.

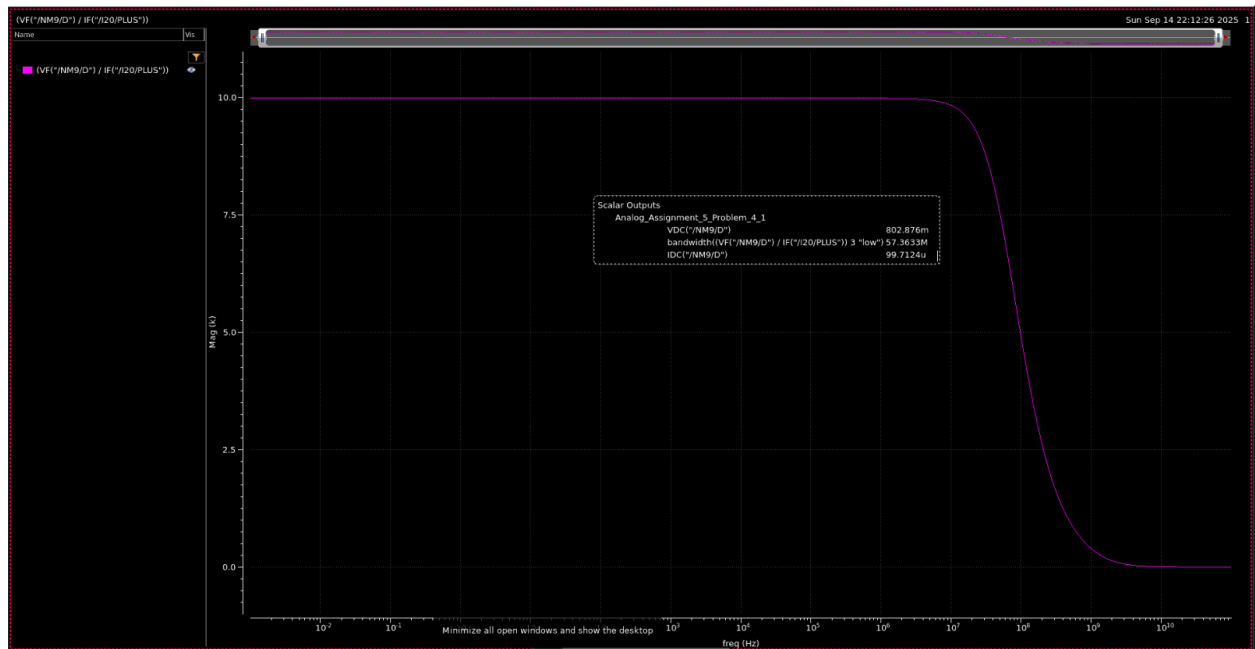


Name	Value
File	Problem_4
Project	Analog_Assignment_5_Problem_4_1
Simulator	spice
Analyses	ac 1m 100M 30 Logarithmic Points Per Decade
Design Variables	C1 0.300p L1 180n L2 10k R1 10k V1 1.25 V2 2u W1 100u
Parameters	
Reliability Analyses	
Monte Carlo Sampling	
Checks/Asserts	

Name	Type	Details	Value	Plot	Plot Target	Save	Spec
expr	IDC("NM4/D")		100u	<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
expr	VDC("NM4/D")		800m	<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
expr	(VF("NM4/D") / IF("I19/PLUS"))			<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
expr	bandwidth(VF("NM4/D") / IF("I19/PLUS")) 3 "low"		51.06M	<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
expr	OS("NM4" "region")		2	<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
/NM4	opposite	/NM4 region		<input type="checkbox"/>		<input checked="" type="checkbox"/>	

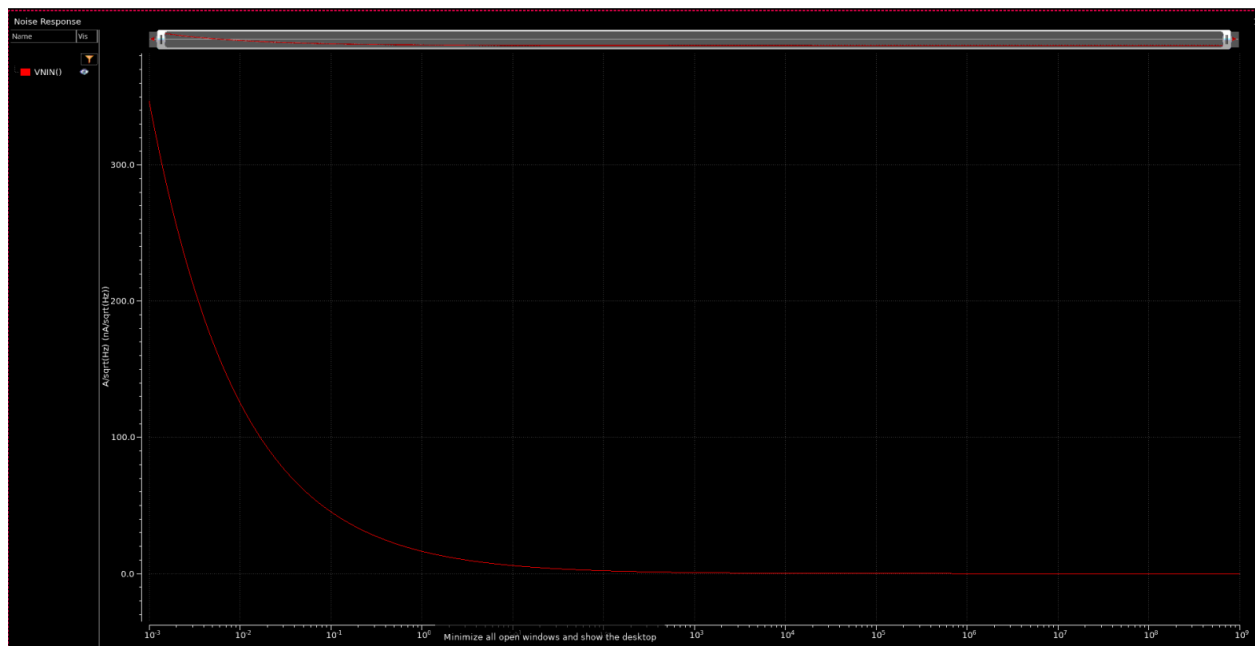
Explorer Run Summary
0 Corner
Nonlinear Corner

b.

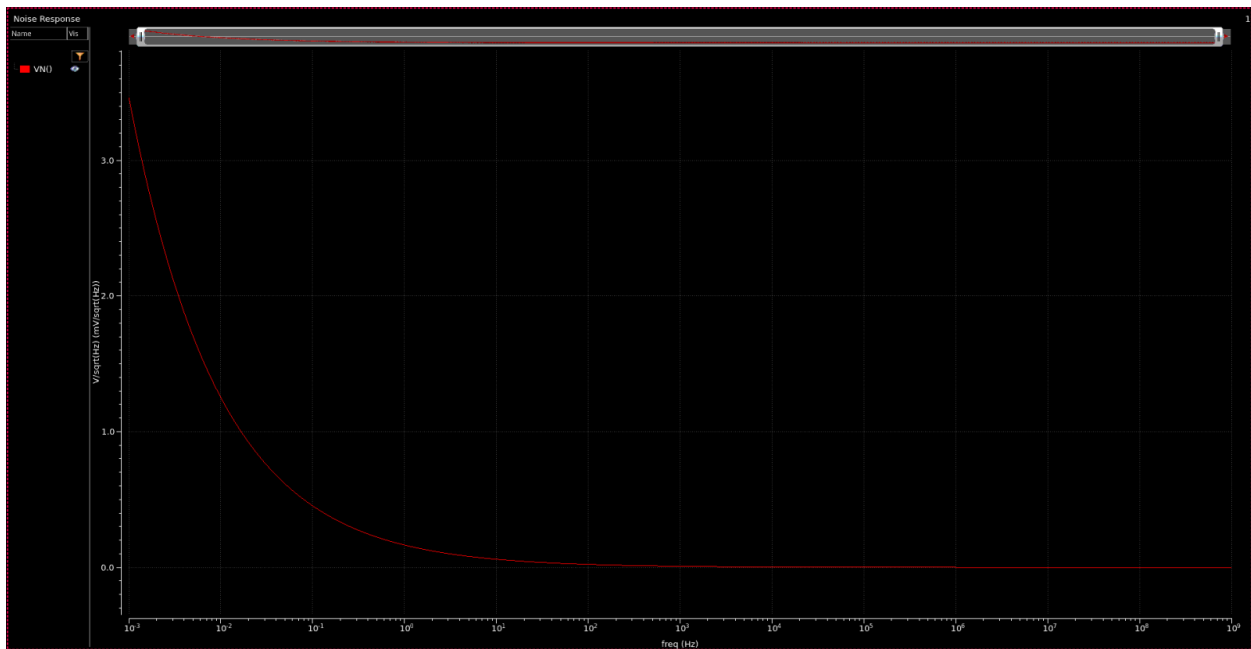


c.

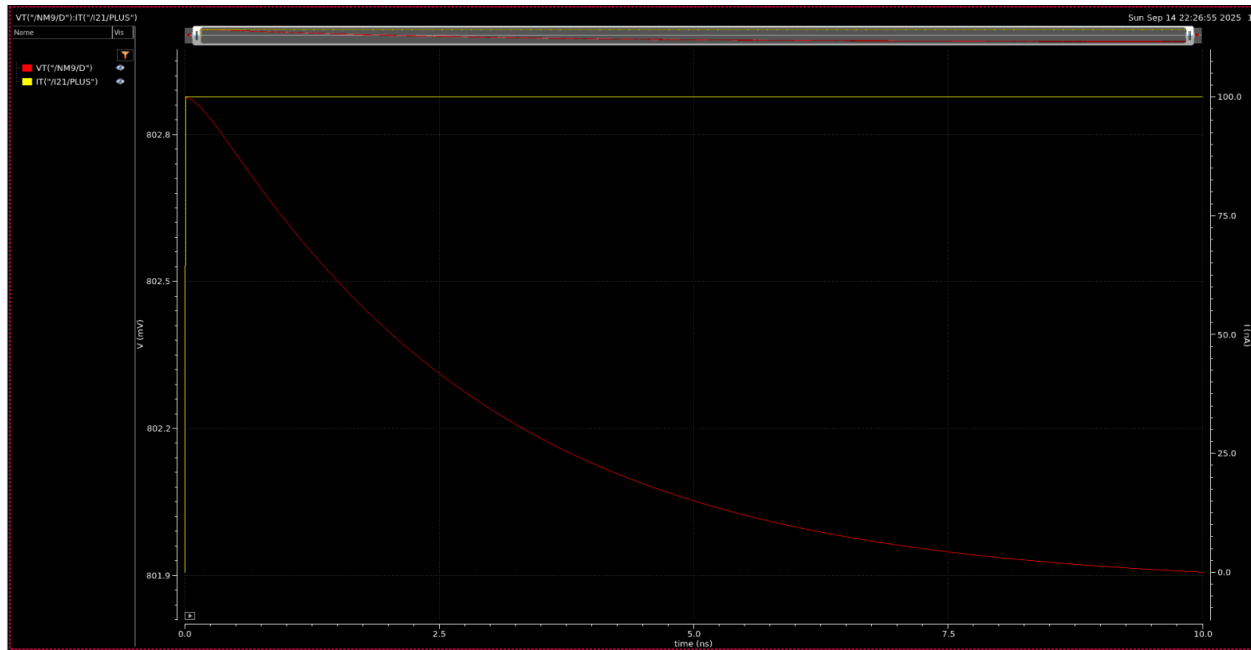
Input referred Noise



Output referred noise



d.



e.

