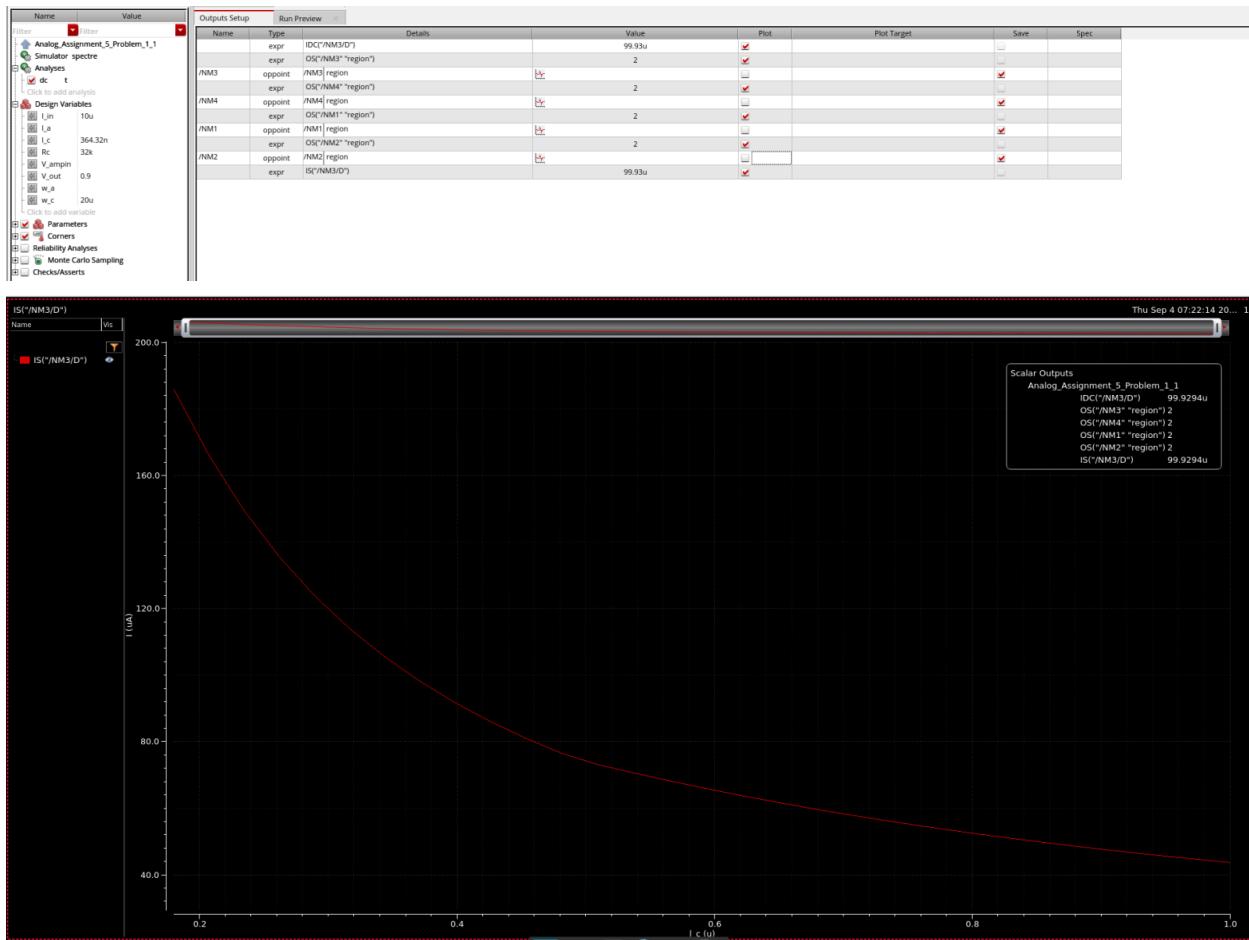
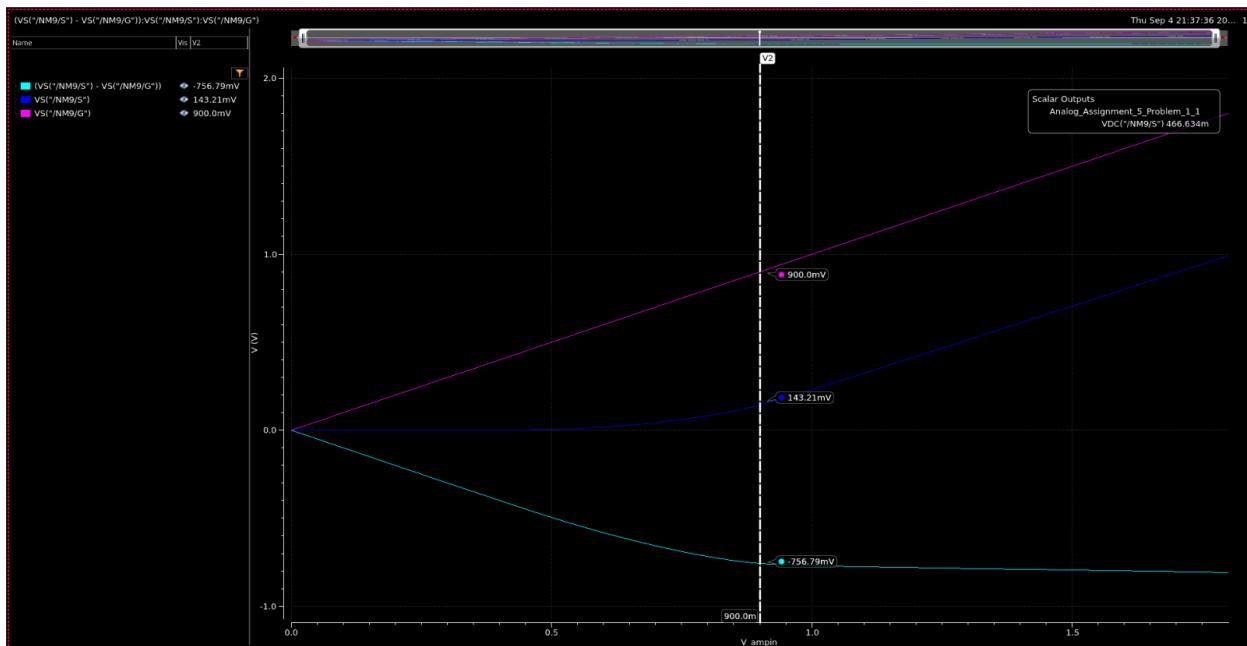
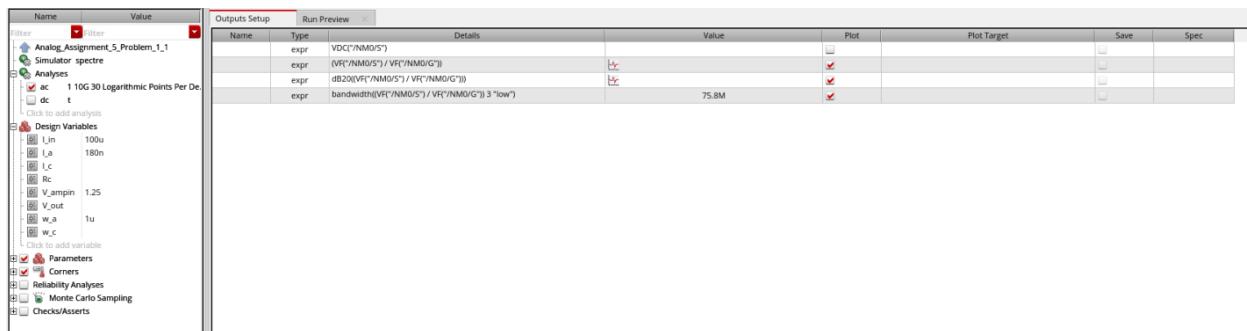
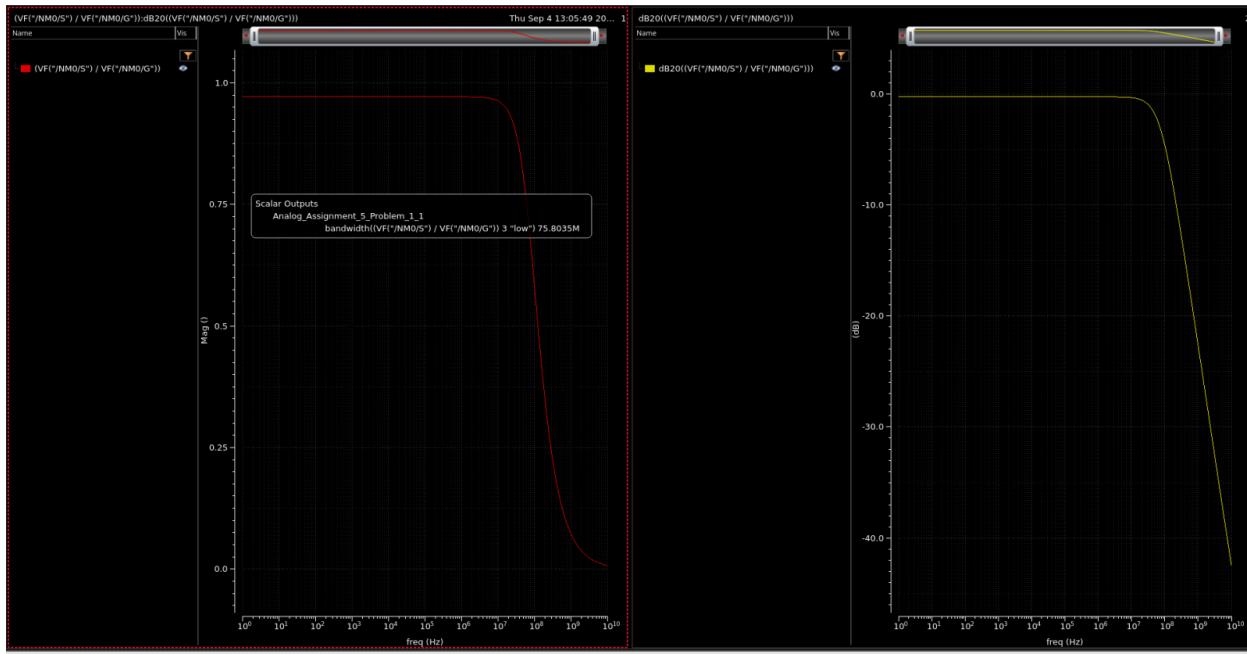


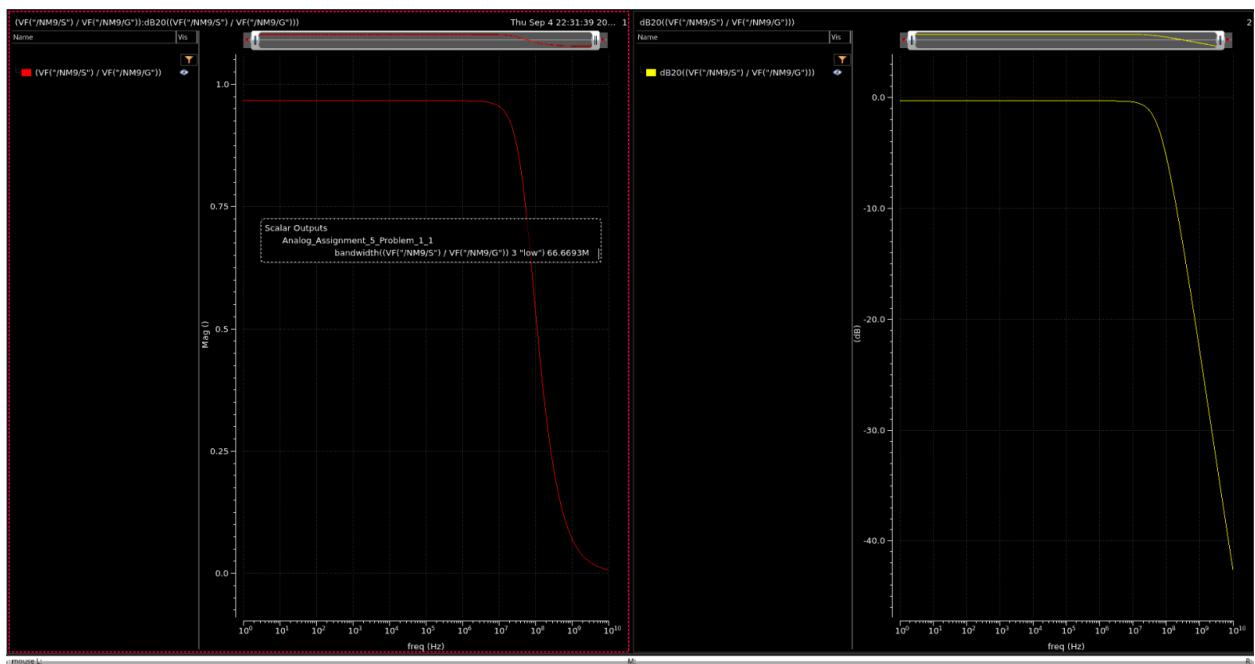
# Analog Assignment-5

1.

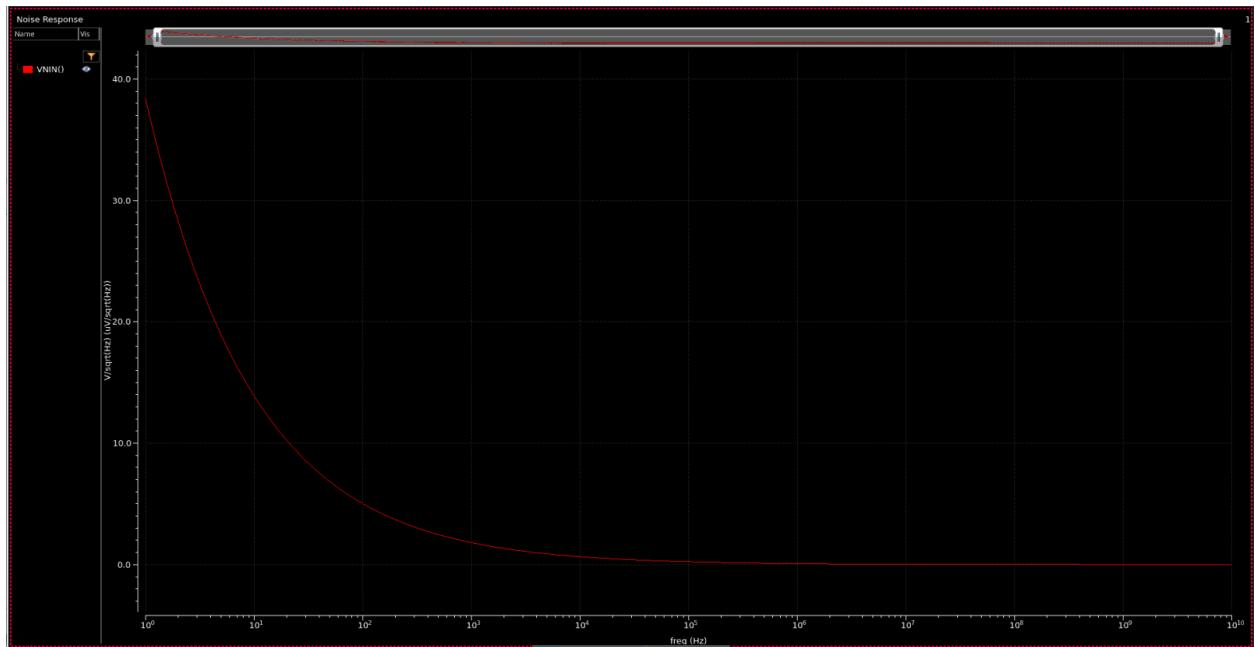




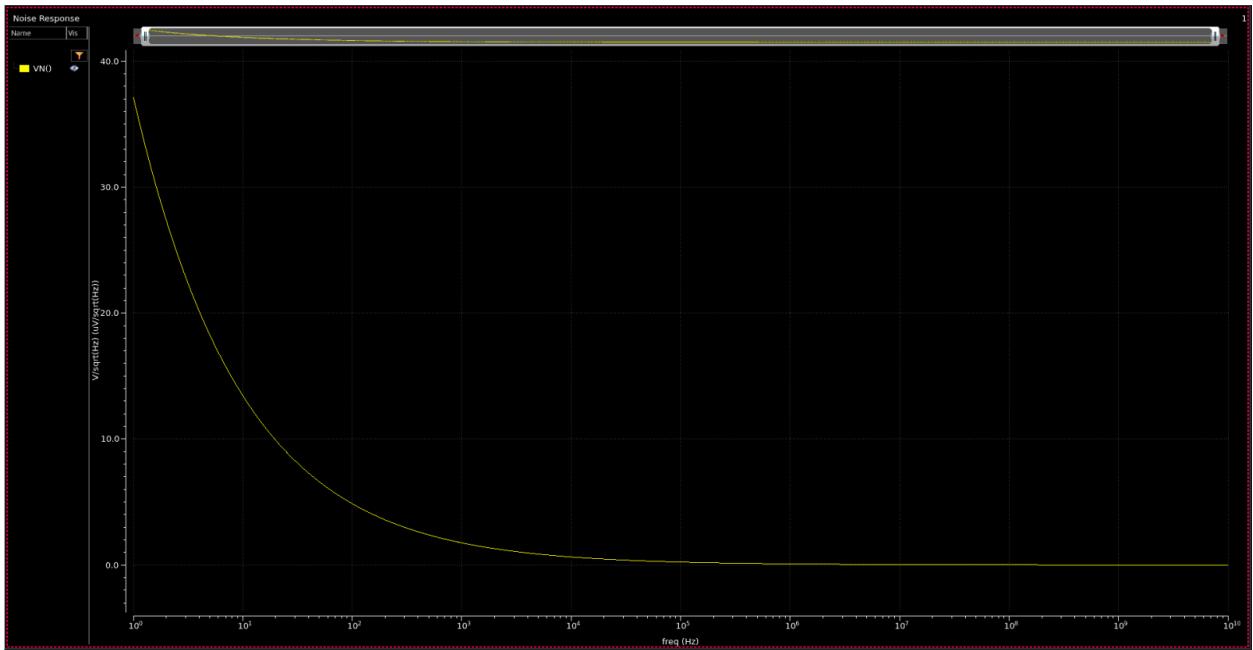
C.



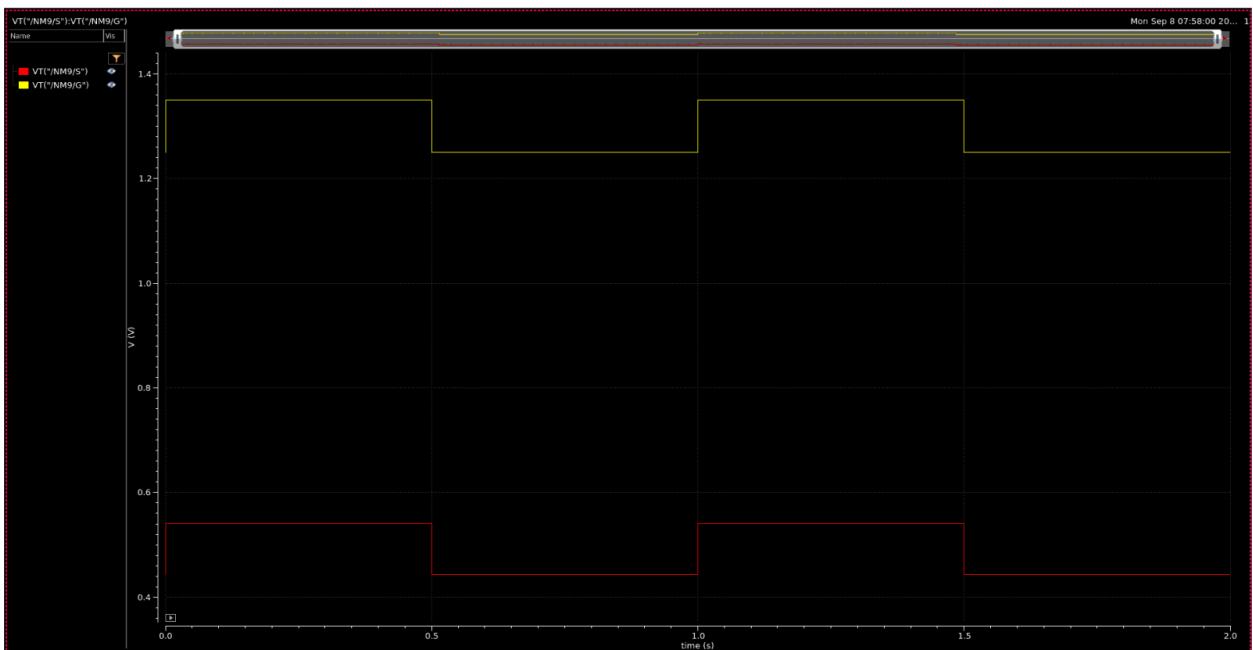
d. Input referred voltage noise.



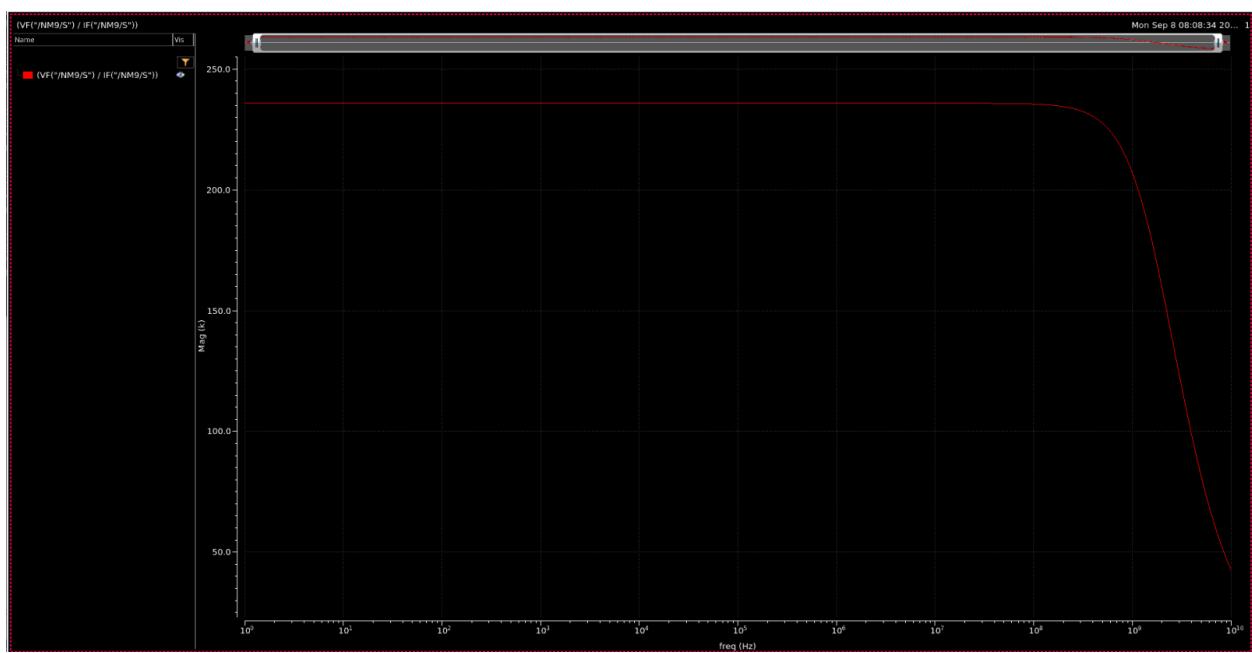
## Output noise



e.

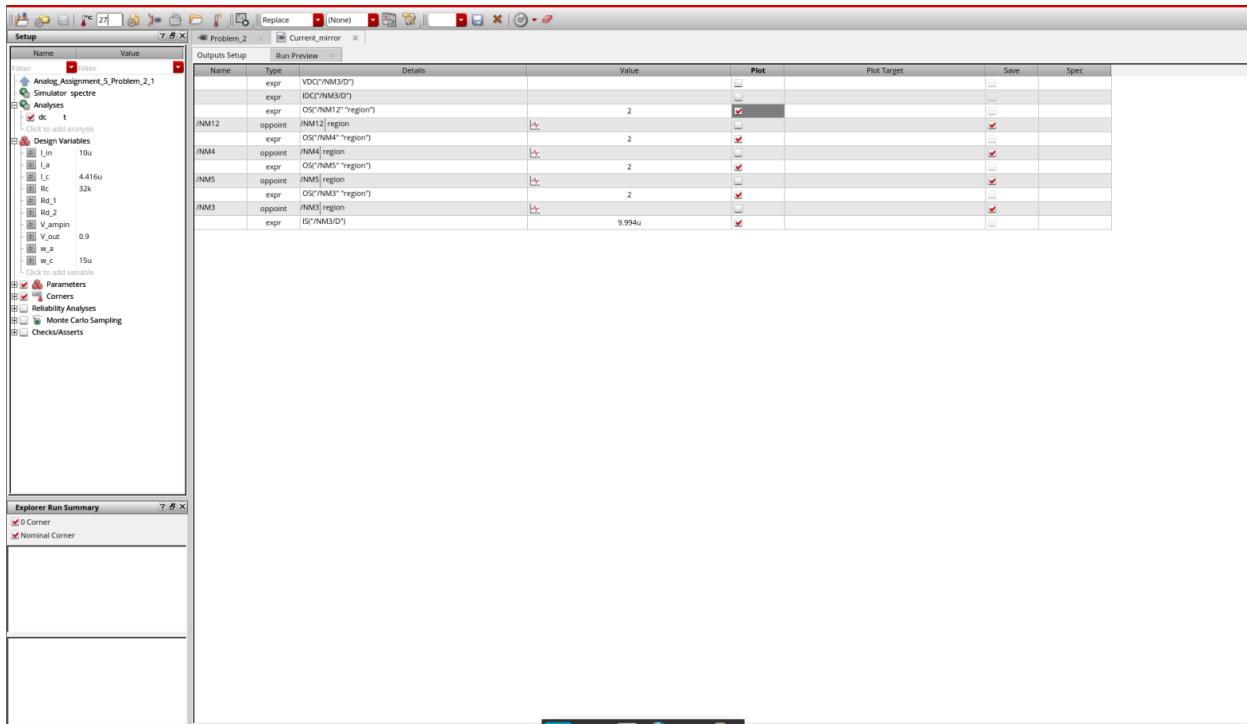


f.

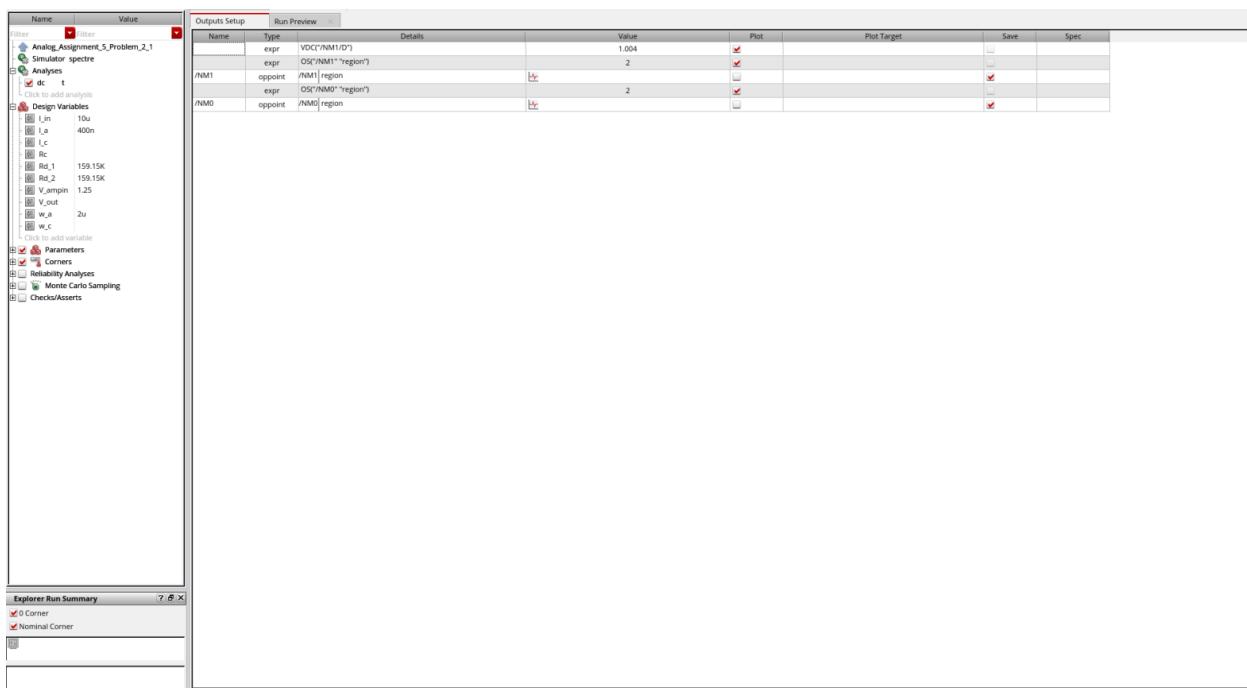


## Problem\_2

### Current mirror design with nmos in saturation



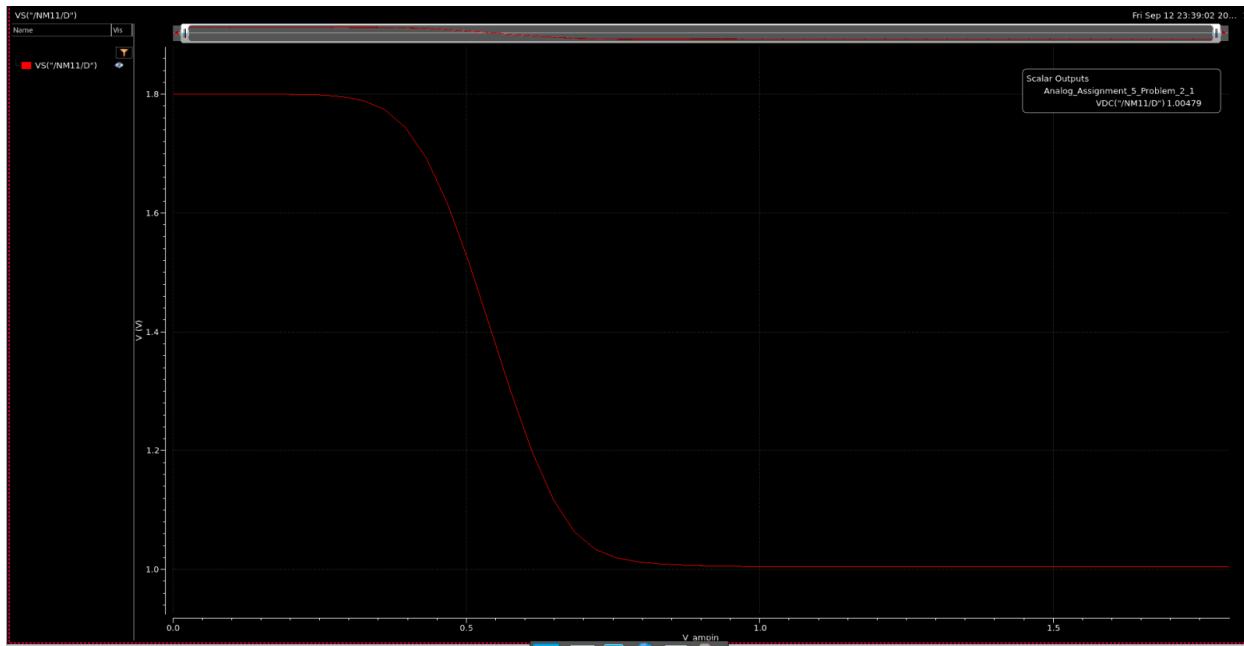
Top circuit design parameters with nmos in saturation



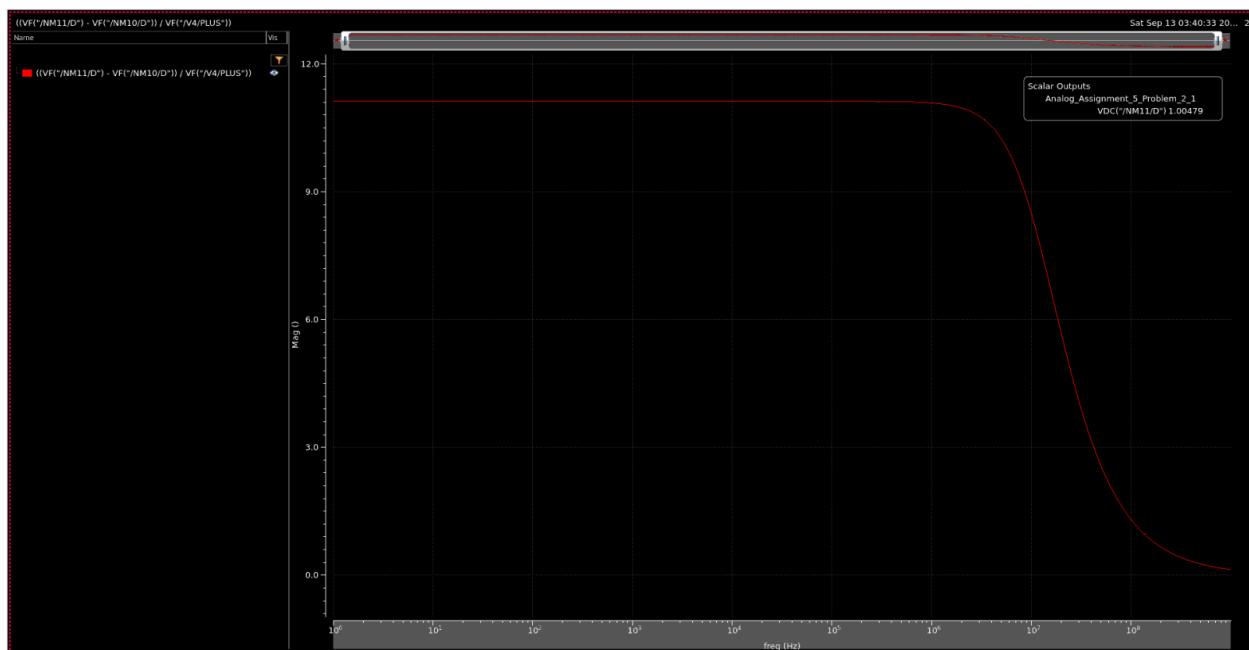
b.

The **input common-mode range (ICMR)** is the range of **DC input voltage (Vdc)** for which an amplifier (like a MOS differential amplifier) operates properly and maintains input–output linearity.

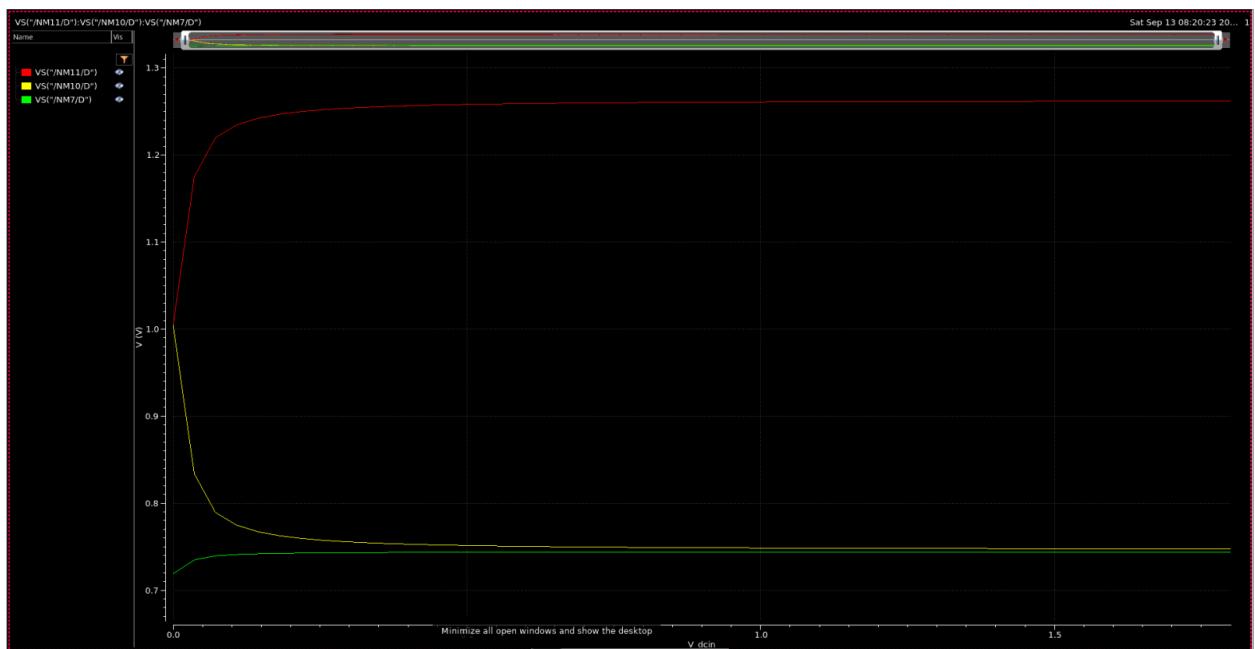
The range of Vdc for which amplifier operates in input-output linearity is 380 mV to 680 mV.



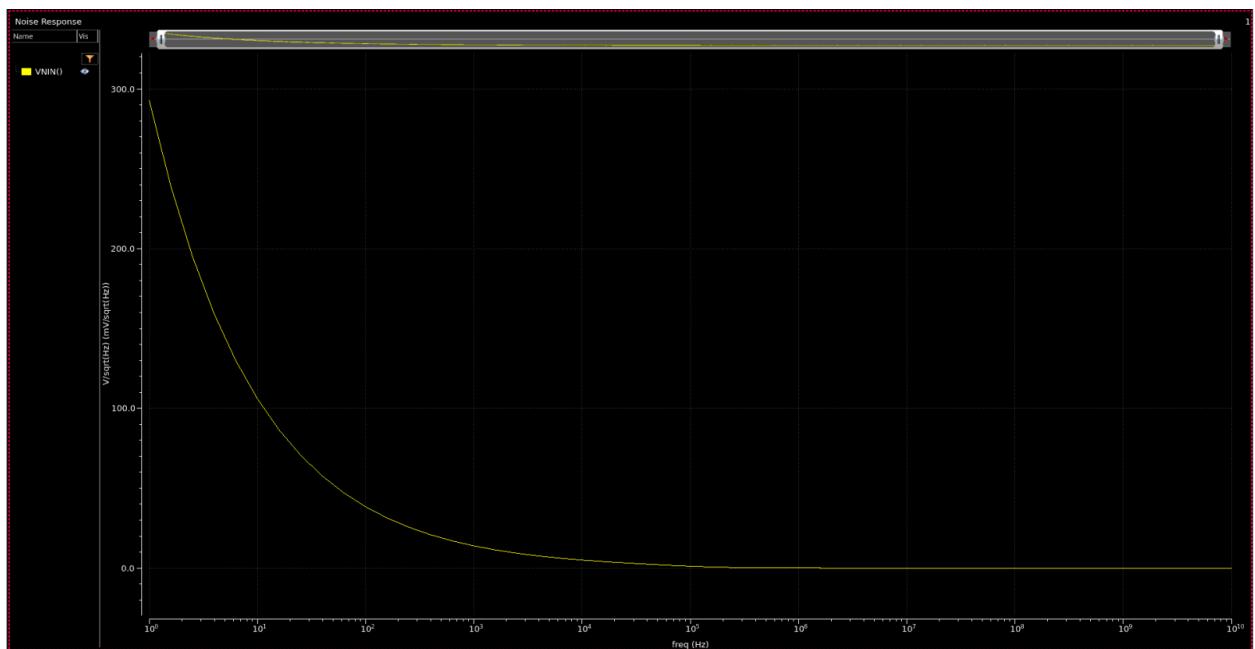
C.

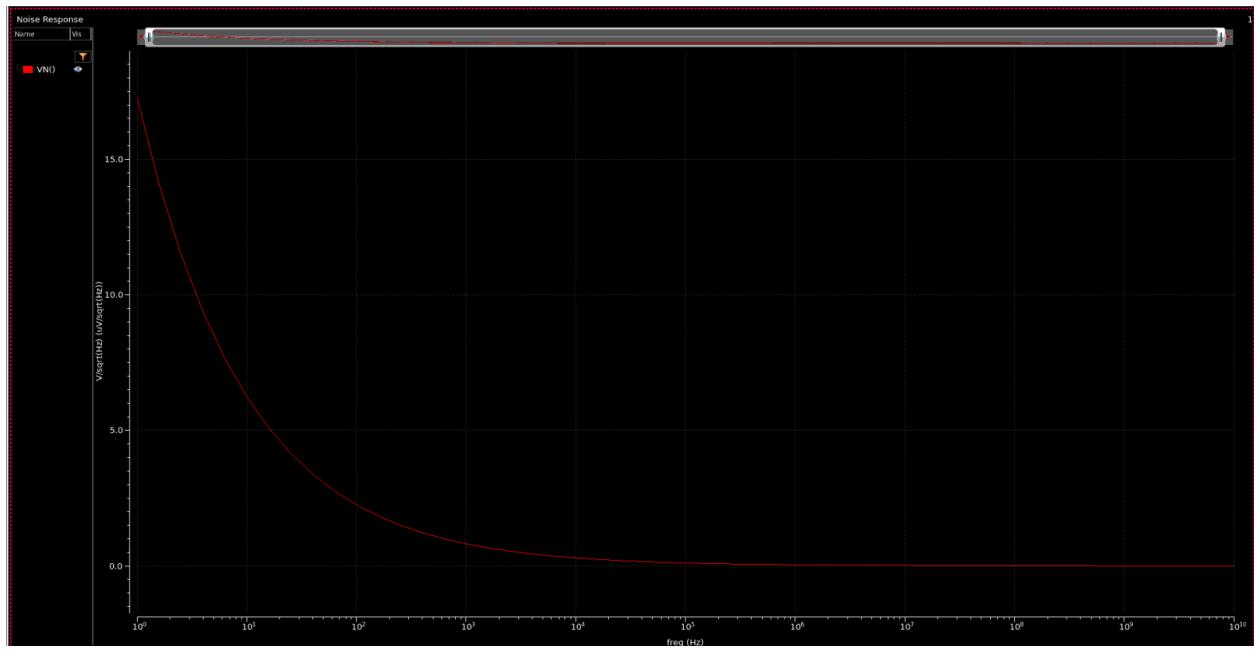


d.

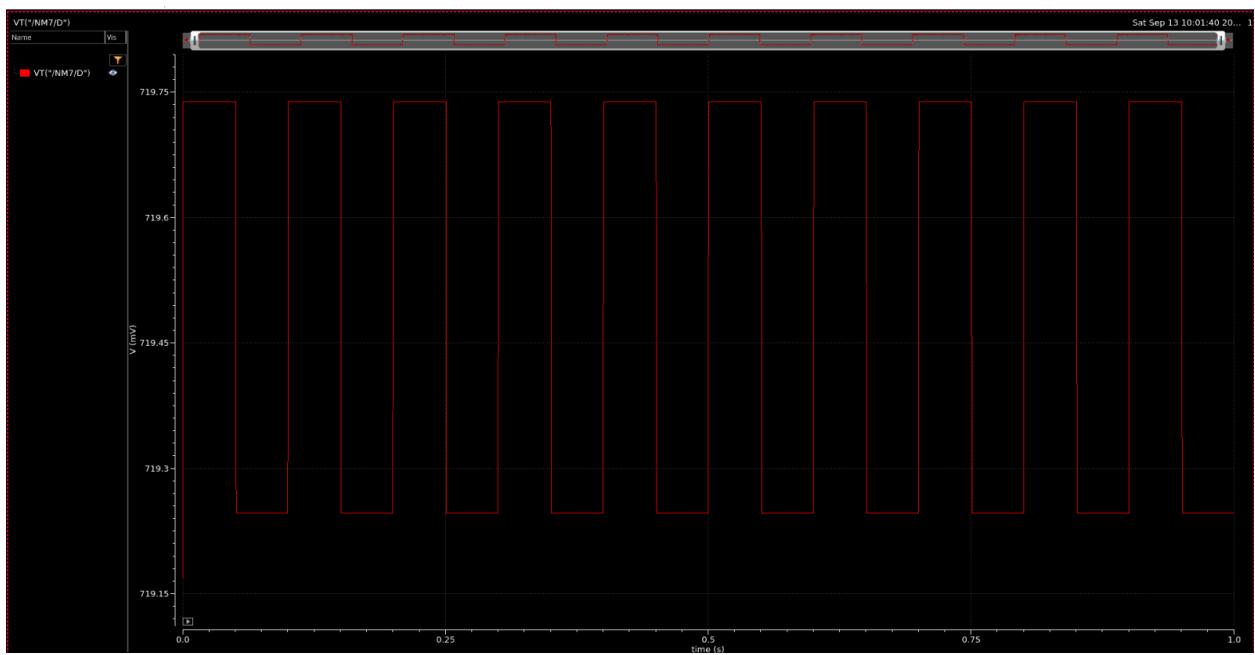


e.



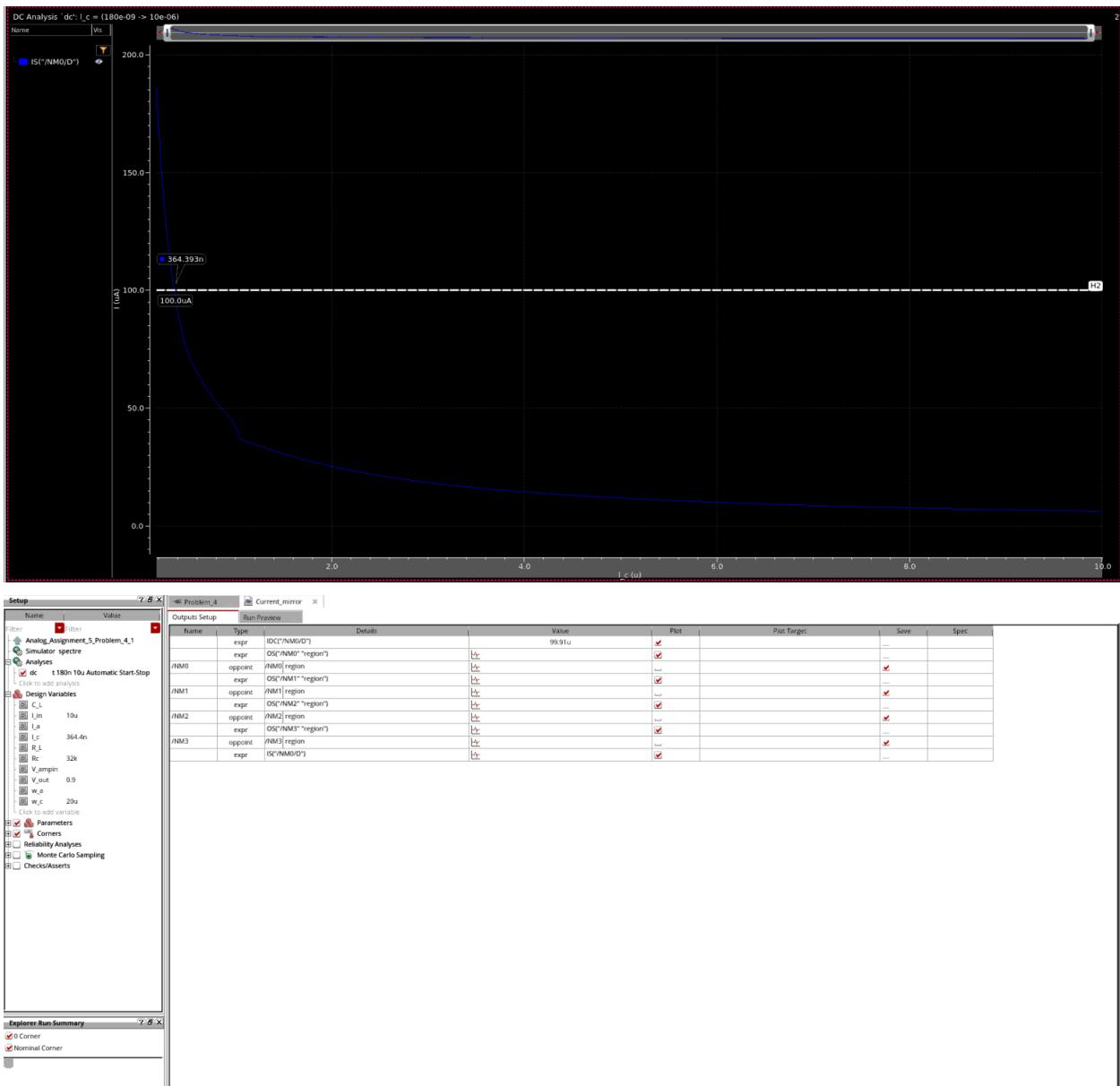


f.

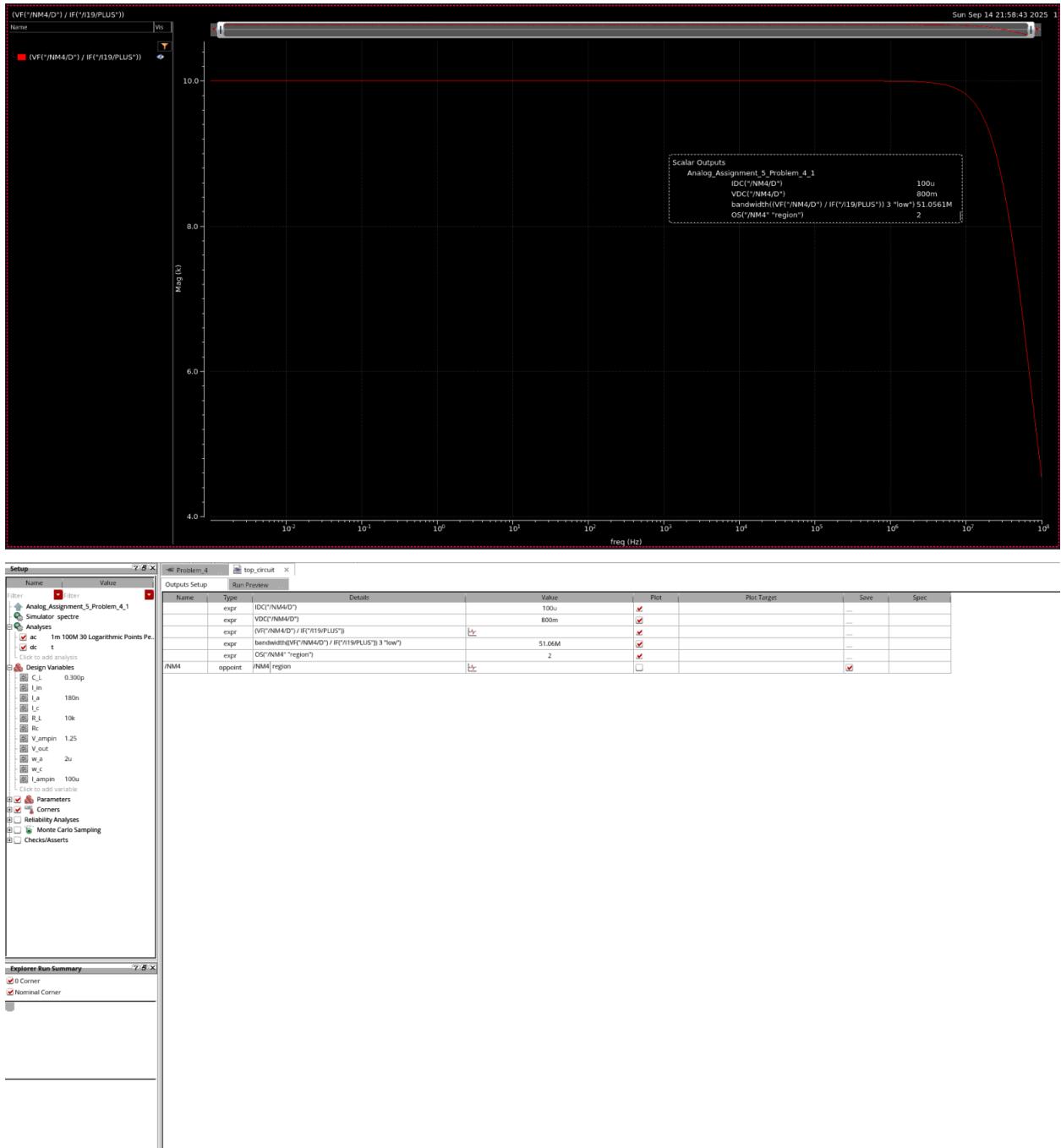


#### 4.

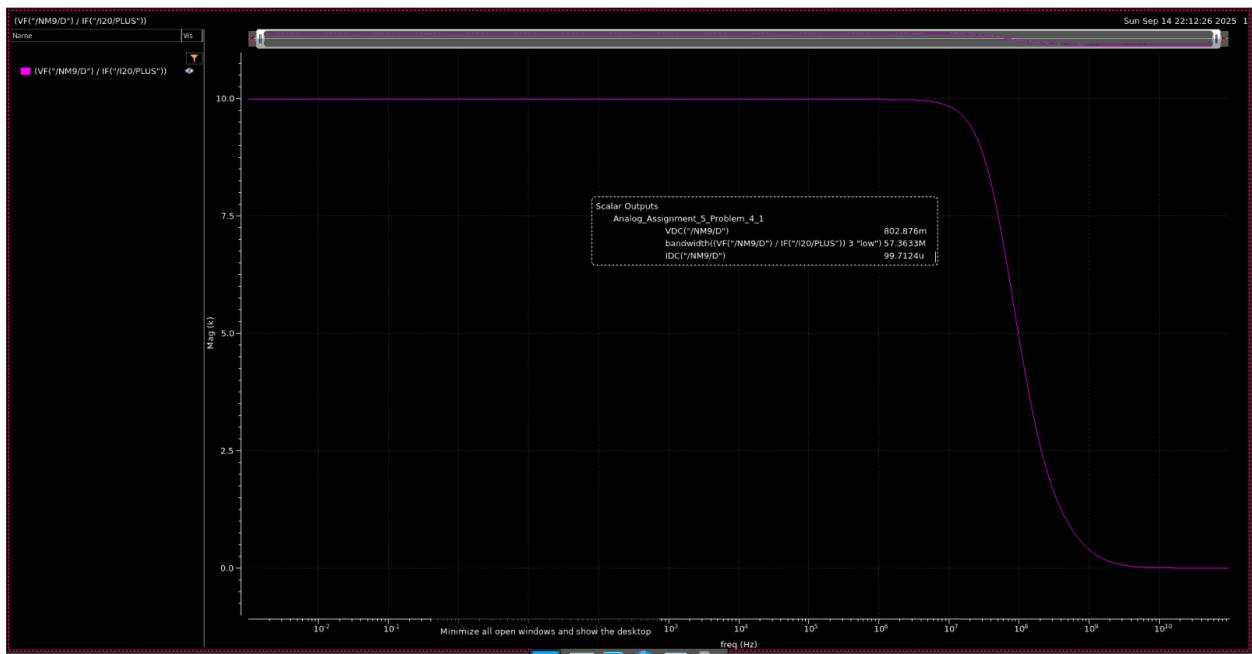
##### a. current mirror design



Gain is provided as 10k, hence  $R_a \approx 10k$  ohm considered.

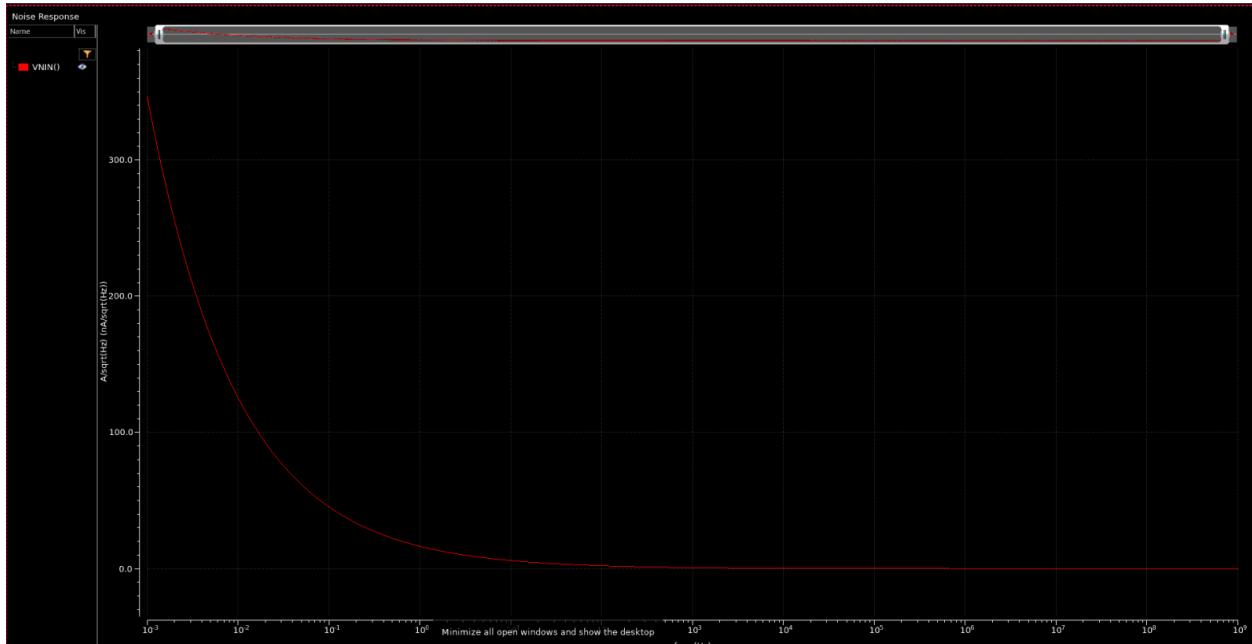


b.

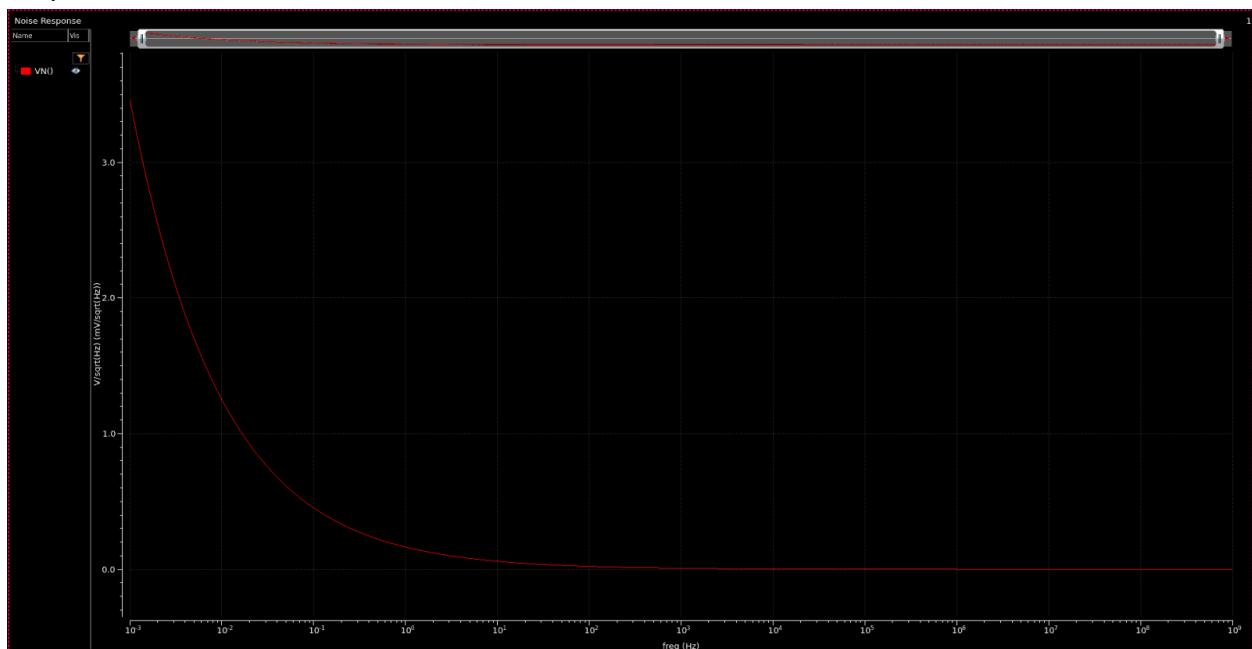


c.

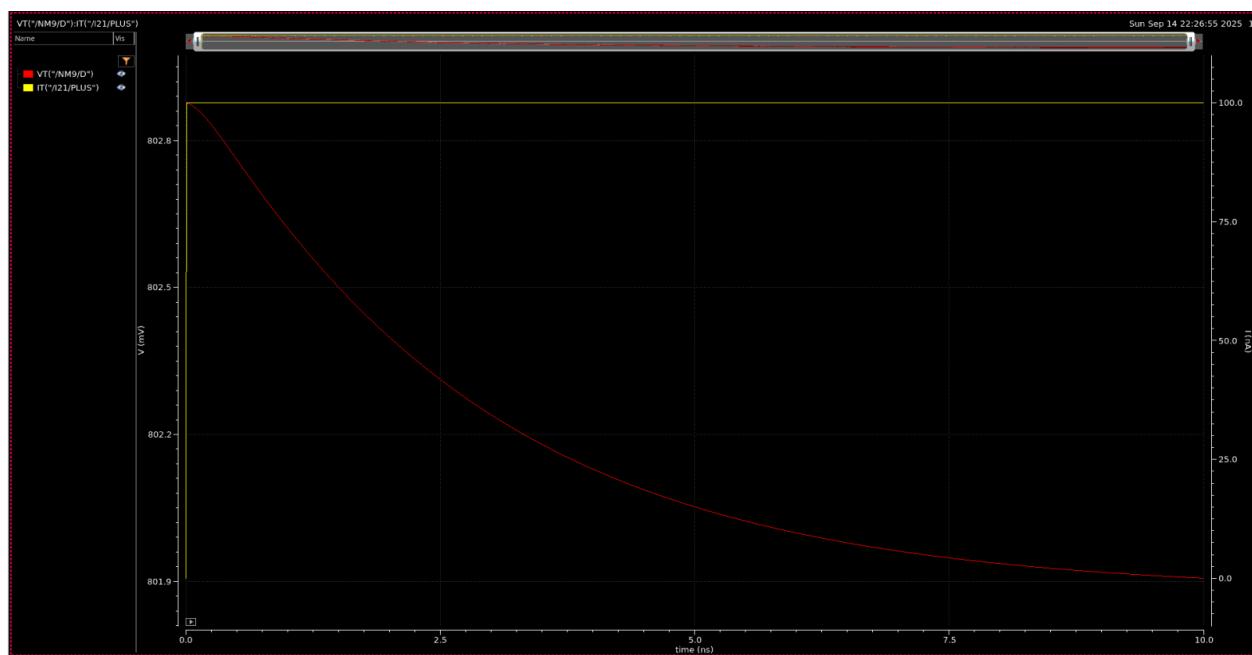
### Input referred Noise



## Output referred noise



d.



e.

