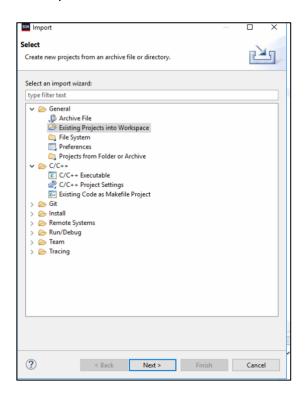
Authored by – Siddharth Chawla Linkedin - https://www.linkedin.com/in/siddharth-chawla/ Github repo = https://github.com/sidd91/cudaProject

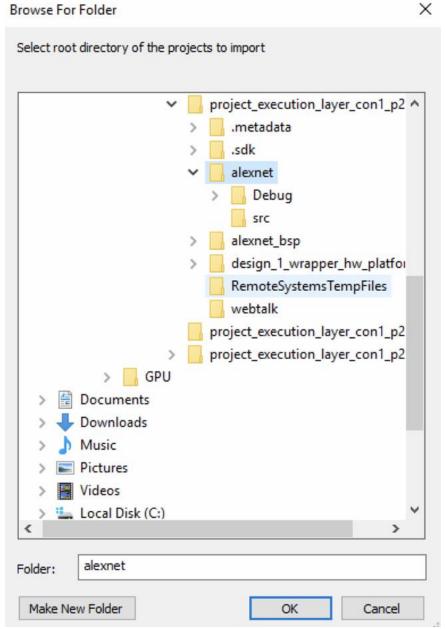
Loading and running a project using Vivado and Xilinx SDK.

Note: This tutorial is not a standalone tutorial. It is the part of <u>this repository</u>. Please read the README before going to the steps below.

- 1. Launch Vivado
- 2. Go to File -> Project -> Open Project
 - a. Navigate to the directory where the git repo was cloned
 - b. Go the FPGA directory and under the directory Vivado select project execution layer con1 p2
 - c. Select project_execution_layer_con1_p2.xpr
- 3. After the project has loaded. Go to File -> Launch SDK
- 4. SDK will take a moment to load the configuration
- After the project has loaded. Go to File -> Import and select Existing projects into Workspace and click Next

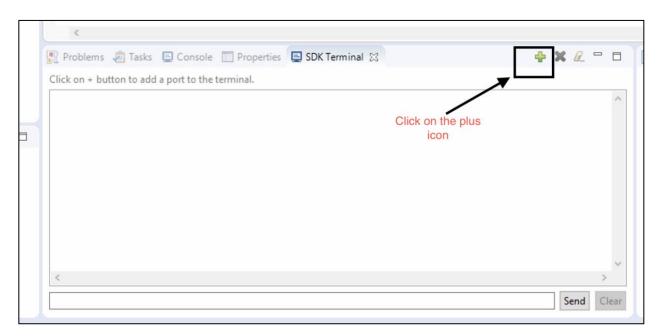


 and navigate to cudaProject/FPGA/Vivado/project_execution_layer_con1_p2/project_execution_layer_ con1_p2.sdk 7. Select alexnet directory and select **alexnet** and click OK.

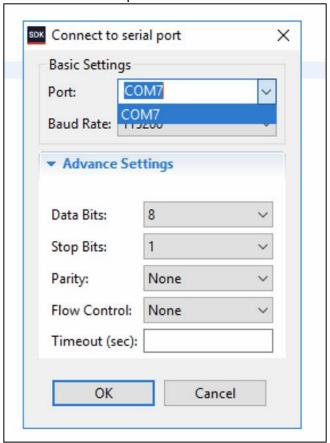


8. Repeat the same steps for importing alexnet_bsp

9. Configure the SDK Terminal

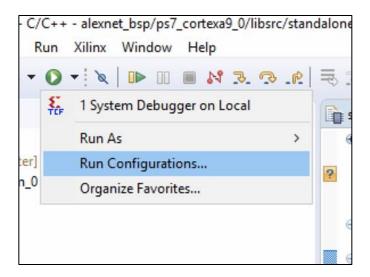


10. Select the com port and click on OK

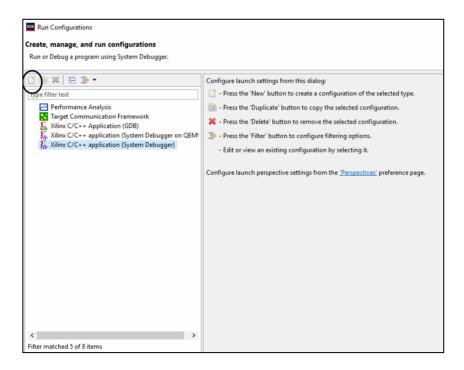


Setting up the run configuration -

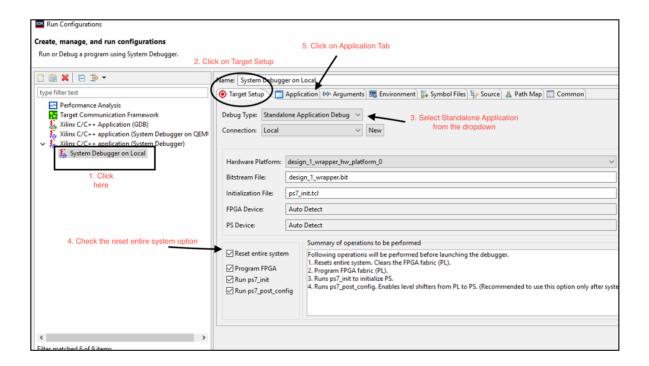
11. Go to the run button and click on Run configurations



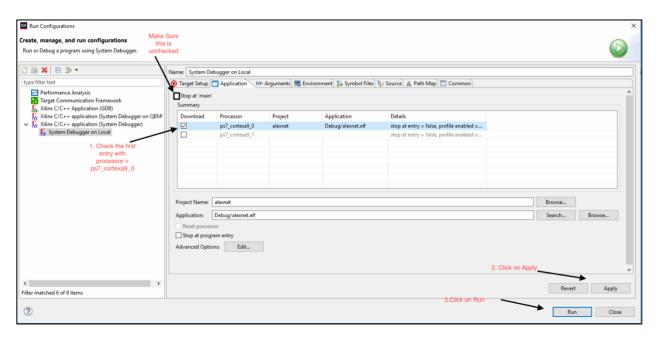
12. After clicking on Run Configurations. Below window will appear. Click on the Xilinx C/C++ application (System Debugger) and click on the New configuration icon



13. After Step 12, follow the steps below as shown in the screenshot



14. Set up the configuration.



15. Click on the resume button after clicking run.

16. You will see the print messages in the SDK Terminal.

