A Saliency-Driven LCD Power Management System

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I. Introduction

SINCE the invention of the first television, the display system has manifested into a critical human-computer-interface capable of bringing vivid and abundant visual information to users. As technology evolved, flat panel LCD display systems were invented and are now widely used in various multimedia systems, ranging from large screen home theatre systems to personal laptops and mobile devices. As projected in [1], LCD TVs are becoming progressively bigger in size every year, with customers demanding powerful visual experiences as video technology evolves. As the smartphone industry explores newer dimensions of innovation, there is an increasing demand for larger screens in this multimedia category too [2].

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While approximate computing is becoming a powerful paradigm to save energy in the vision space [5], another promising way to solve the power problem specific to LCD panels is to dim the LED backlight. Many different methods have been proposed in the same vein. Active dimming approaches adjust the luminance level of the backlight based on pixel information such as contrast, color, or brightness. Passive dimming methods modify the luminance level by monitoring user attention with a camera or sensors. The backlight is dimmed when users are away and restored to full level when users are in front of the display.

In [6], an active dimming strategy is proposed, where visual saliency is used to adaptively change the luminance level of the backlight panel at a zone granularity. Most objects of interest have distinct features that stand out in contrast to their background. [6] used three feature channels - *intensity*, *color* and *orientation* - for locating salient or interesting objects/regions in the scene. These low-level features, when applied to images,

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perform extremely well, as was demonstrated in [6]. However, due to the static nature of these channels, the system, when operating on a video stream, is constrained in that it fails to behave like a person who can focus attention on new objects entering the frame, or moving objects across a series of frames. To overcome these handicaps, we extend [6] with the following contributions:

- LCD-based devices these days invariably have streaming video applications running on them and consume more power than when a static image is being observed. As highlighted in [7], two additional processing channels, *motion* and *flicker* are effective in finding salient regions in a temporal environment. We thus incorporate these two channels into [6] to account for dynamic occurrences across a stream of video frames.
- We then highlight the impact of these changes on user experience. Since the original system settings in [6] introduce a *shimmer* (discussed later) in the final compensated video, new dimming and compensation coefficients are used. A movement constraint for salient zones between two continuous frames is also adopted for preserving video quality. Using these new compensation coefficients, verification of our system is undertaken with 29 different videos as part of a user perception evaluation test.
- To address system constraints such as power, performance and resource tradeoffs, this paper adopts a generic fieldprogrammable gate array (FPGA) channel architecture.
 On average, a 50% power saving can be achieved with our extended system while maintaining real-time constraints.

The organization of the rest of this paper is as follows: Section ?? highlights related work on bio-inspired systems in general and various LED power saving schemes in specific; Section ?? introduces Saliency, the adopted biological attention model, in detail; the LED backlight system and its power model is outlined in Section ??; Section ?? shows the design and implementation details of the proposed power management system; all the experimental results are provided in Section ??; finally we conclude the paper in Section V.

II. BEYOND DEEP LEARNING

Since the invention of the first television, the display system has manifested into a critical human-computer-interface capable of bringing vivid and abundant visual information to users. As technology evolved, flat panel LCD display systems were invented and are now widely used in various multimedia systems, ranging from large screen home theatre systems to personal laptops and mobile devices. As projected in [1], LCD TVs are becoming progressively bigger in size every year, with customers demanding powerful visual experiences as

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III. INTERFACES

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IV. HARDWARE

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V. CONCLUSION

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ACKNOWLEDGMENT

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John (Jack) Sampson received the B.S. degree in Electrical Engineering and Computer Science from the University of California, Berkeley in 2002 and the Ph.D. in Computer Engineering from the University of California, San Diego in 2010. He is an Assistant Professor in the Department of Computer Science and Engineering at the Pennsylvania State University. His research interests include energy-efficient computing, architectural adaptations to exploit emerging technologies, and mitigating the impact of Dark Silicon.



Vijaykrishnan Narayanan received the B.S. degree in Computer Science and Engineering from the University of Madras, India, in 1993 and the Ph.D. in Computer Science and Engineering from the University of South Florida, Tampa, USA, in 1998. He is a Professor of Computer Science & Engineering and Electrical Engineering at the Pennsylvania State University. His research interests include power-aware and reliable systems, embedded systems, nanoscale devices and interactions with system architectures, reconfigurable systems, com-

puter architectures, network-on-chips, domain-specific computing.

Dr. Narayanan has received several awards including the Penn State Engineering Society Outstanding Research Award in 2006, IEEE CAS VLSI Transactions Best Paper Award in 2002, the Penn State CSE Faculty Teaching Award in 2002, the ACM SIGDA outstanding new faculty award in 2000, Upsilon Pi Epsilon award for academic excellence in 1997, the IEEE Computer Society Richard E. Merwin Award in 1996 and the University of Madras first rank in Computer Science and Engineering in 1993. He is currently the editor-in-chief of IEEE Transactions on Computer Aided Design of Integrated Circuits and Systems. He has received several certificates of appreciation for outstanding service from ACM and IEEE Computer Society. He is a Fellow member of IEEE and ACM.