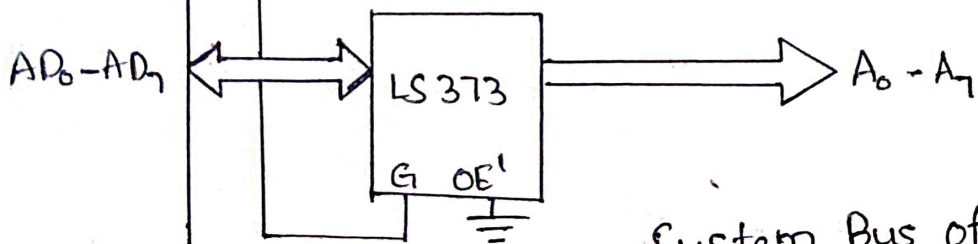
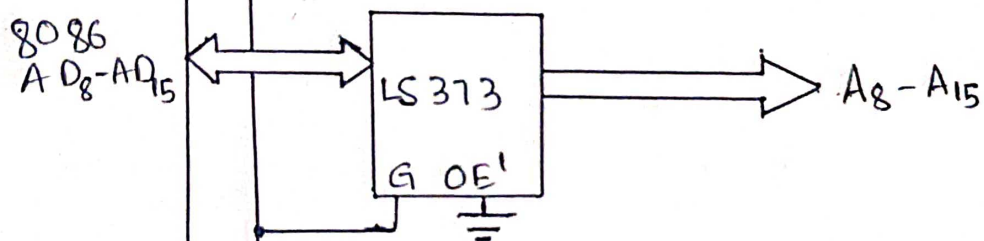
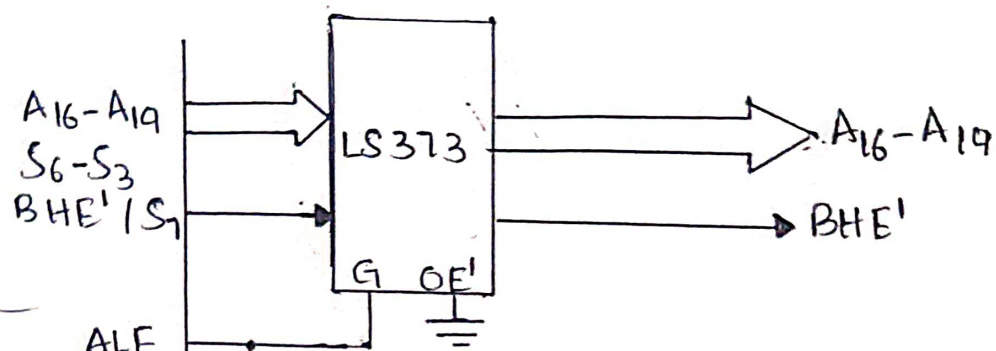
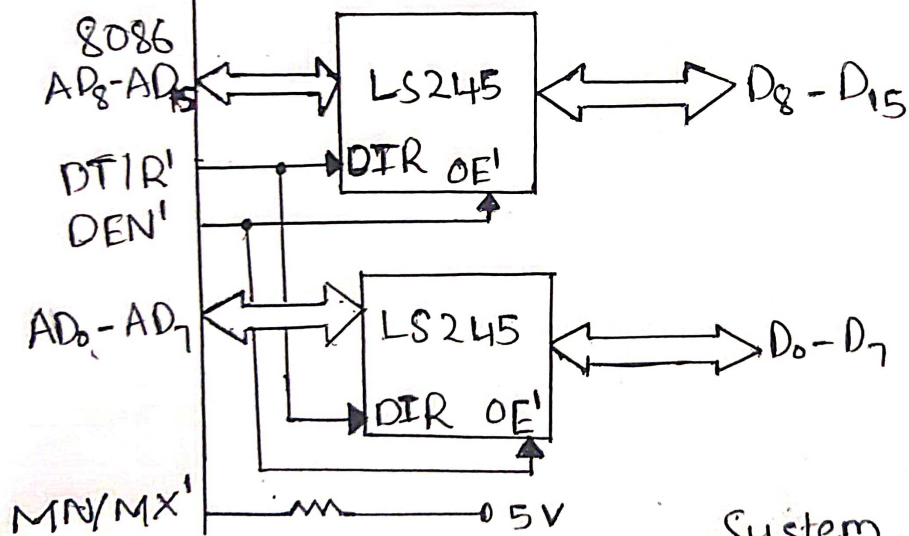
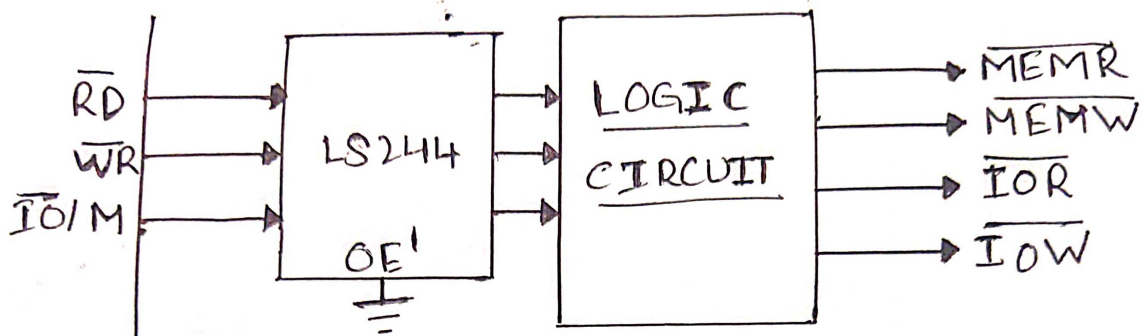


8086 Inputs



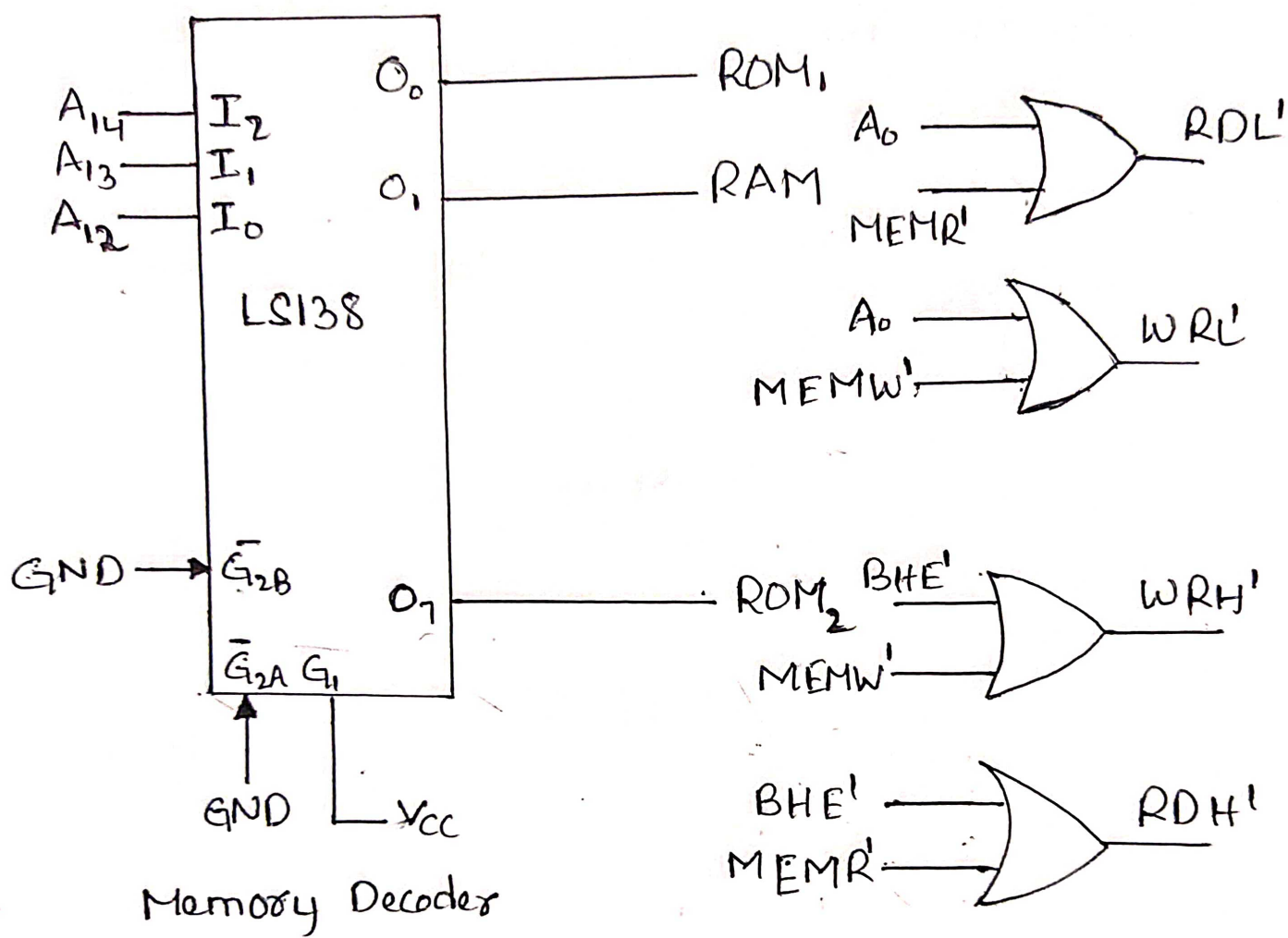
System Bus of 8086 (Address)

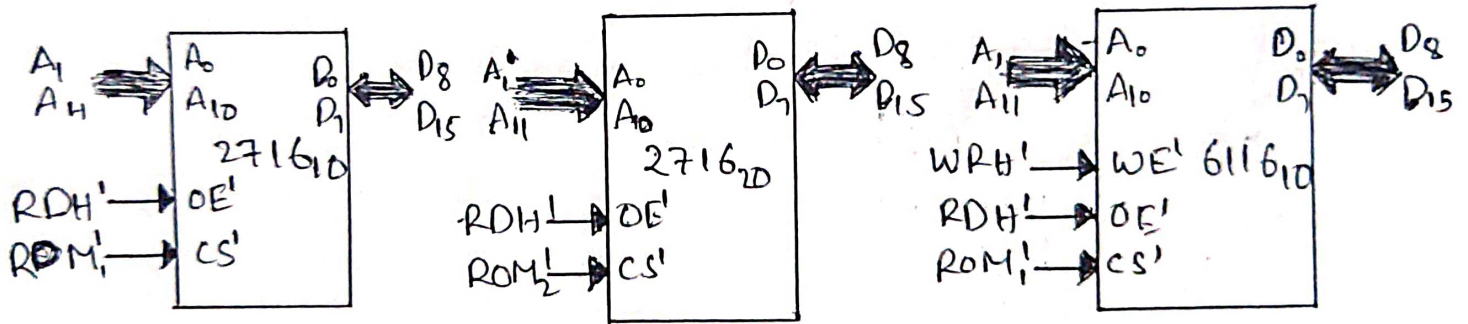
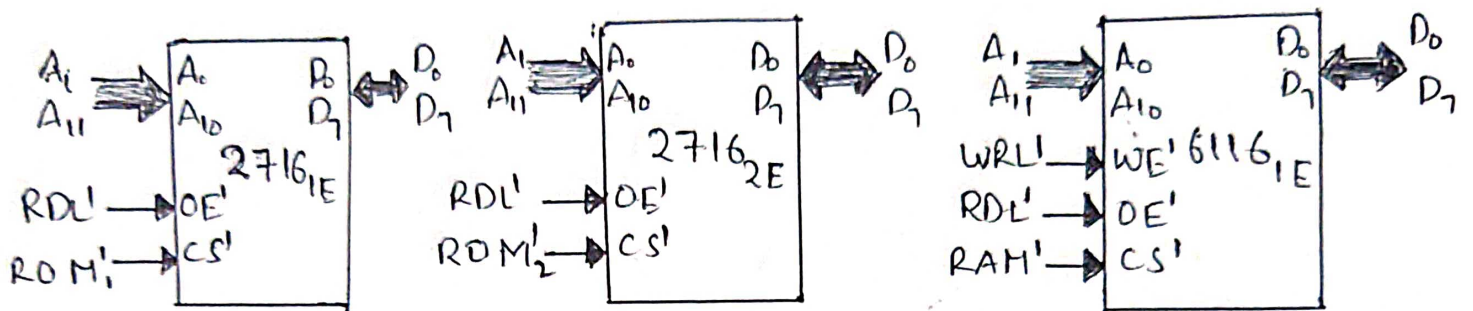


System Bus of 8086 (Data+Control)

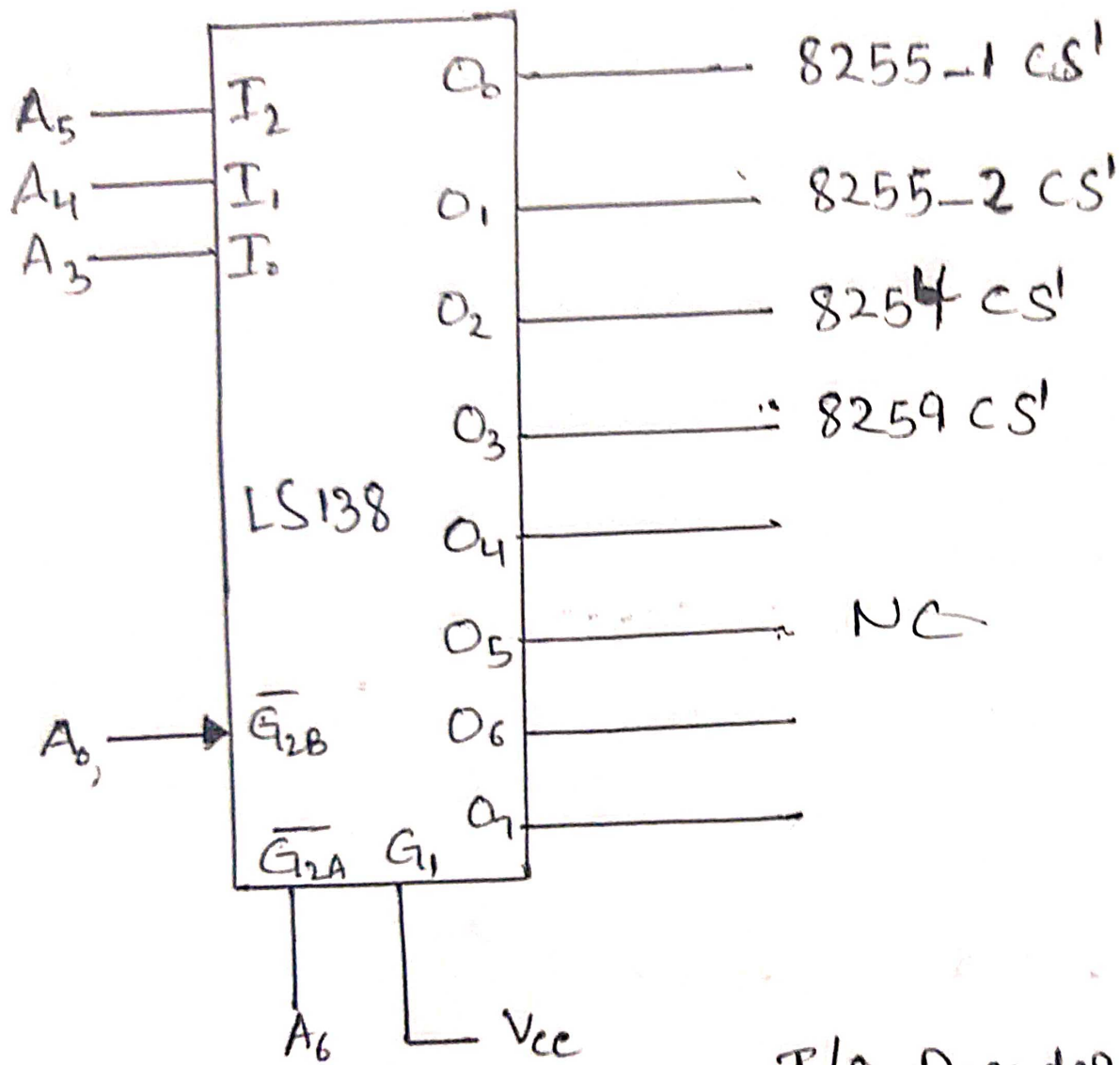


$M/IO'$	$RD'$	$WR'$	Bus cycle
1	0	1	$MEMR'$
1	1	0	$MEMW'$
0	0	1	$IOR'$
0	1	0	$IOW'$





Memory Interfacing



I/O Decoder

# 8255 Interface to processor

$A_1$  ———  $A_0$

$A_2$  ———  $A_1$

8255-1  $\overline{CS}$  ———  $\overline{CS}$

8255-1

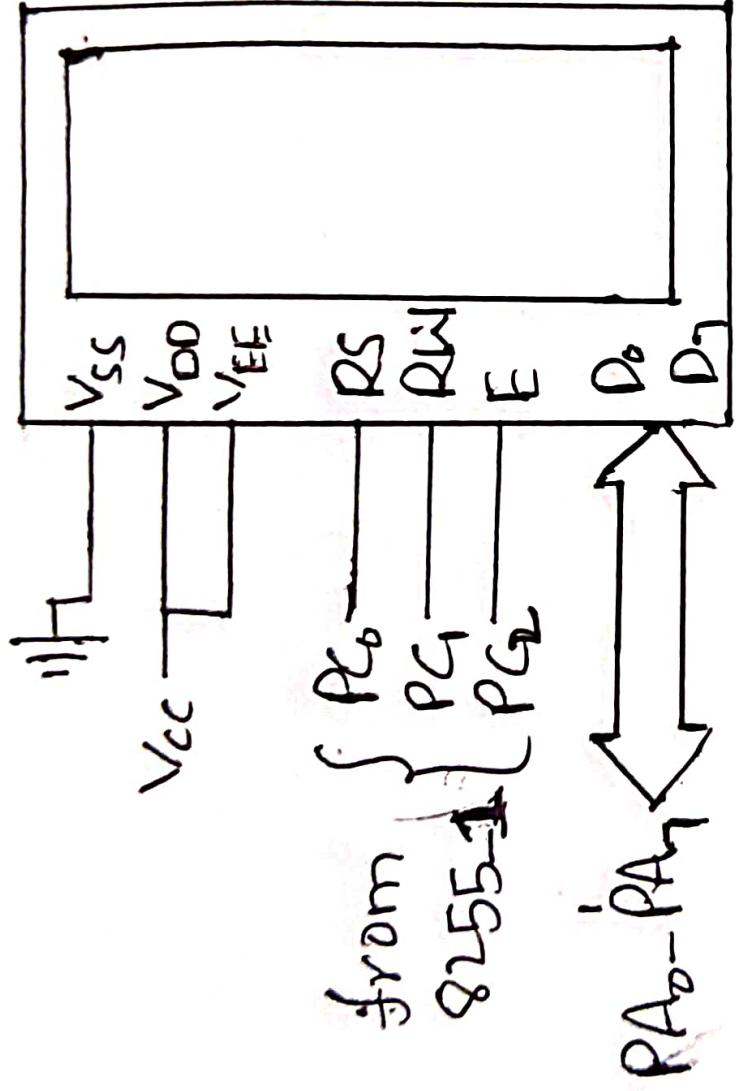
$\overline{IOR}$  ———  $\overline{RD}$

$\overline{IOW}$  ———  $\overline{WR}$

$D_0-D_7$   $\longleftrightarrow$   $D_0-D_7$

$\overline{RESET}$  from 8284 ———  $\overline{RESET}$

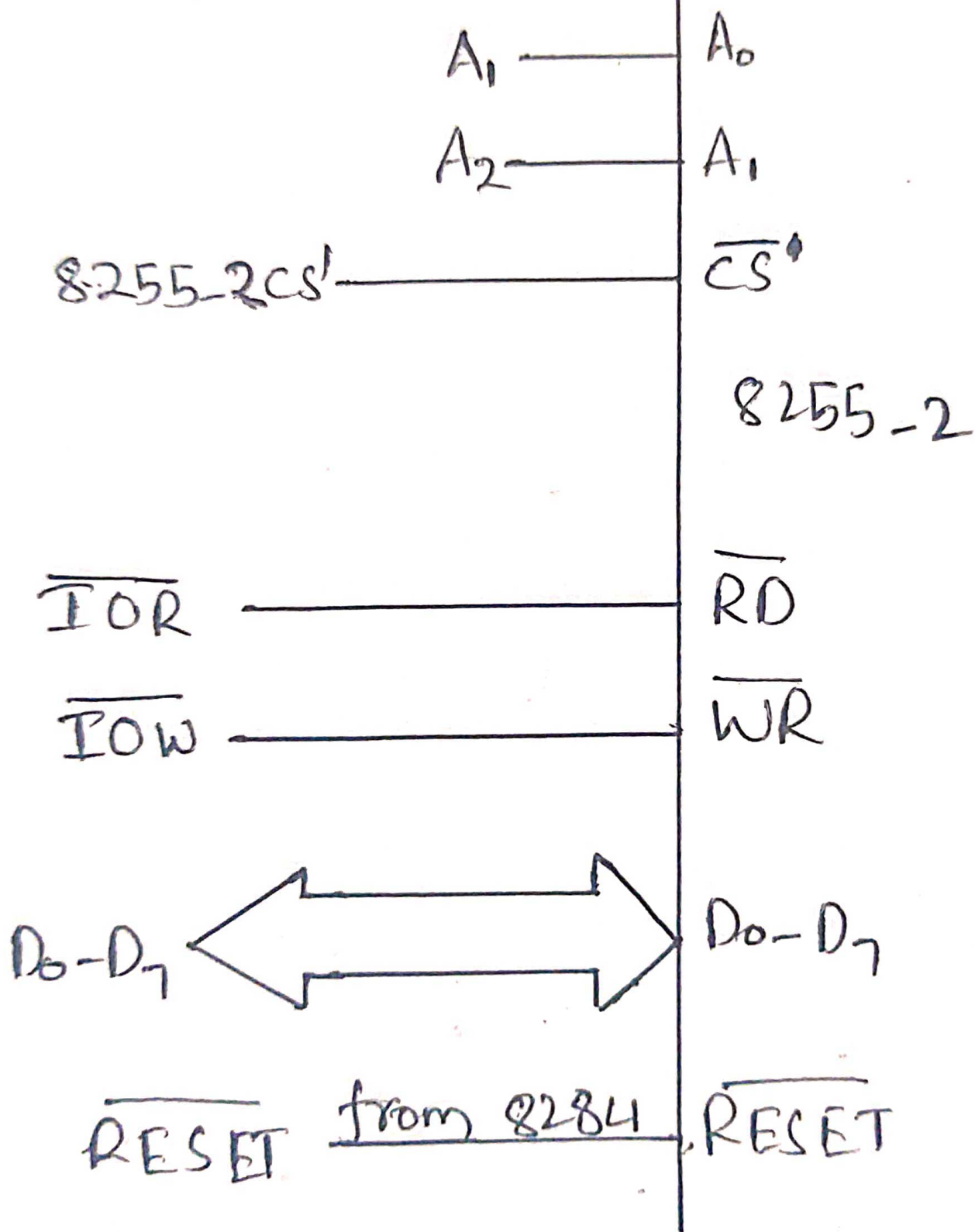


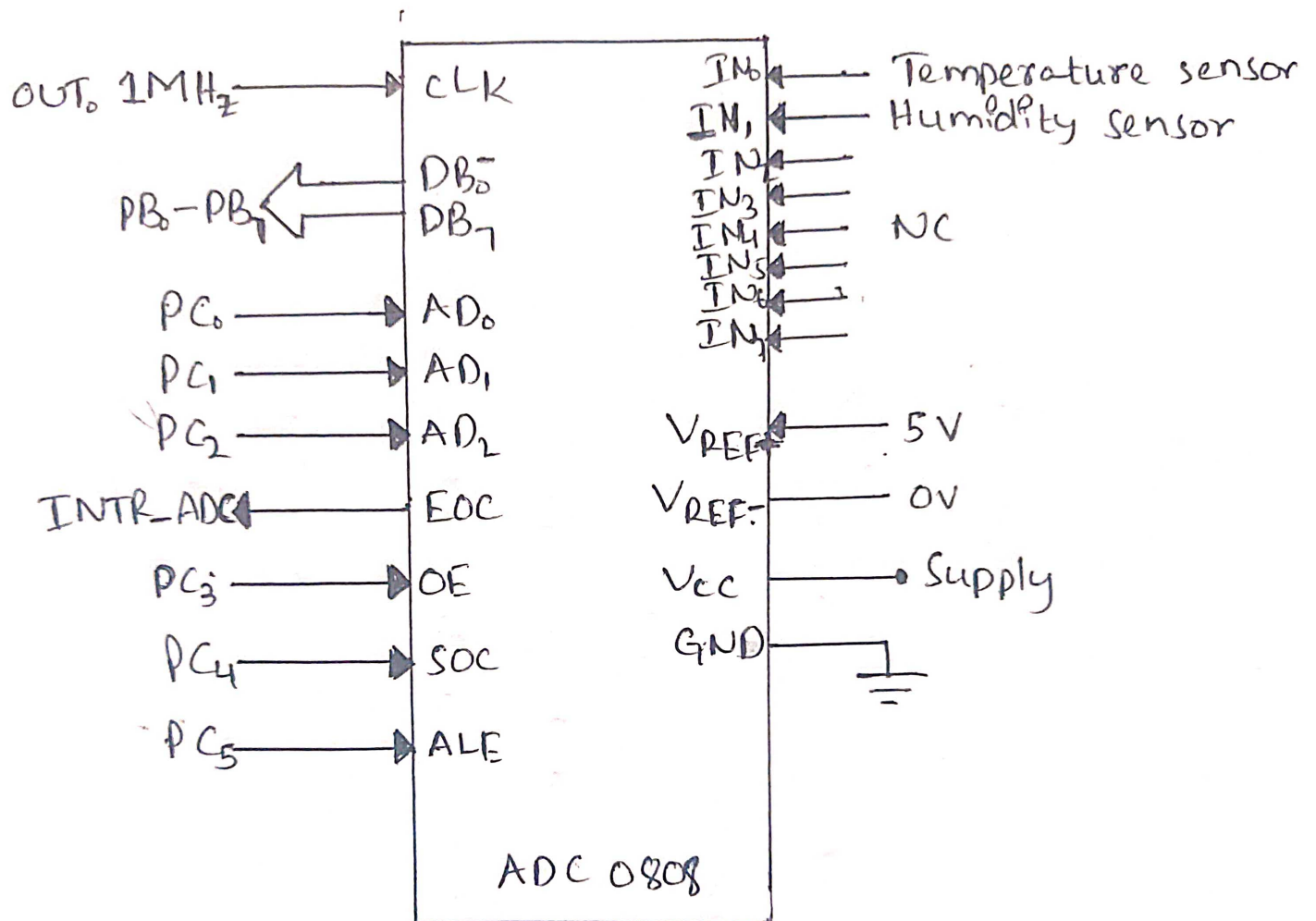


**LM016L**

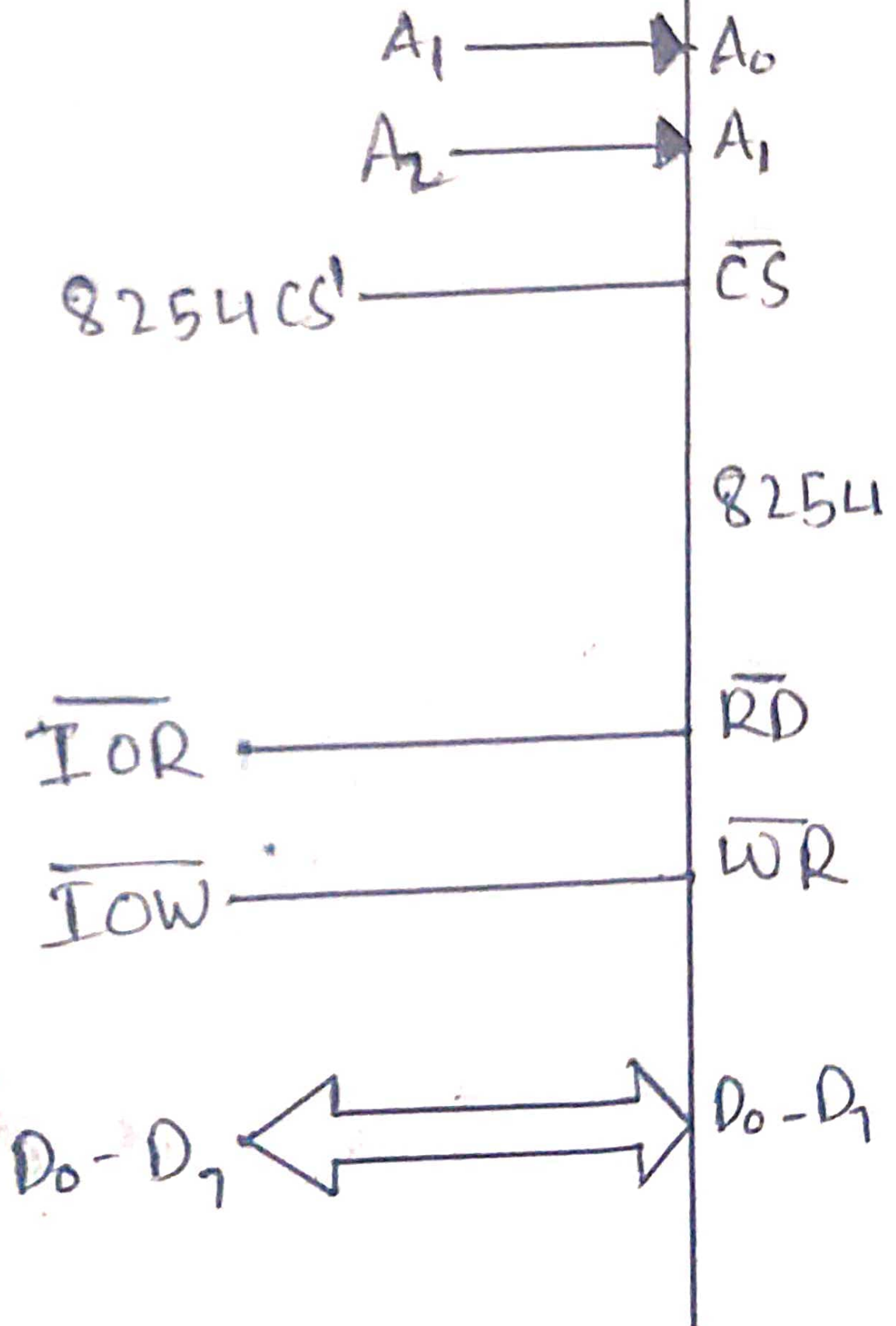
(16x2 ALPHANUMERIC LCD DISPLAY)  
LCD Interfacing

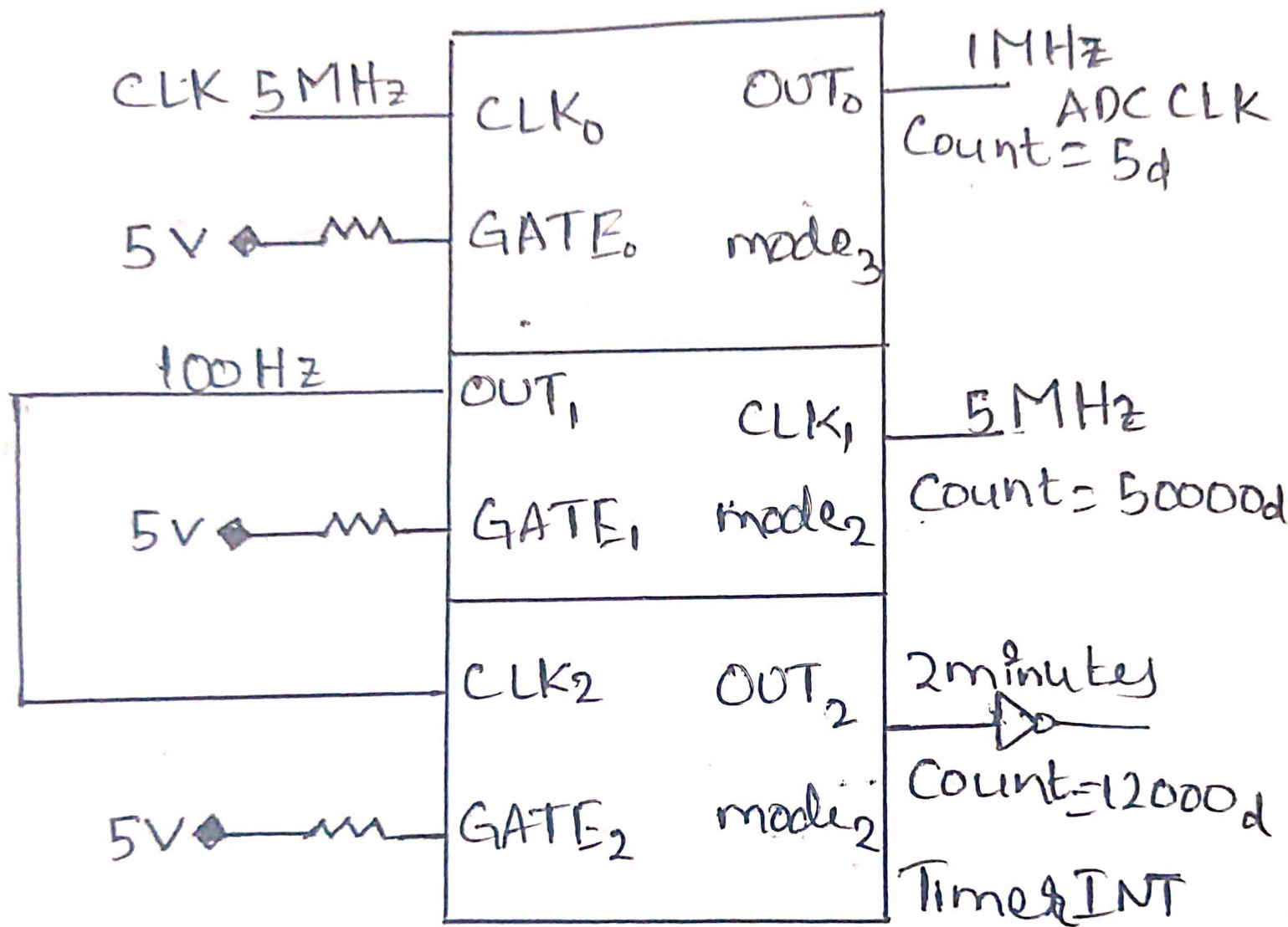
# 8255-2 Interface to processor





# 8254 Interface to the processor





8254

