#	Requirements	Priority
1	Color code for specific indication	High
2	Small in size	High
3	Good aesthetic view	Low

# Task Name	Resource	Duration (hr)	Start	Finish	% Complete	Comments
1.0 User Stories						
1.2 User Story workshops	Project Manager	0.3	8-Jan-21	8-Jan-21	100%	
1.3 User Stories Walk-through and sign off	Project Manager	0.3	0-Jan-21			
2.0 Product Backlog						
2.1 Create Product Backlog	Project Manager	0.3				
2.2 Story Estimation	Project Team	0.2	8-Jan-21	8-Jan-21	100%	
2.3 Prioritize	Project Manager	0.1				
3.0 Sprint-0						
3.1 Create project timeline	Project Manager	0.1		8-Jan-21	100%	
3.2 Draft resource plan	Project Manager	0.1	8-Jan-21			
3.3 Plan project budget	Project Manager	0.1				
4.0 Sprint - 1 (Circuit Design)						
4.1 Circuit Design On Paper	Project Team	0.3		9-Jan-21	100%	
4.2 Circuit Design Simulation	Project Team	0.3	9-Jan-21			
4.3 Circuit Design on Bread-Board	Project Team	0.1				
5.0   Sprint - 2 (PCB Design)						
5.1 Design PCB on CAD Software (Protious)	Project Team	0.1	9-Jan-21	9-Jan-21	100%	
5.2 Actual PCB making	Project Team	1	9-Jan-21			
6.0   Sprint - 3 (Testing and Review )						
6.1 Testing according to user stories	Project Team	0.2		10-Jan-21	100%	
6.2 Review Design	Project Team	0.1	10-Jan-21			
6.3 Deliverable or implementable prototype enclosure	Project Team	0				

Total Time 3.6

## **Product Backlog**

Details						Team				
Epic	Sprint	User Story Role	User Story Name	Story Details	Acceptance Test Criteria	Tasks		Analyst / Prep work		
Tester Insertion	2		Connects USB Tester to hardware	Gets Green light from tester as output reading	Green LED glows which indicates power port of USB is OK.	Remove the USB Tester from socket	none	1		
Tester Insertion	2	USB port has no defect	Connects USB Tester to hardware	Gets Red light from tester as output reading	of USB has some defect.	Remove the USB Tester from socket     Check ether Blue light is also glowing .	Blue light glow indicates that the data line has some defect.	2		

Milestones/Deliverables	Sprint 1(8 Jan 2021)	Sprint2(9 Jan 2021)	Sprint3(10 Jan 2021)
First Half Milestones		PCB Design on Software:	Component Mounting
Second Half Milestones	Circuit Design:	PCB Design in real hardware	Testing Output product
Third Half Milestone	Circuit Design :	Testing PCB Design	Final working product

Sprint	Description	Task	Sub-Task	Assigned to	Estimate Time (hr)	Time Taken (hr)	Status	Remarks
0	Product Backlog	1. Project Study	0.1.1. Literature Survey	Siddhant Joshi			Complete	
			0.1.2. Changes in Existing System	Siddhant Joshi			Complete	
			0.1.3. Analyzing Drawbacks	Siddhant Joshi			Complete	
			0.1.4. Existing System Present	Siddhant Joshi			Complete	
			0.1.5. Virtual System Implementation	Siddhant Joshi			Complete	
		2. Understanding Industrial Methods	0.2.1. Agile Methodology Study	Siddhant Joshi	0.3	0.3	Complete	
			0.2.1. Scrum Methodology	Siddhant Joshi			Complete	
		3. User Story	0.3.1. Forming User Stories	Siddhant Joshi			Complete	
		4. Sprint Planning	0.3.2. System Use Cases	Siddhant Joshi			Complete	
			0.4.1. Defining Sprints	Siddhant Joshi			Complete	
			0.4.2. Assigning Tasks	Siddhant Joshi			Complete	
1	Circuit Design	1. Components Study	1.1.0 Connection of USB	Siddhant Joshi			Complete	
			1.1.1 Basic of Electronics Resistor	Siddhant Joshi			Complete	
			1.1.2 Basic of Electronics LED	Siddhant Joshi			Complete	
		2. Software Study	1.2.0 Learn Protious Schematic Diagram design	Siddhant Joshi	1.5	1.5	Complete	
			1.2.1 Learn Protious PCB design	Siddhant Joshi	1.5	1.5	Complete	
		3. Design Circuit	1.3.0 Design Circuit on Paper	Siddhant Joshi			Complete	
			1.3.1 Design Circuit on protious	Siddhant Joshi			Complete	
			1.3.2 Design Circuit on Bread Board	Siddhant Joshi			Complete	
2	PCB Design	1. Design PCB	2.1.0 Design PCB on Protious	Siddhant Joshi			Complete	
			2.1.1 Move PCB from Paper to Epoxy sheet with plated copper	Siddhant Joshi	1.1	1.1	Complete	
		2. Testing PCB	2.2.0 Testing PCB	Siddhant Joshi			Complete	
3	Testing and Review	Testing according to user stories	3.1.0 PCB Testing in actual environment	Siddhant Joshi			Complete	
			3.1.1 Flaws Finding and solving	Siddhant Joshi	30	30	Complete	
			3.1.2 Enclosure	Siddhant Joshi			Complete	