



FPGA Computing Systems : Background Knowledge & Introductory Materials

Quiz Answers

Week 2 : LUT and CLB

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1. Which of the following sequences correctly orders the steps to configure a LUT?

Exploit the truth Table - Configure a circuit - Compile the LUT - Test the LUT

Define the problem - Exploit all its possible solutions - Compile the LUT

Configure a circuit - Exploit the truth table - Compile the LUT

Define the problem - Configure a circuit - Exploit the truth table - Compile the LUT

2. What information the CLB frame carries?

LUT Information + Information of how to set the muxs in the CLB slices

LUT Information

Information of how to set the muxes in the CLB slices

Information regarding only the truth table of the circuit
