coursera

FPGA Computing Systems :
Background Knowledge & Introductory Materials

Quiz Answers

Week 3 : Functionalities and their Implementations

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1. In which languages is it possible to describe a core?

C
Correct
It's correct, we can describe it in C

VHDL/Verilog
Correct
It's correct, we can describe it in VHDL/Verilog

Intermediate representation (es. data flow graph)
Correct
It's correct, we can describe it by using an intermediate representation

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2. It is sufficient the pure functionality core implementation without communication infrastructures?

Yes

No
Correct
That's correct, it is not sufficient!