



# **FPGA Computing Systems : Background Knowledge & Introductory Materials**

## **Quiz Answers**

### **Week 1 : Reconfigurable Computing Module**

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1. Who provided the first definition of reconfigurable computing?

**Gerald Estrin**

**Correct**

**He provided the first definition in 1960**

Katrine Compton

Christophe Bobda

2. Which can be an unofficial, but still feasible, definition of reconfigurable computing architecture?

A computing architecture composed of multiple GPPs

A computing architecture composed of both a general-purpose processor and some GPUs

**A computing architecture composed of both a general-purpose processor and some reconfigurable hardware logic**

**Correct**

**This is the correct answer**

3. Which of the following statements are false

**Processor application design time is lower than ASIC design time**

**Correct**

**This is false**

Reconfigurable computing is a trade-off between ASIC and processors

**The main advantage is the lack of a standard computing model**

**Correct**

**The lack of standard computing model is a disadvantage, since it increases the required design time**

**ASIC fabrication costs are lower than the costs in reconfigurable computing**

**Correct**

**This is false, since ASIC are custom computational components which require a lot of time and expertise to be produced**



4. FPGAs have a program counter.

True

**False**

**Correct**

**That's correct FPGAs do not have a program counter**

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5. FPGAs are semiconductor devices containing:

**Programmable interconnects and programmable logic components and, in some cases, other components (e.g. BRAM)**

**Correct**

**That's the most complete answer**

**Programmable logic components**

**Correct**

**Correct, but not only!**

**Programmable interconnects and programmable logic components**

**Correct**

**Correct, but not only**

**Programmable interconnects**

**Correct**

**Correct, but not only!**

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