



# **FPGA Computing Systems : Background Knowledge & Introductory Materials**

## **Quiz Answers**

### **Week 2 : FPGA Configuration and Bitstream**

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**Name : Harsh Siddhapura**

**Degree : Bachelor of Technology**

**Dept. : Information & Communication Technology**

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1. The FPGAs configuration memory is

**an SRAM**

a DRAM

a ROM

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2. The FPGAs configuration is

Always lost on power off

**Lost on power off but could be downloaded from a ROM on power-on**

Persistent

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3. How can an FPGA be reconfigured?

**With full or partial bitstream**

Only with partial bitstream

Only with a full bitstream

Neither of the other answers

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4. The bitstream file is composed by:

header, dummy words and fpga configuration

**Header, dummy words, sync word and fpga configuration**

Header and effective fpga configuration

Header, sync words and fpga configuration

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5. The Frame Address Register is:

**Where the position of the first configuration frame is written**

Used to check the correct transferring of the configuration data

Written with a 32 bit word unique for every device

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