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FPGA Computing Systems: Background Knowledge & Introductory Materials

Quiz Answers

Week 2: Physical Design

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Quiz Answers



A group of 6 programmable switches

None of the others

A group of 6 MUX

A group of 6 transistors

2. What is done in the Technology Mapping phase?

Find the best technology implementation for each element using a technology library

Map the FPGA resources

Synthetize the HDL specification

None of the others