



# **FPGA Computing Systems : Background Knowledge & Introductory Materials**

## **Quiz Answers**

### **Week 1 : FPGA and Reconfiguration**

-----  
**Name : Harsh Siddhapura**

**Degree : Bachelor of Technology**

**Dept. : Information & Communication Technology**  
-----



1. A key property of FPGAs is being capable of reconfiguration :

just one time

Just few times

**An infinite number of times**

**Correct**

**That's correct... at the end of the day, it is not 100% correct saying an infinite number of time, but we can assume this as a good approximation of the truth**

---

2. In run time reconfiguration it's necessary to stop the computation to reconfigure the chip.

True

False

**It depends**

**Correct**

**That is correct, it depend on the technology we are using**

---

3. An FPGA cannot be partially reconfigured

False

True

**It depends**

**Correct**

**That is the correct answer, it does depend on the technology we are using**

---