```
1 d A = clCreateBuffer(clContext,CL MEM READ WRITE,size,NULL,&clStatus);
                                                                                               2 d B = clCreateBuffer(clContext,CL MEM READ WRITE,size,NULL,&clStatus);
  kernel void vadd( global float* input1,
                                                                                               3 d C = clCreateBuffer(clContext,CL MEM READ WRITE,size,NULL,&clStatus);
 global float* input2, global float* output,
 int n)
                                                                                               4 /*H2D Copy */
                                         COMMAND QUEUE
                                                                                               5 clEngueueWriteBuffer(clCommandQueue,d A,CL FALSE,0,size,h A,0,NULL,&ev1);
  int i = get global id(0);
                                                                                               6 clEnqueueWriteBuffer(clCommandQueue,d B,CL FALSE,0,size,h B,0,&ev1,&ev2);
  if(i<n)
                                                                 INPUT BUFFER 1
                                               WRITE
   output[i] = input1[i]+input2[i];
                                                                 INPUT BUFFER 2
                                               WRITE
                                                                     KERNEL
                                                                     BINARY
                                               EXECUTE
Processing
Element
                                                                OUTPUT BUFFER
                                                READ
                                                                                               15 /*Work item Processing*/
     Compute Unit
                       OpenCL Device
                                                                                               19 clWaitForEvents(1,&ev4)
```

- 7 /* Setting Kernel Parameters */ 8 int tx = 32:
 - 9 size t block[3] = $\{tx,1,1\}$; 10 size t grid[3] = {num elements,1,1};
 - 11 clSetKernelArg(clKernel,0,sizeof(cl mem),(void*)&d A); 12 clSetKernelArg(clKernel,1,sizeof(cl mem),(void*)&d B);
 - 13 clSetKernelArg(clKernel,2,sizeof(cl mem),(void*)&d C);
 - 14 clSetKernelArg(clKernel,4,sizeof(int),(void*)&num elements);
 - 16 clEnqueueNDRangeKernel(clCommandQueue,clKernel,2,NULL,grid,block,0,&ev2,&ev3);
- 17 /*D2H Copy*/
 - 18 clEnqueueReadBuffer(clCommandQueue,d C,CL TRUE,0,size,h C,0,&ev3,&ev4);