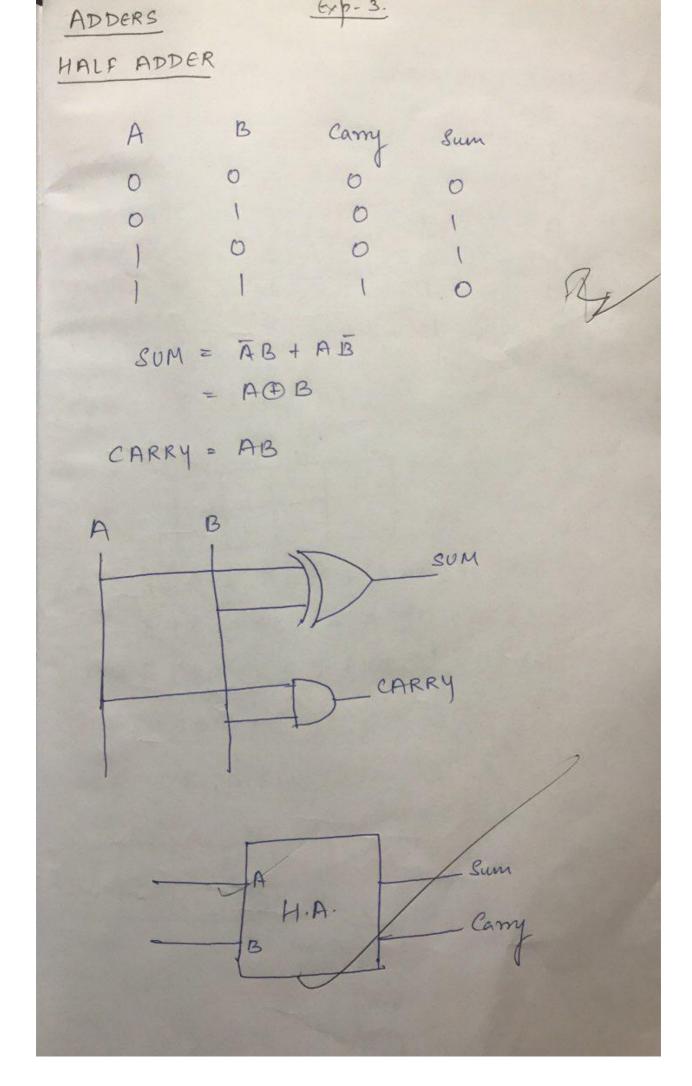
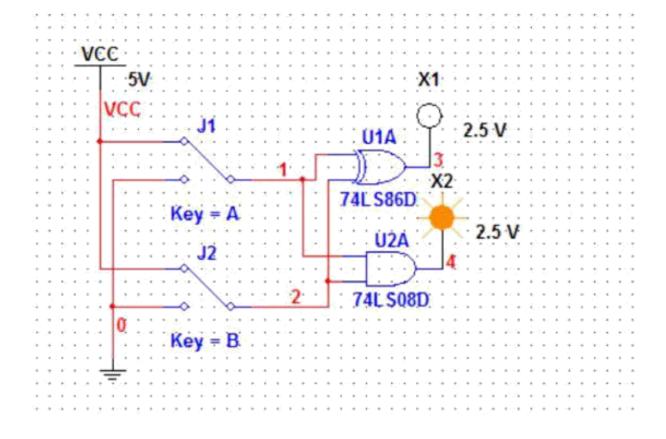
# Siddhi Singh

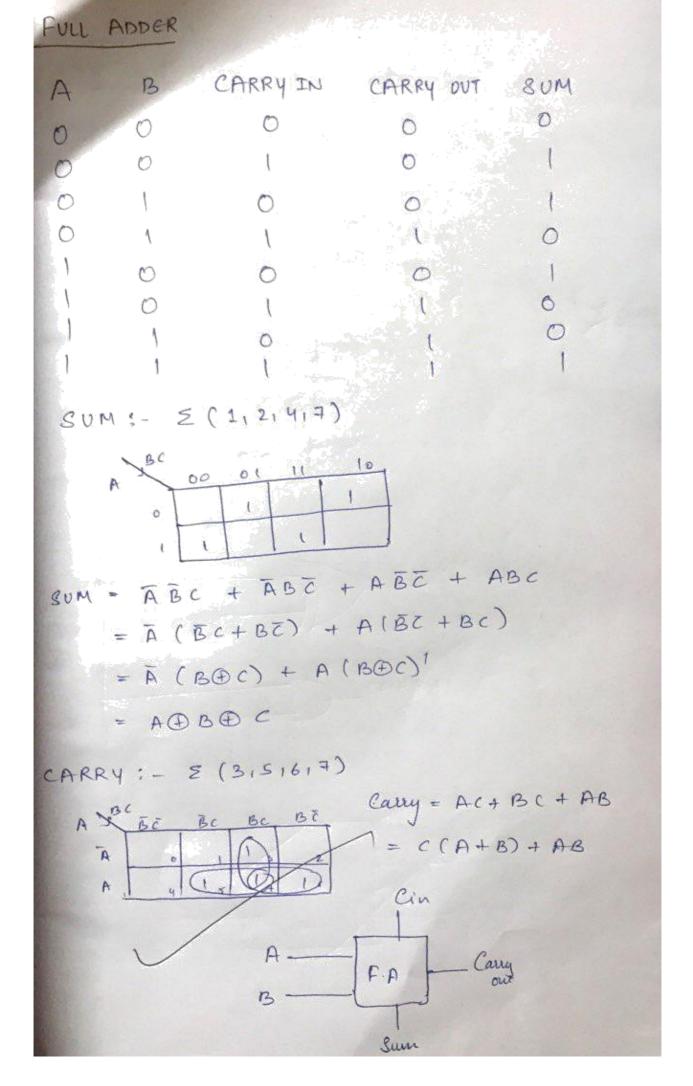
17BIT0028

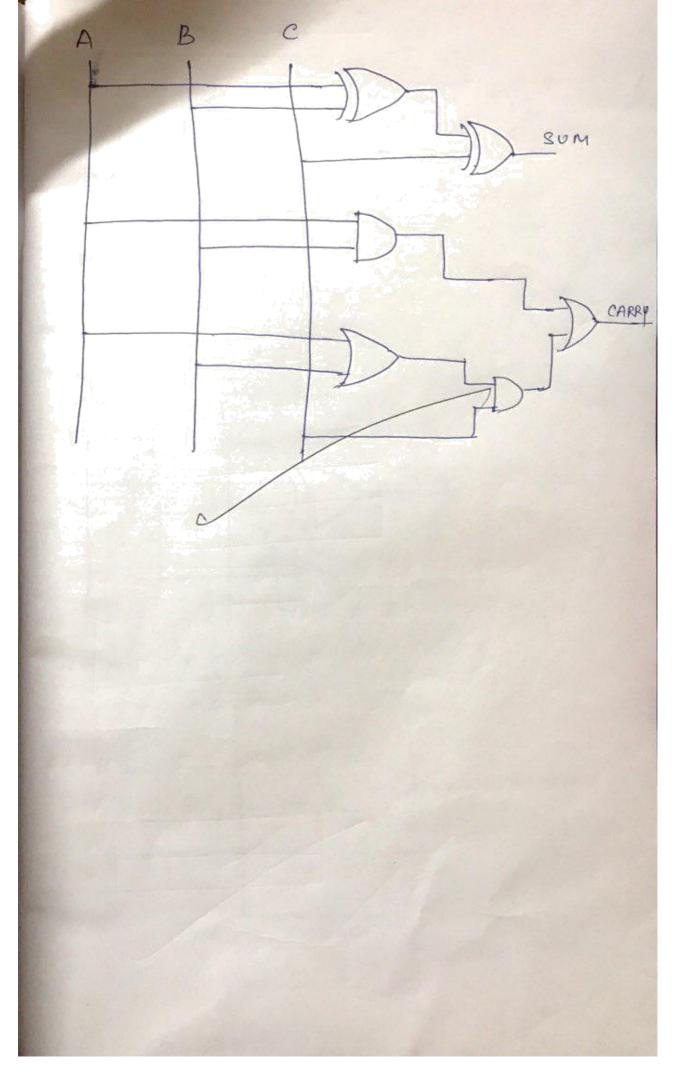
Lab 3



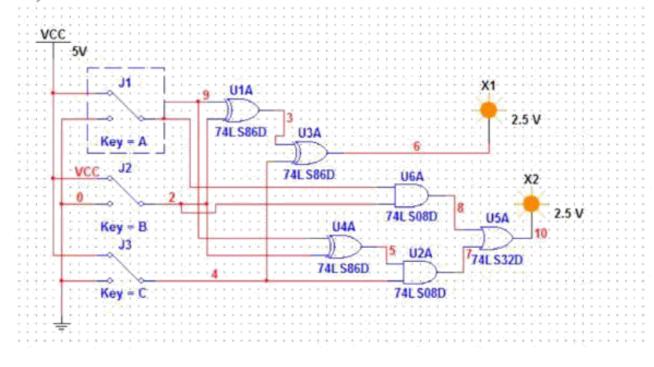
## 1) HALF ADDER

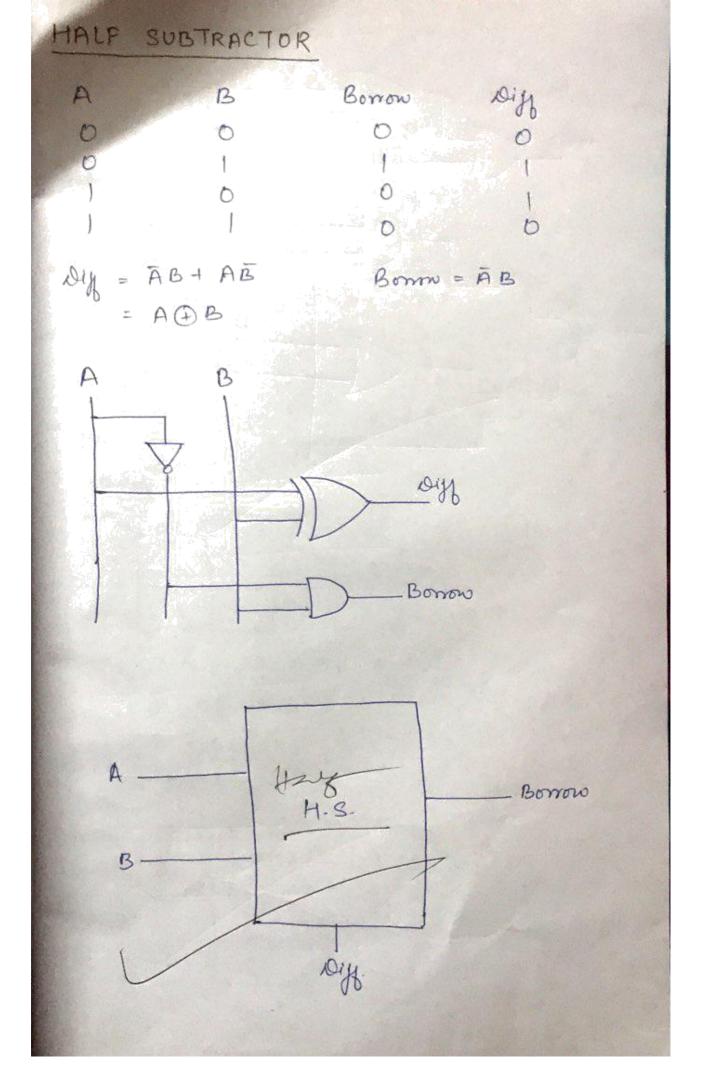




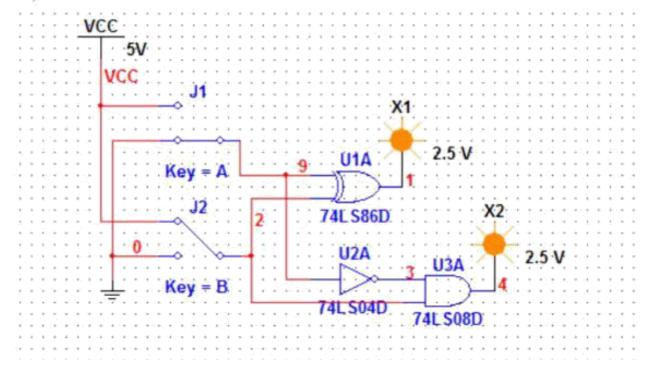


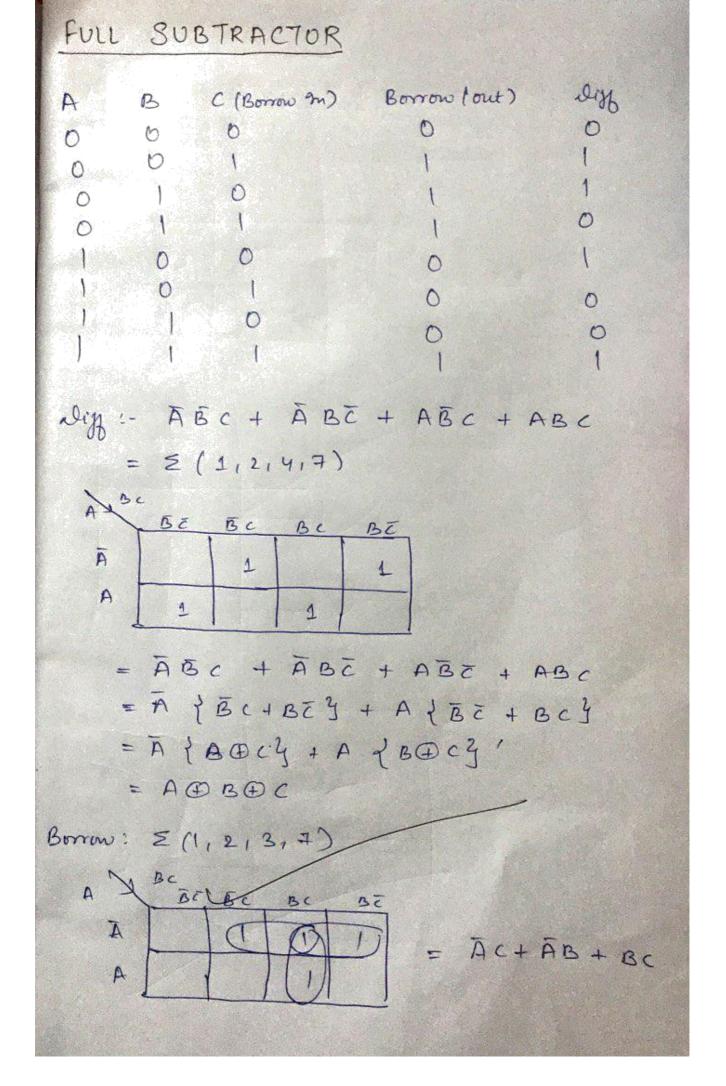
# 2) FULL ADDER

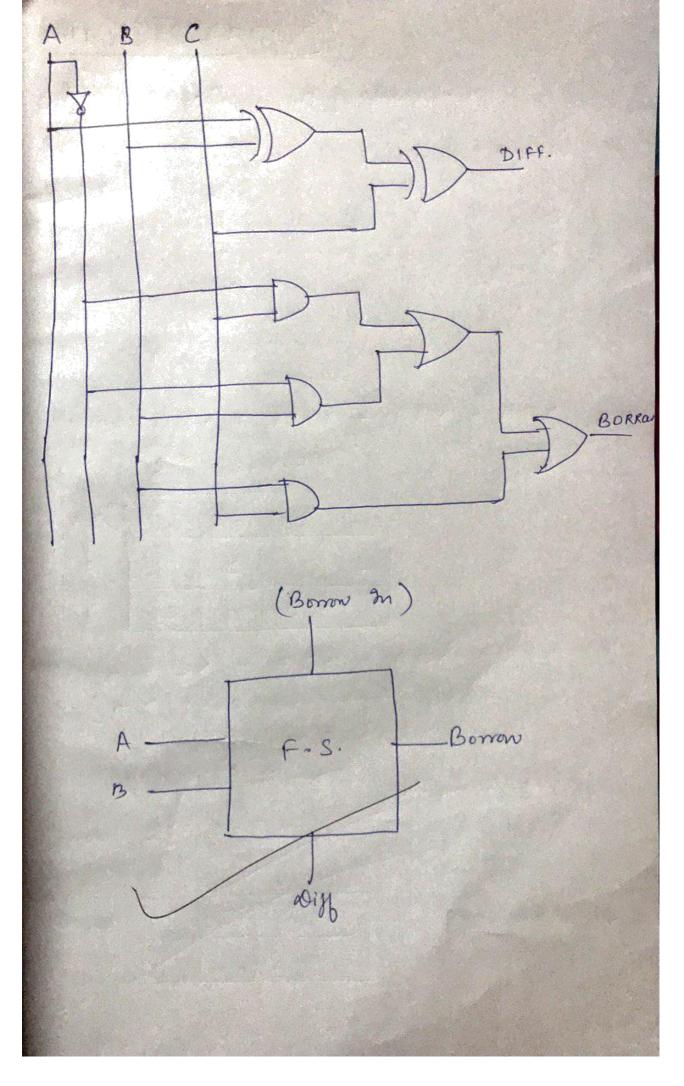




#### 3) HALF SUBTRACTOR







## 4) FULL SUBTRACTOR

