Siddharth Rajguru

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Summary

A motivated Electrical Engineer with experience in embedded systems, medical devices and open source cloud based development

Education

M.S., Electrical Engineering, Texas Tech University, TX. (GPA: 3.5) Dec. 2015

B.S., Electrical Engineering (Math. Minor), Texas Tech University, TX. Dec. 2013

Experience

(References on request)

Blackberry Corp., Redwood City, CA.: Software Developer Student (Enterprise Cloud IoT applications) May 2015 – Aug. 2015

- Implemented and managed VM configuration scripts using Ansible configuration management tool
- Created Node Package Management (npm) solutions for private publishing and package hosting
- Automated and created customized cloud VMs
- Implemented scripts in Jenkins for continuous integration and automated publishing
- Created functional test cases and tested NodeJS applications and databases
- Received experience building cloud infrastructure and designing for scalability in an Agile Scrum environment

Carl Zeiss Meditec, CA.: System/Software Intern (Cirrus HD-OCT product line research and development) Jun. 2014 – Oct. 2014

- Debugged C# applications, performed integration and performance testing
- Stress tested medical device hardware using over-clocking and benchmarking tools
- Took the lead to provide a product workflow strategy for optimizing hardware performance and cost on future products
- Received training in TFS (code control), Scrum, medical device lifecycle, software workflow and testing/monitoring tools

Texas Tech University, TX.: Tutor, Electrical Engineering Jan. 2014 – May 2015

- Create instructional videos and assist students on FPGAs and digital logic theory

Texas Tech University, TX.: IT support, IT Solutions Center Aug. 2013 – Nov. 2013

- Provided support on Microsoft products and network related issues

X-Fab, TX.: Electrical Engineering Intern Sep. 2012 – Dec. 2012

- Performed substation modelling for power factor and load flow analysis

Academics 2011-2015

Academic Projects

- MR Imaging: Image significance mapping for multi-coil image reconstruction (MATLAB, SENSE)
  - Created a Matlab algorithm for FOV based image cropping and significance map generation
  - Reconstructed images using Sine and Cosine function based significance maps
- Remote hardware accelerated service for image processing (UML, C#)
  - Lead the team and designed system architecture
  - Implemented OpenCV based image processing algorithms
- Embedded Functionality Tester (Verilog, ARM, Spartan3E)
  - An embedded tester with a command line interface
  - Test modules on a FPGA
- 32-Bit RISC behavioral model (Xilinx ISE and Verilog)
  - Developed chip-level designs for functional and storage modules on a standard RISC architecture

Tools and Language familiarity

OpenNebula	Ansible	npm	NodeJS	JavaScript	Sunstone	GRUNT	Shell Script	JSON
NGiNX	Couchdb	Jenkins	Git / Gerrit	Mocha	JIRA	MATLAB	Visual Studio	Linux

Self-started projects

- GPU and CPU benchmarking (Visual Studio 2010, CUDA)
- ARM SoC programming (ARM Beaglebone, Eclipse RSE, C/C++, Linux)

Activities and Awards

- Ray Butler Scholarship awarded by Texas Tech University 2015
- Russell Seacat Jr. Scholarship awarded by Texas Tech University 2014
- Volunteer judge at Coop 2012 (a robotics competition for kids)