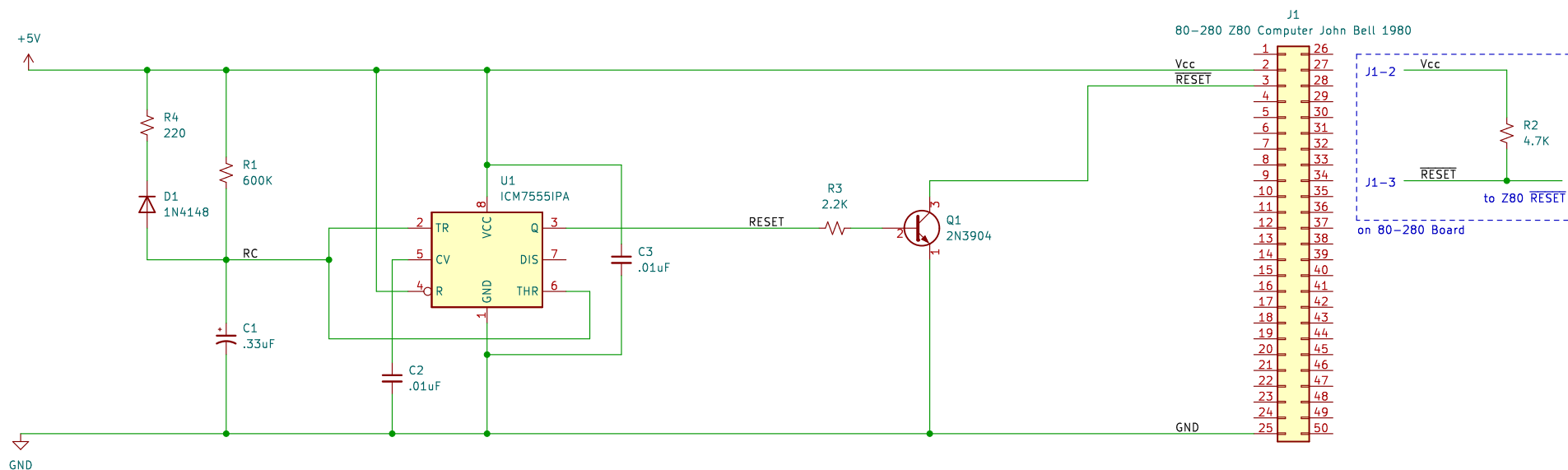Note: Power-on delay = $-\ln(1-0.67)*R*C = 1.108663RC = 1.108663*(600*10^3)*(0.33*10^{-6}) = 220$ msec
Note: 2N2222 may be substituted for Q1.

+5V

GND

R4
220

D1
1N4148

R1
600K

RC

C1
.33uF

C2
.01uF

U1
ICM7555IPA

2 TR
5 CV
4 R
8 VCC
1 GND
3 Q
7 DIS
6 THR

C3
.01uF

RESET

R3
2.2K

Q1
2N3904

J1
80-280 Z80 Computer John Bell 1980

Vcc
RESET

1 26
2 27
3 28
4 29
5 30
6 31
7 32
8 33
9 34
10 35
11 36
12 37
13 38
14 39
15 40
16 41
17 42
18 43
19 44
20 45
21 46
22 47
23 48
24 49
25 50

GND

J1-2 Vcc

R2
4.7K

J1-3 RESET

to Z80 RESET

on 80-280 Board

Sig Nin, Morgan Hill, CA
Ninbra Enterprises
Sheet: /
File: z-80_Computer_John_Bell_80-280-Reset.kicad_sch
Title: 80-280 Z80 Computer Power-on Reset
Size: A4   Date: 2022-08-11   Rev: 1.0
KiCad E.D.A. kicad (6.0.6)   Id: 1/1