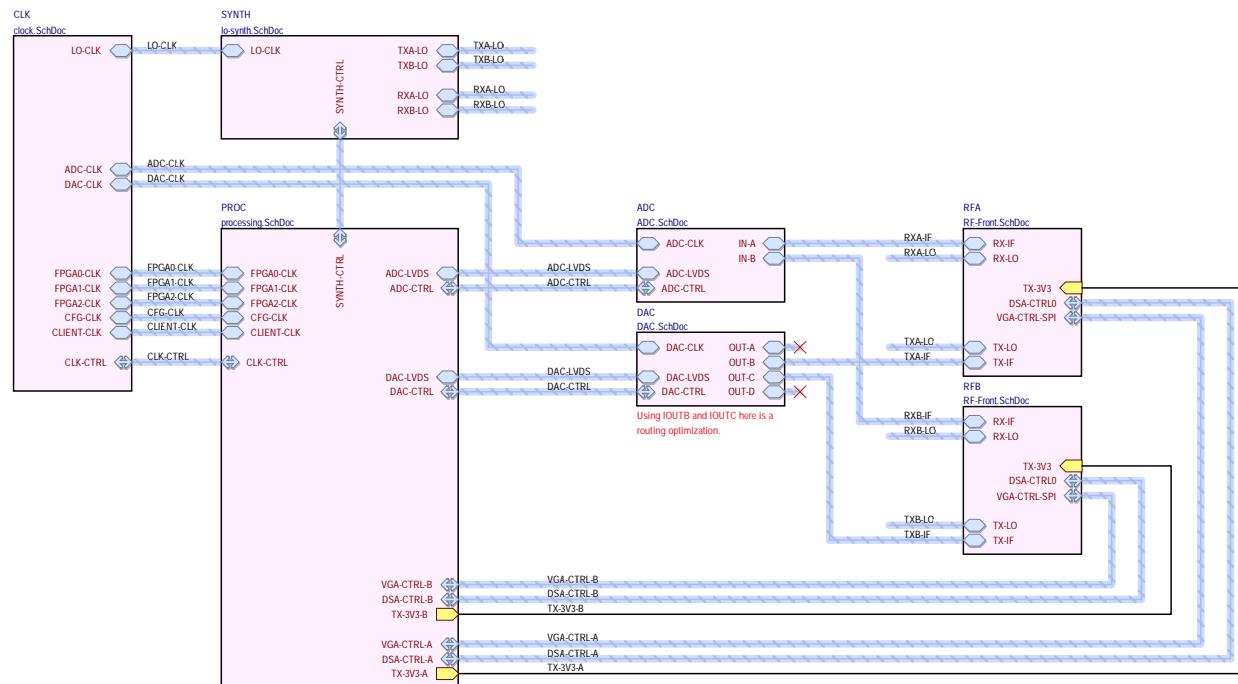


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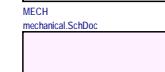
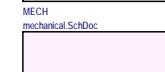
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Graviton - Top Sheet

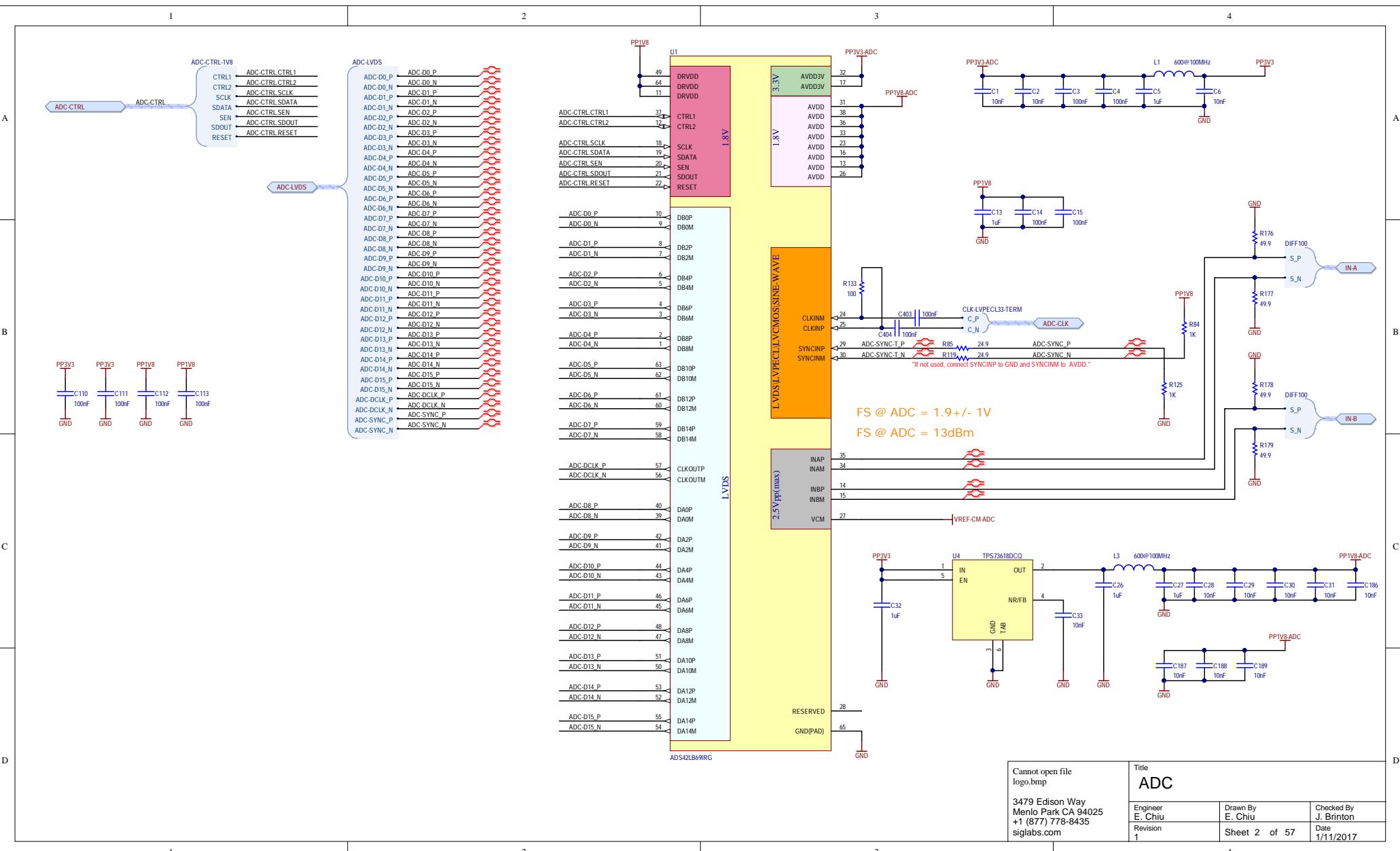
3479 Edison Way
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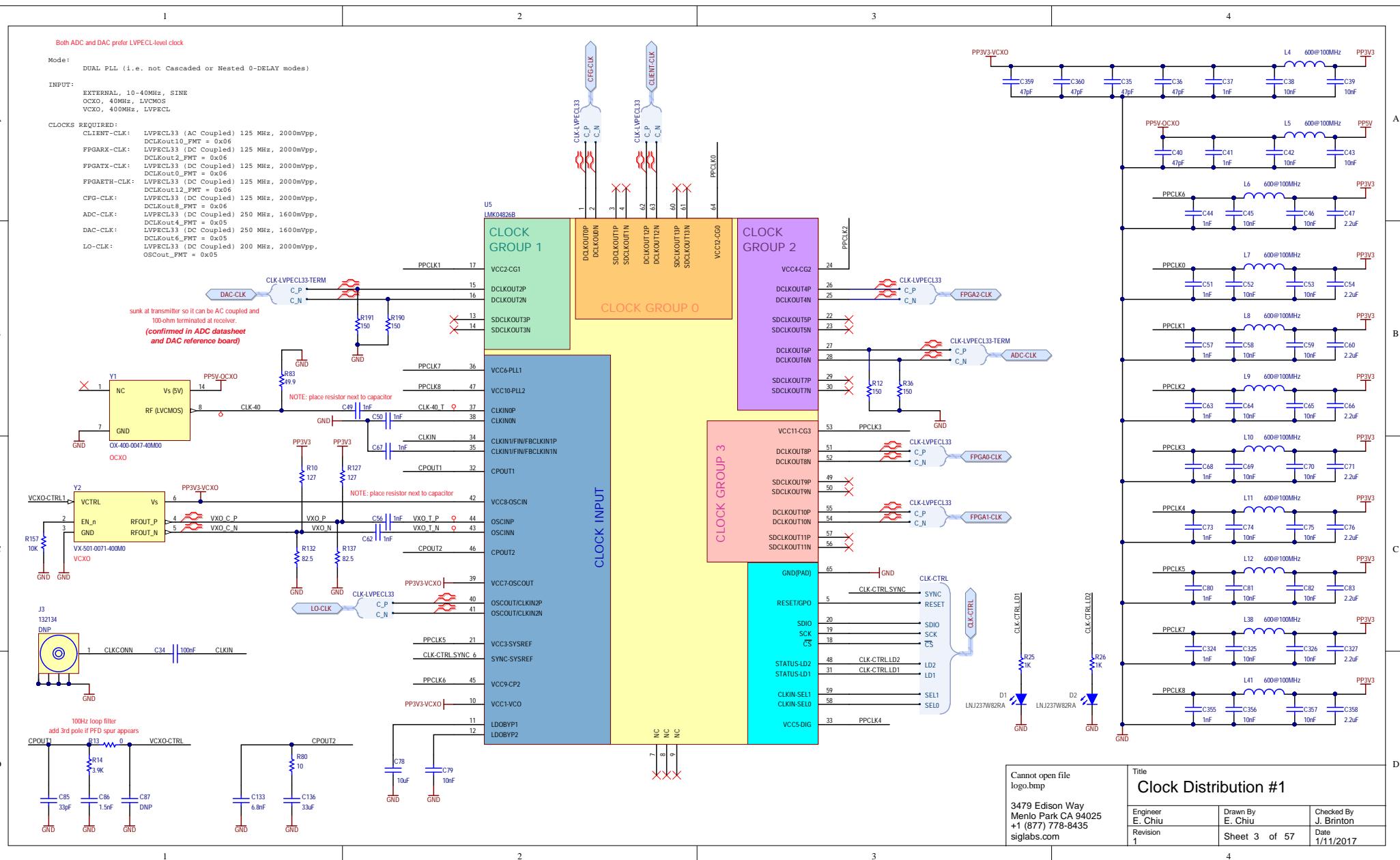
Title

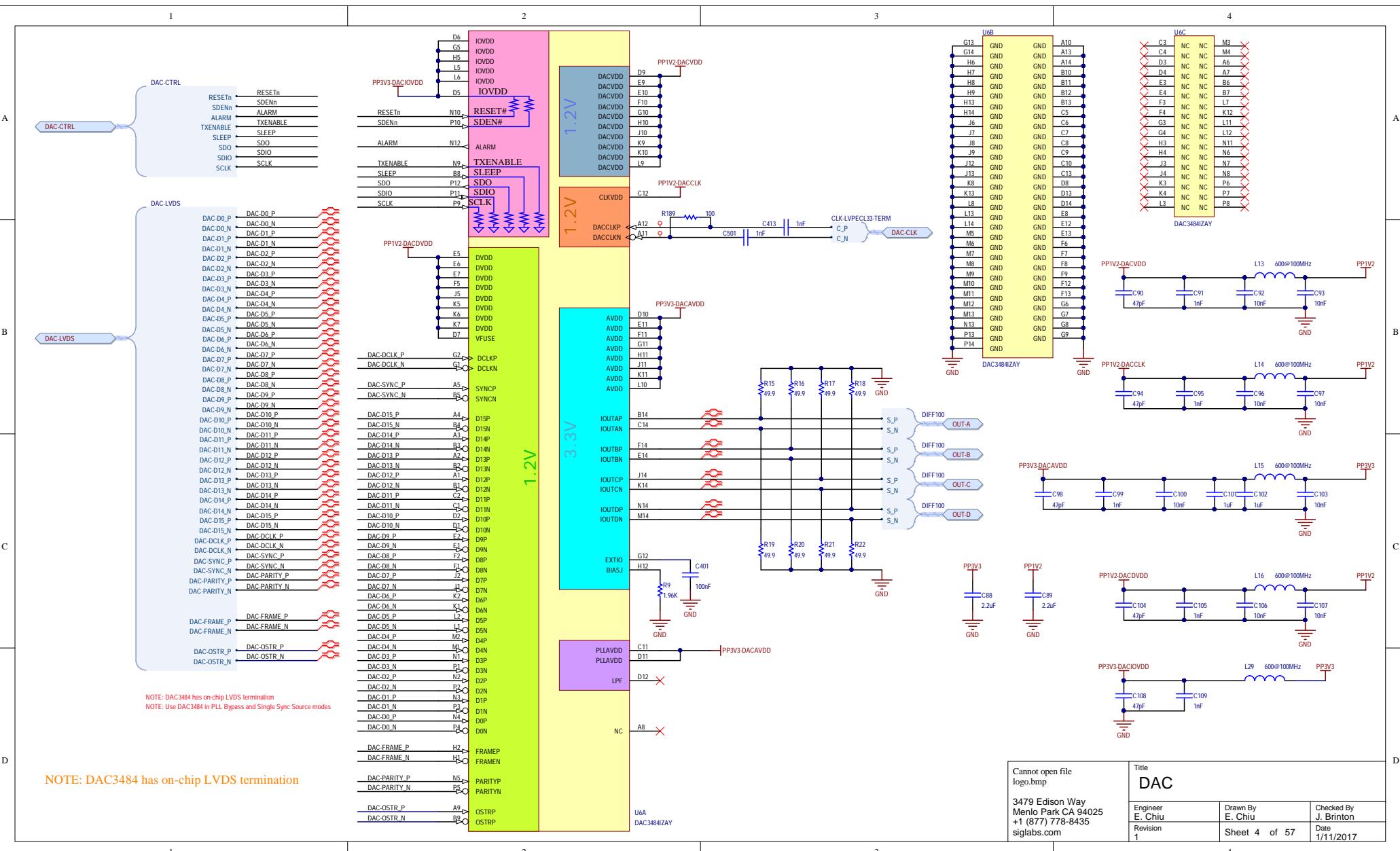
Sheet 1 of 57

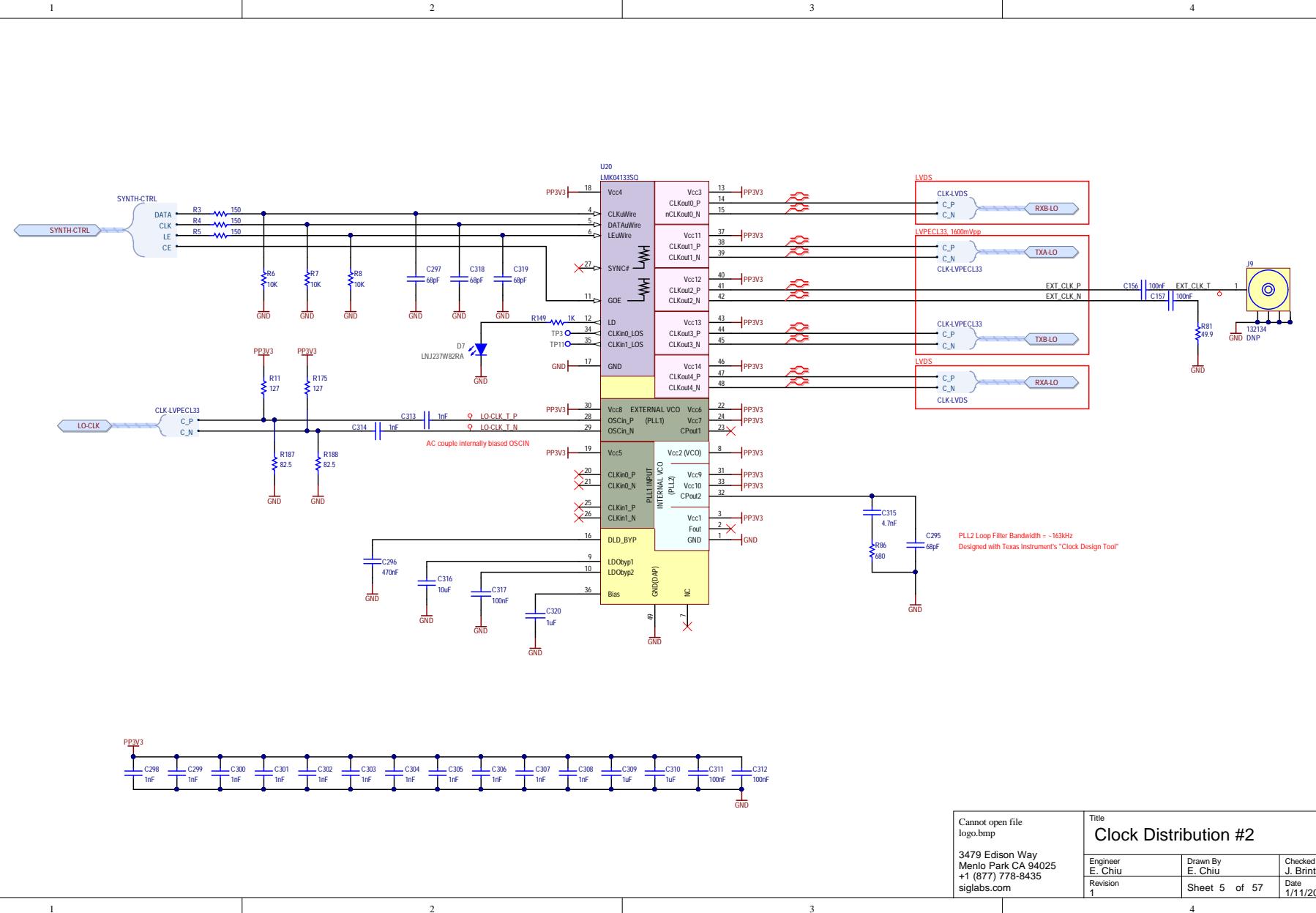
Date

1/11/2017

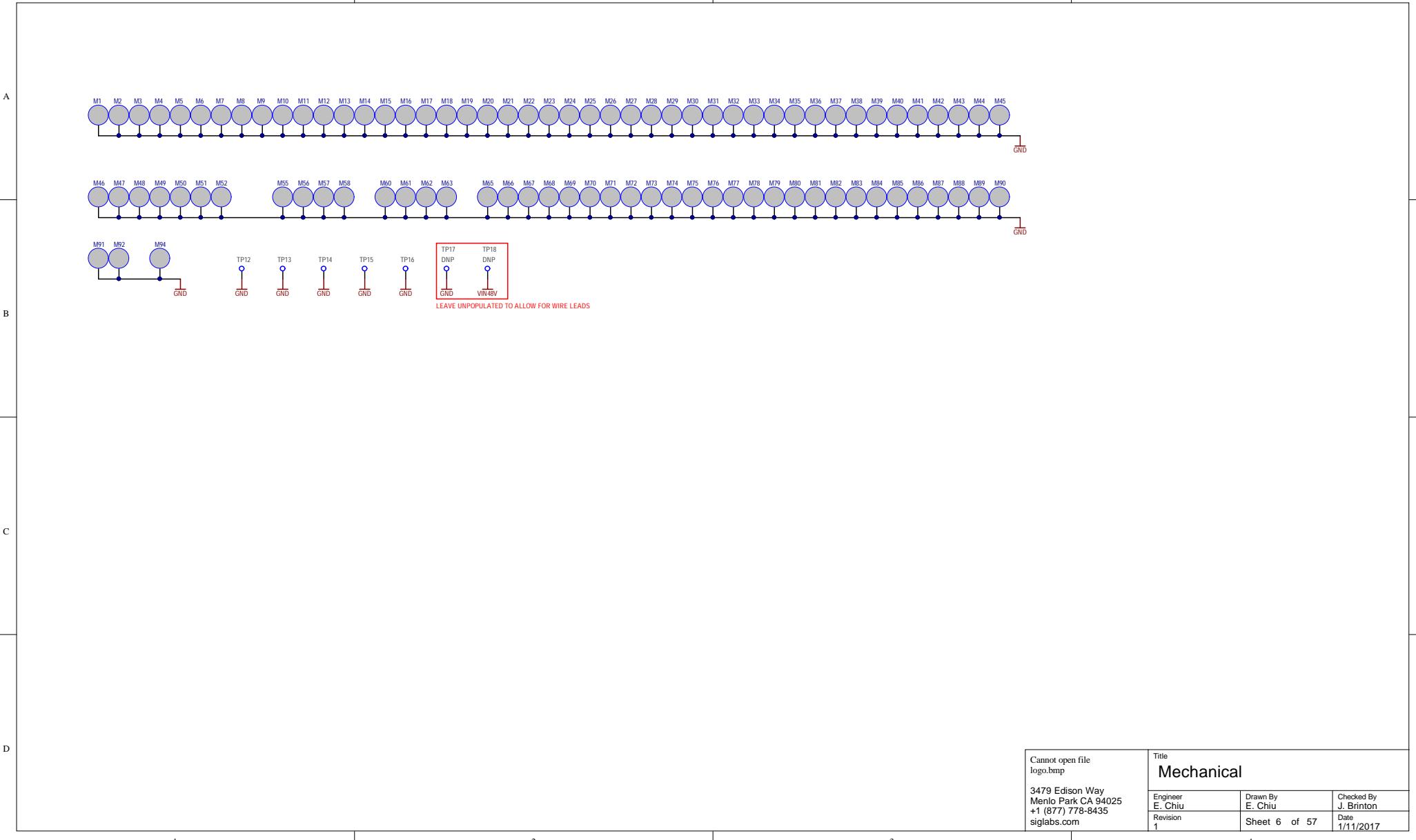




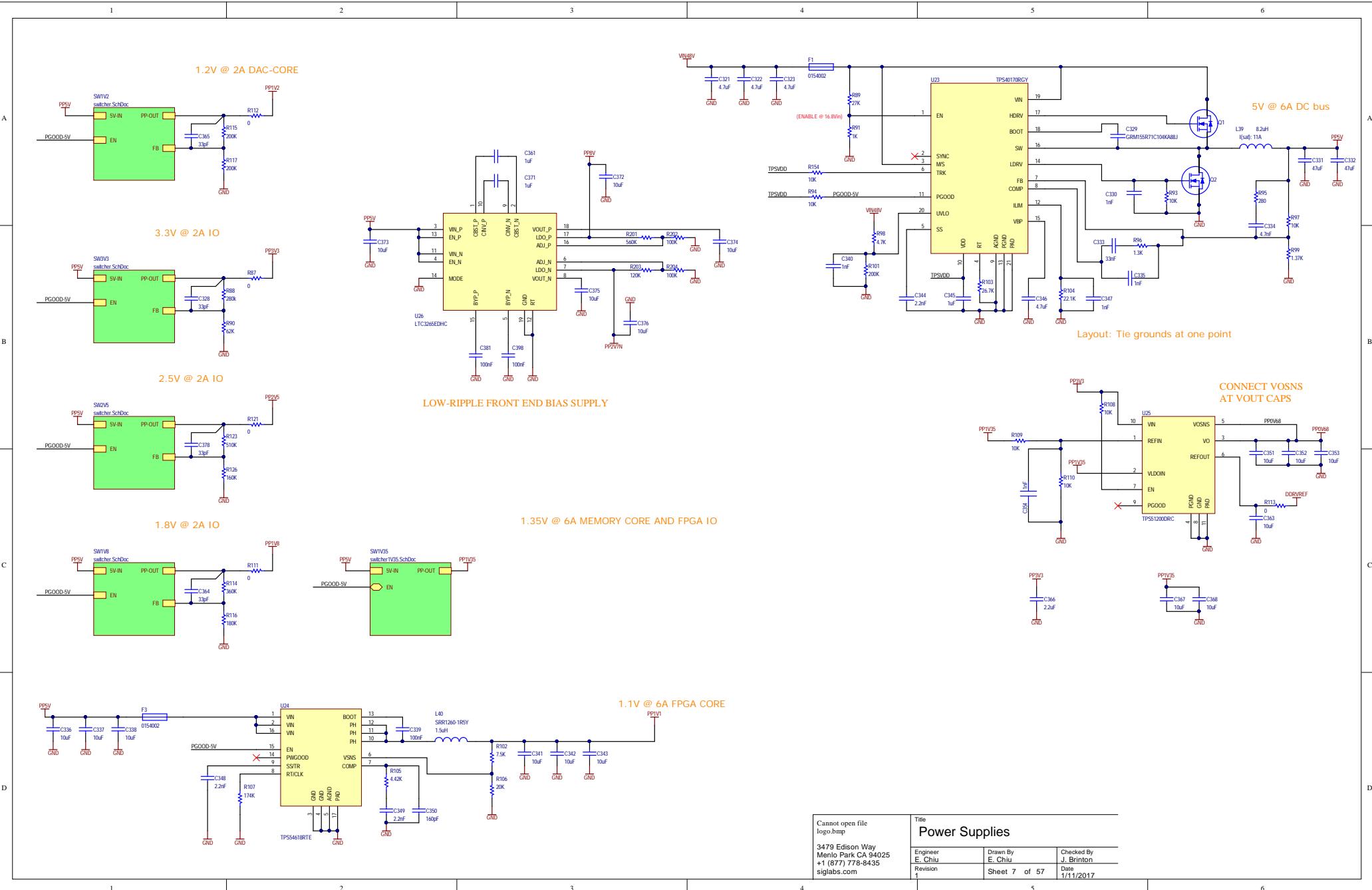




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Power Supplies

Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton
Revision 1	Sheet 7 of 57	Date 1/1/2017

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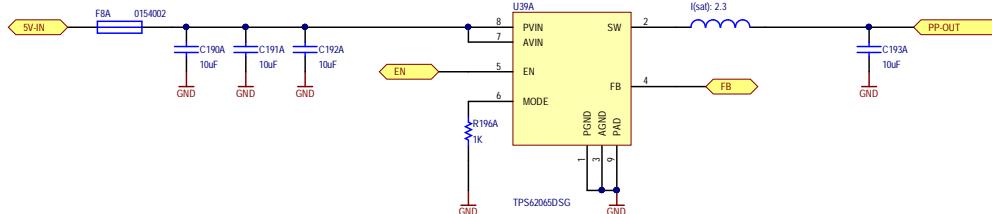
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Title
Switching Power Supply

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 8 of 57	Date 1/11/2017

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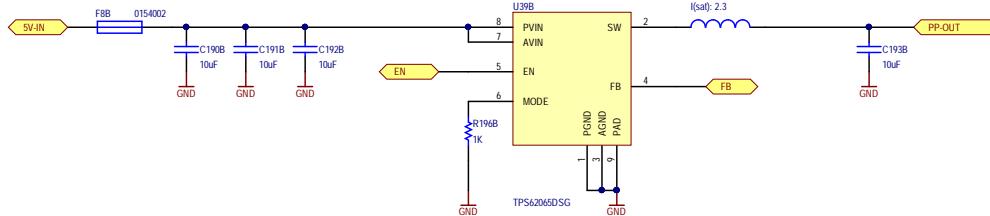
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Title
Switching Power Supply

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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 8 of 57	Date 1/11/2017

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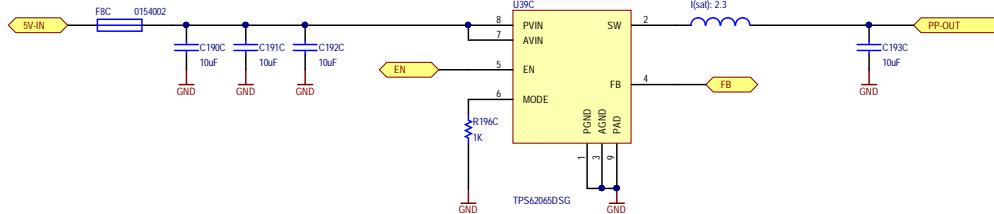
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Title
Switching Power Supply

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 8 of 57	Date 1/11/2017

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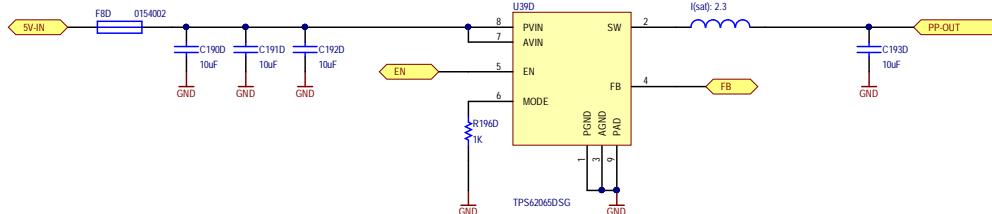
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Title
Switching Power Supply

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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 8 of 57	
		Date 1/11/2017

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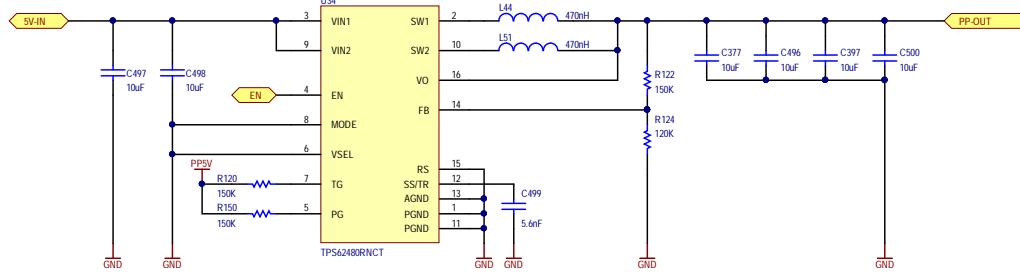
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1.35V Supply

Engineer

E. Chiu

Drawn By

E. Chiu

Checked By

J. Brinton

Revision

1

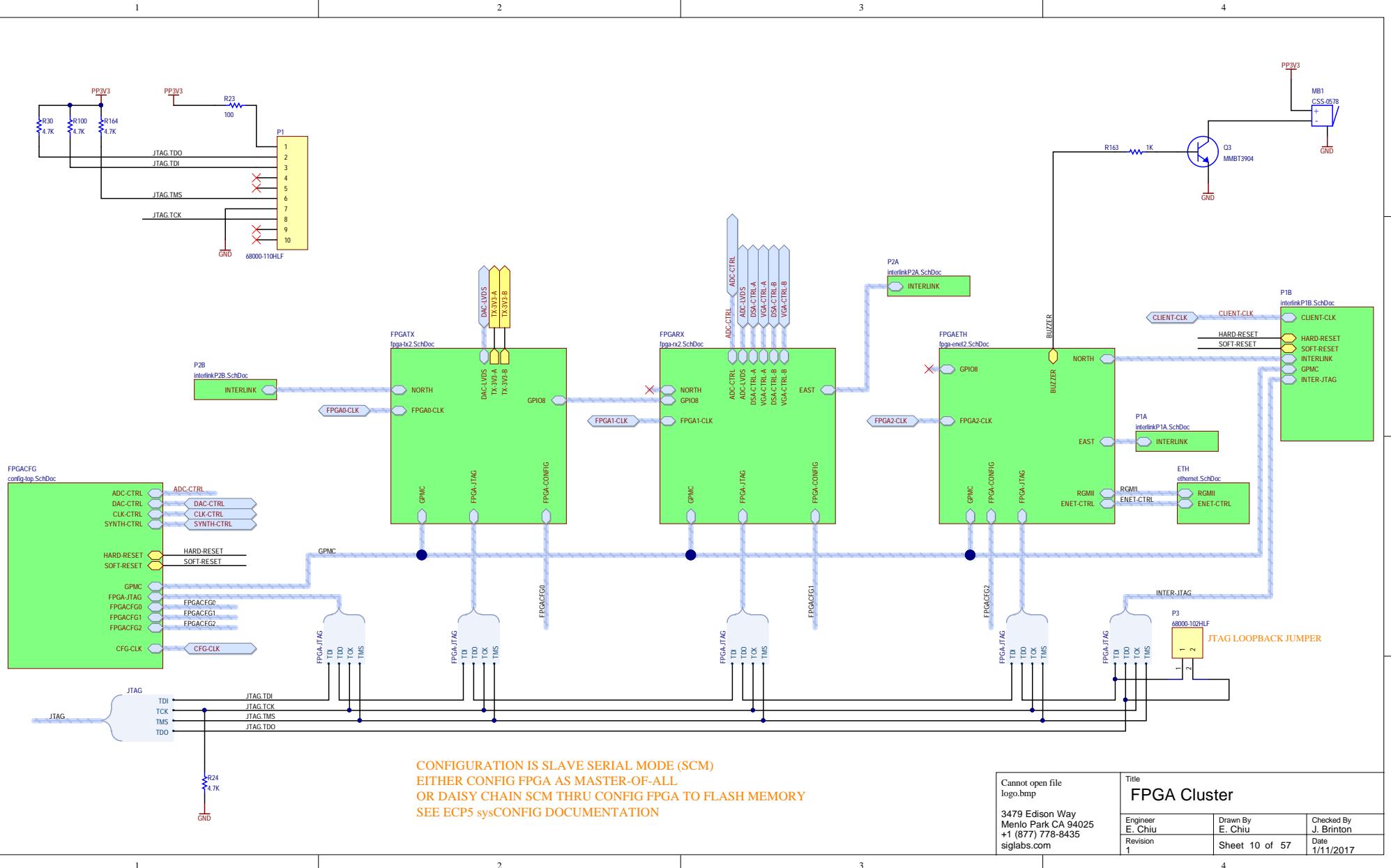
Sheet

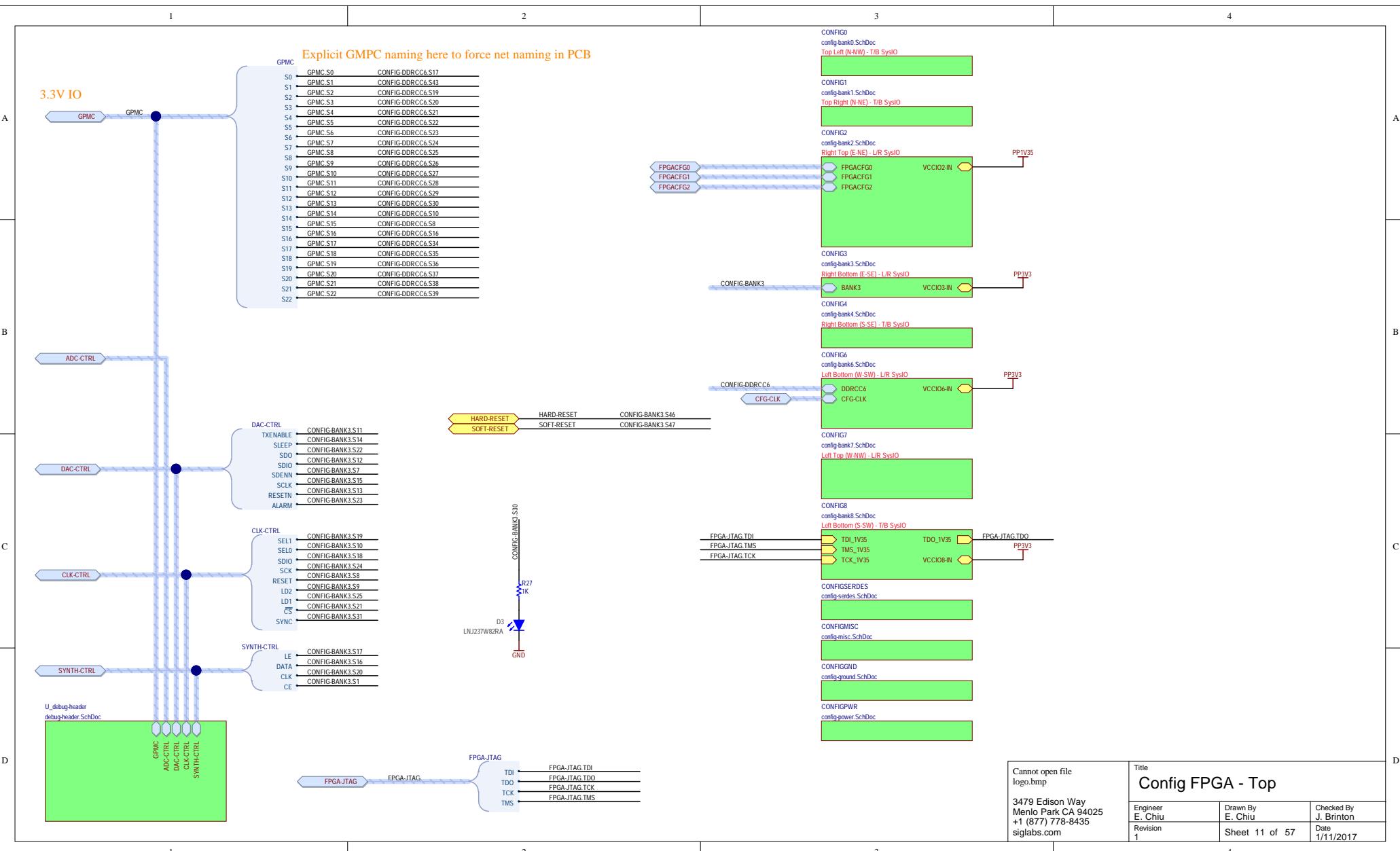
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of

57

Date
1/11/2017





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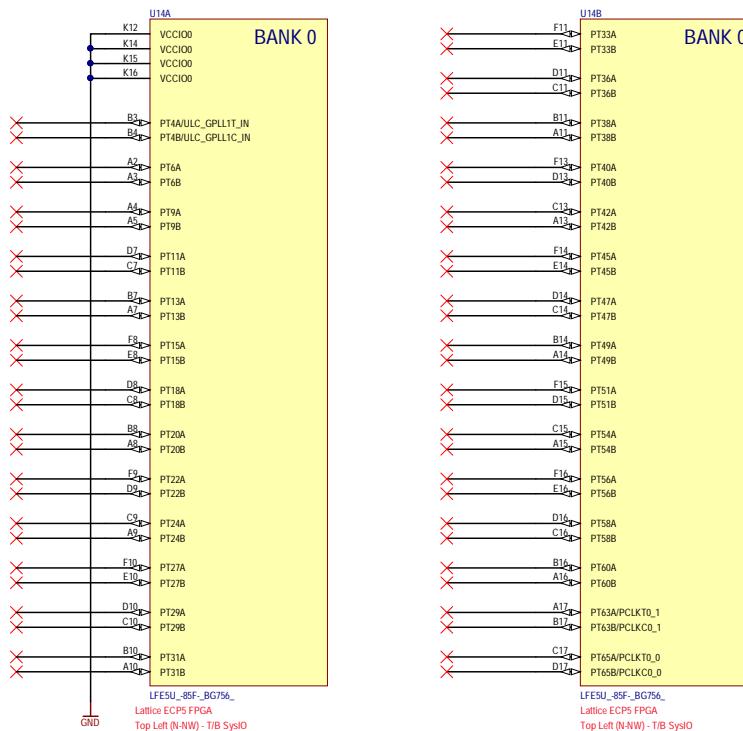
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Config FPGA - Bank 0		
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3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com	Engineer E. Chiu	Drawn By E. Chiu
Revision 1	Checked By J. Brinton	Date 1/11/2017
	Sheet 12 of 57	

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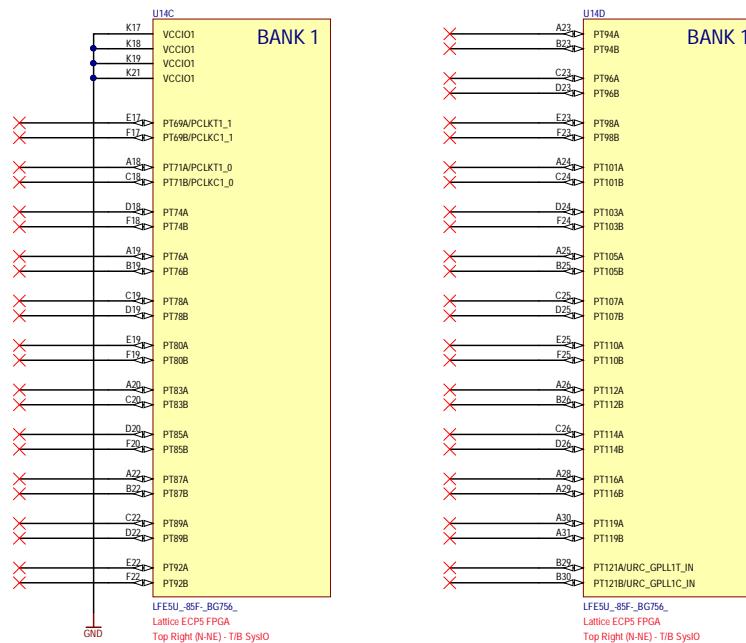
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Title Config FPGA - Bank 1

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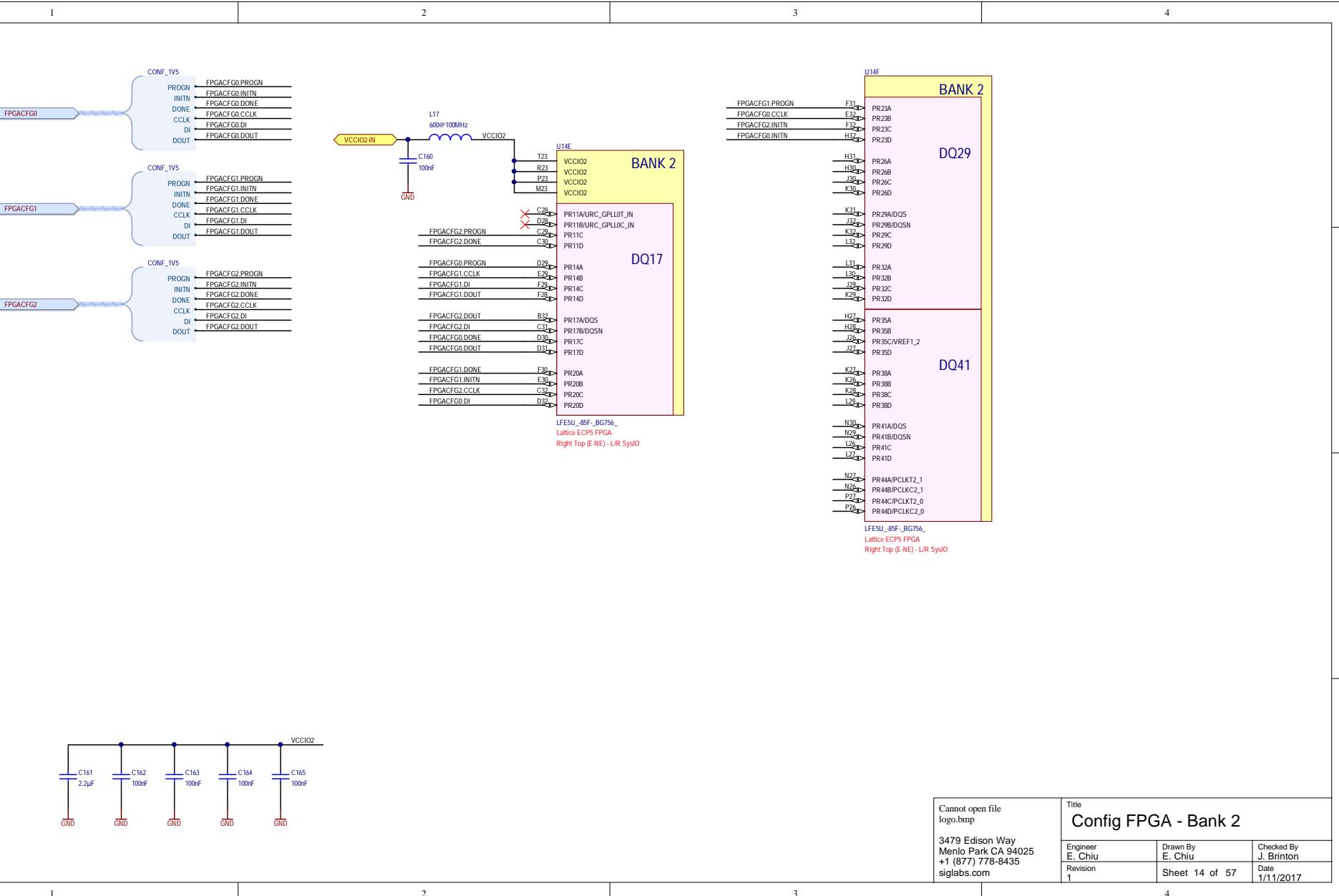
Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton
Revision 1	Sheet 13 of 57	Date 1/11/2017

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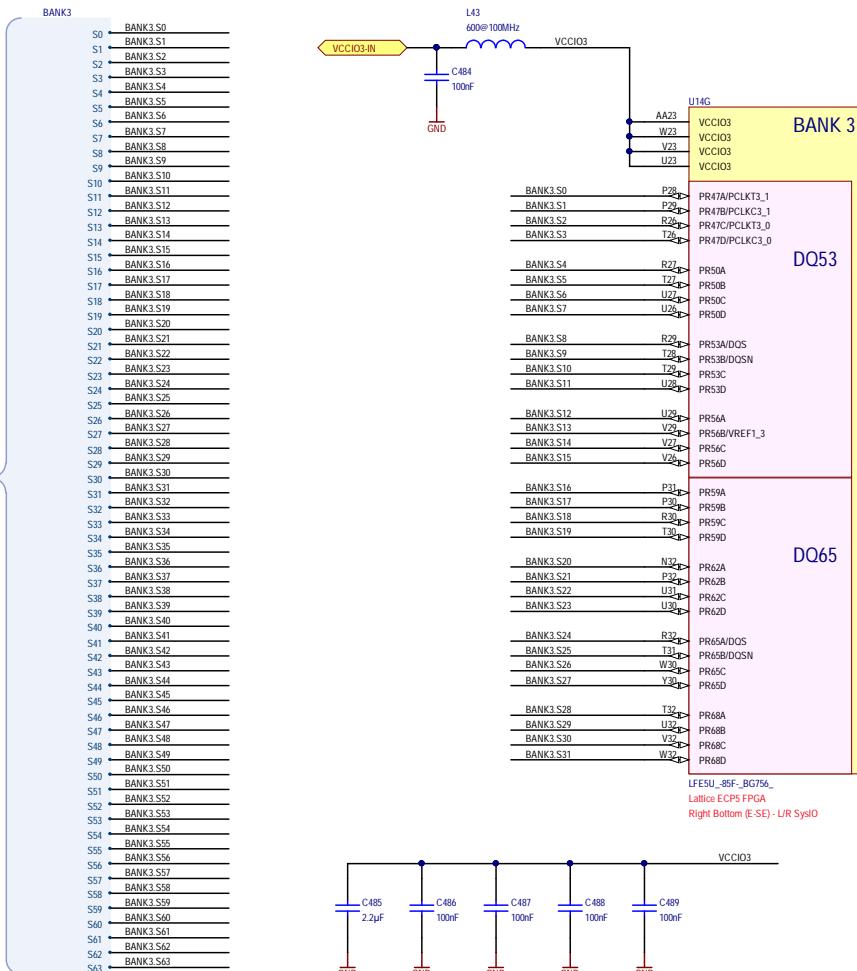
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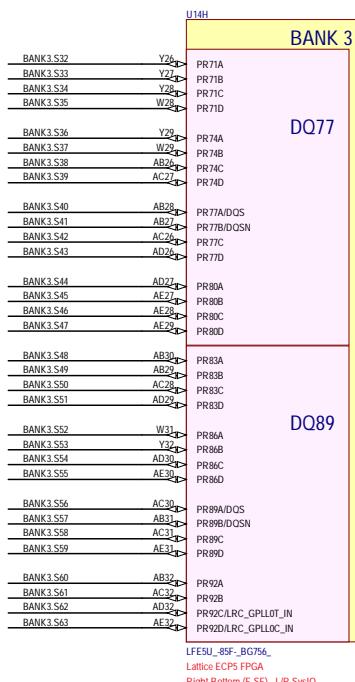
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Title Config FPGA - Bank 3		
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3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com		
Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 15 of 57	Date 1/11/2017

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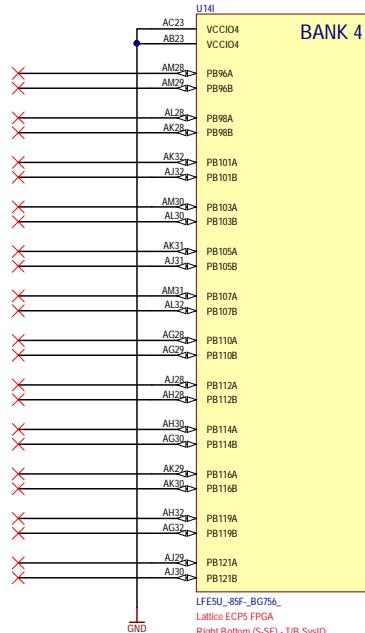
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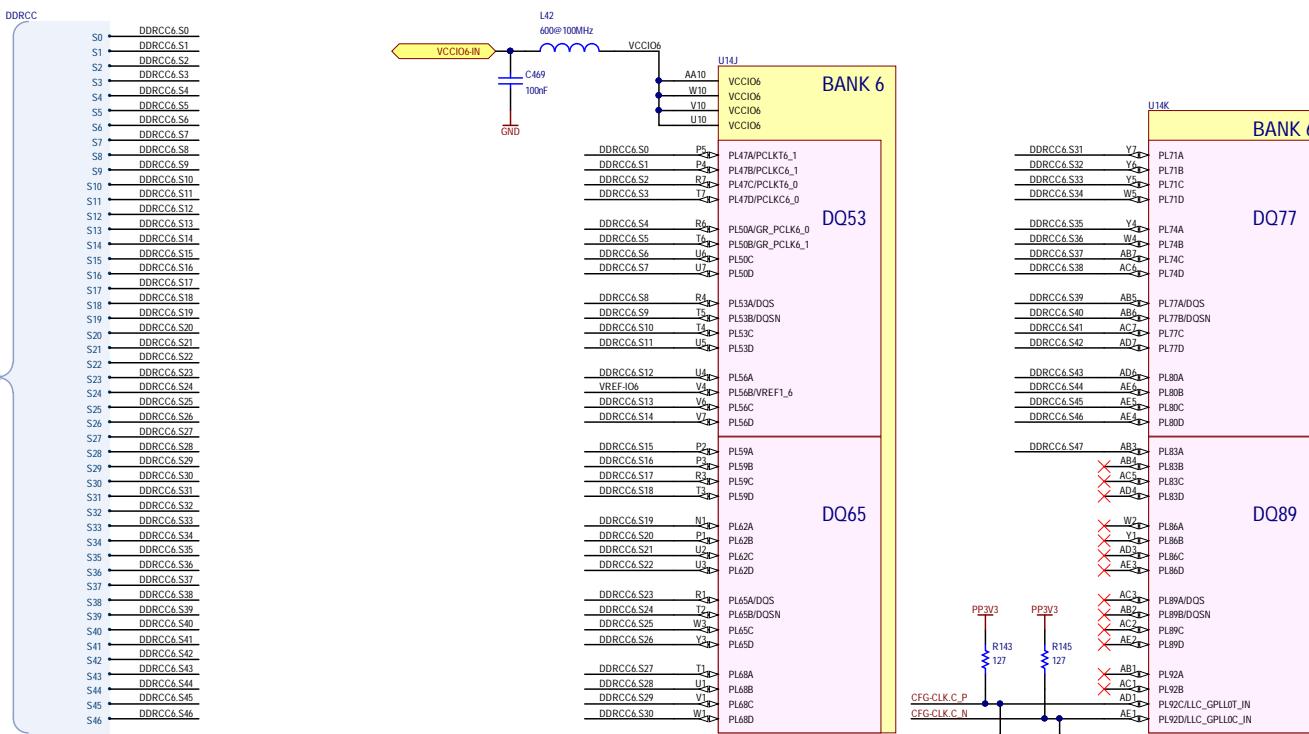
Cannot open file
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Config FPGA - Bank 4

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Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton

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NOTE: Do not use VREF when using differential termination

No on-chip termination required on CLK

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Cannot open file logo.bmp		Title Config FPGA - Bank 6		
Engineer	Drawn By	Checked By		
E. Chiu	E. Chiu	J. Brinton		
Revision 1	Sheet 17 of 57	Date 1/11/2017		

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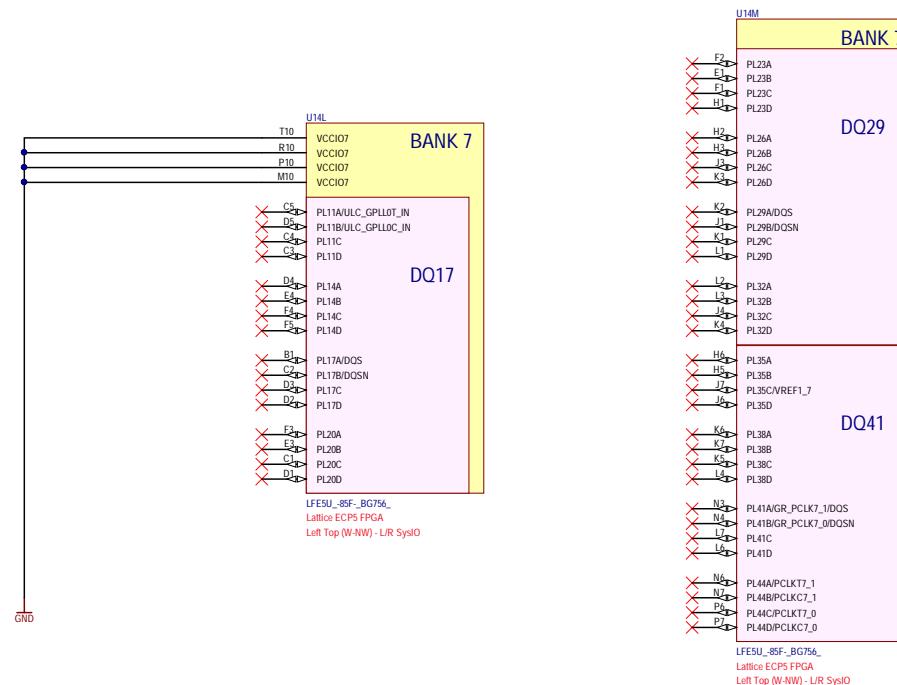
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Title Config FPGA - Bank 7

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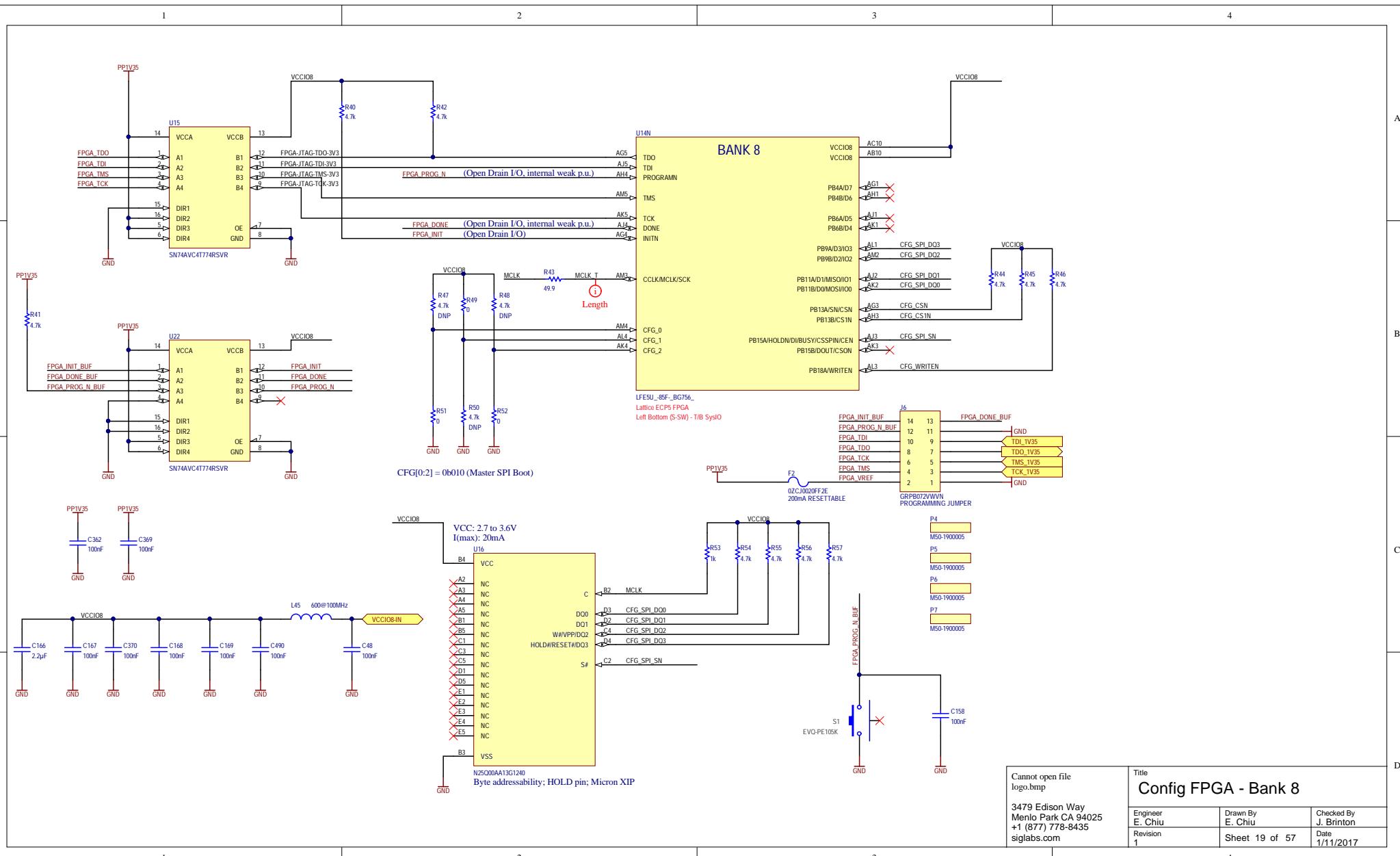
Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 18 of 57	Date 1/11/2017

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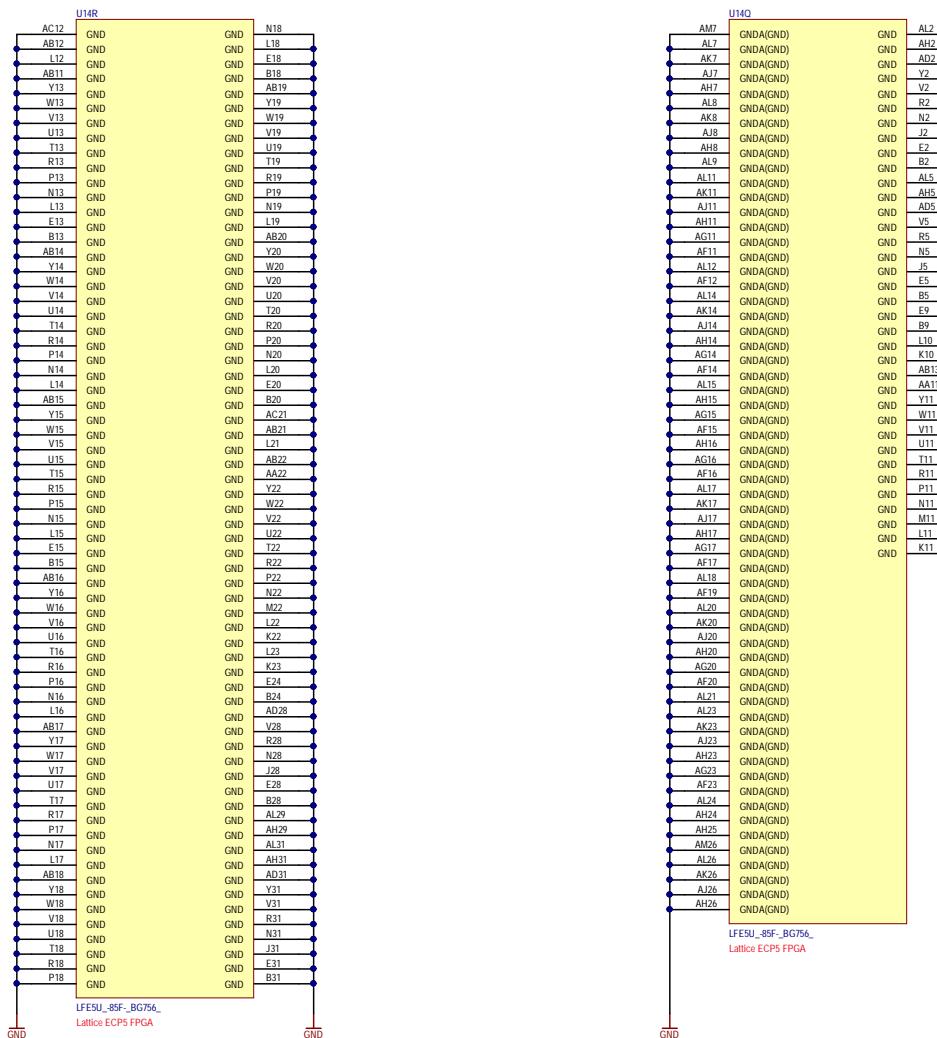
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Title
Config FPGA - Ground

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 20 of 57	
		Date 1/11/2017

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Title
Config FPGA - Misc

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Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton
Revision 1	Sheet 21 of 57	Date 1/11/2017

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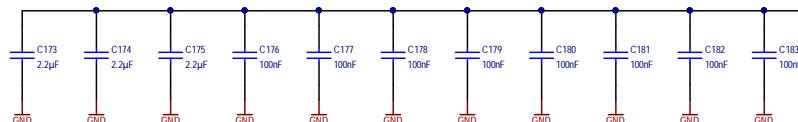
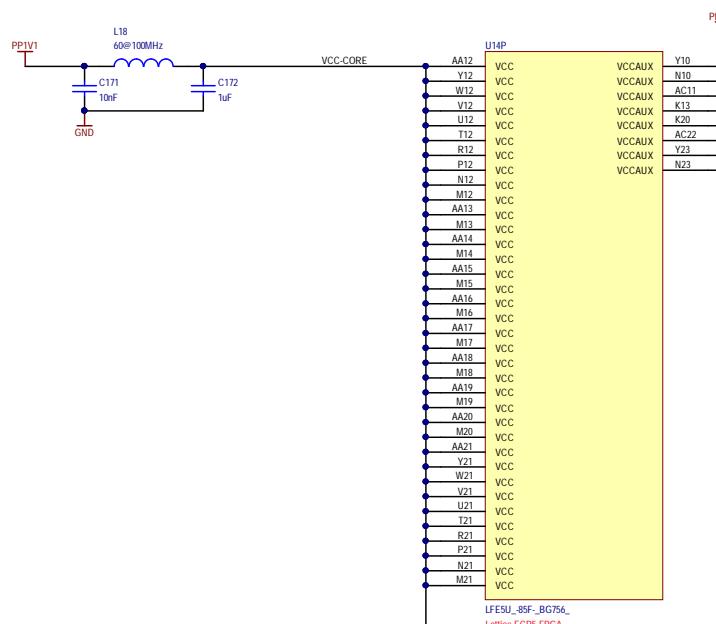
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Config FPGA - Power

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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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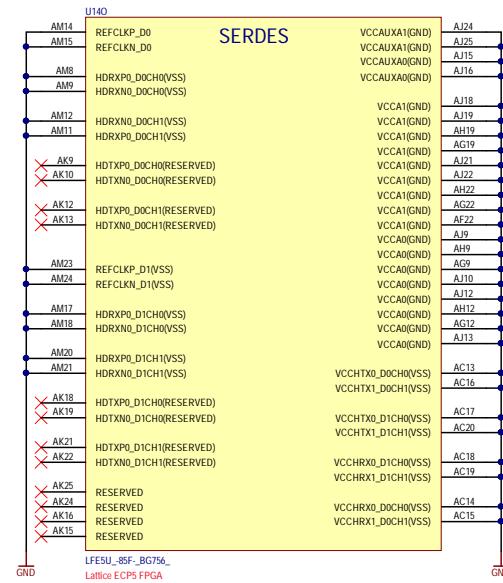
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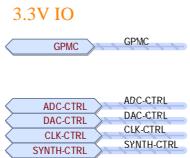
D

Cannot open file
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siglabs.com**Title**
Config FPGA - Serdes

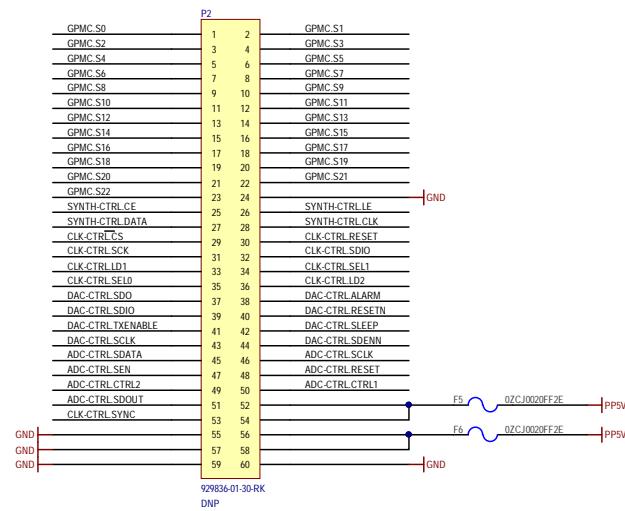
Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton

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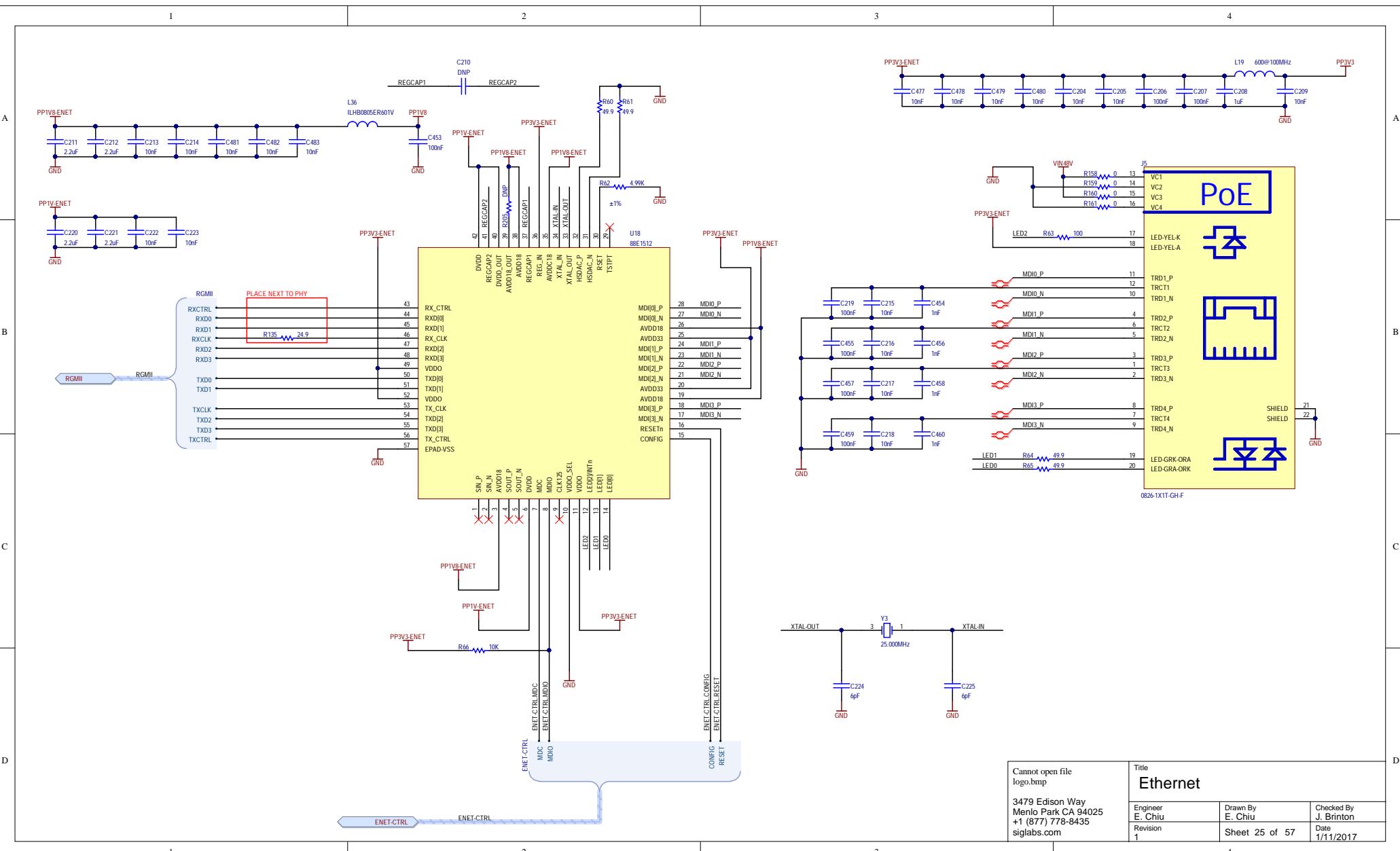
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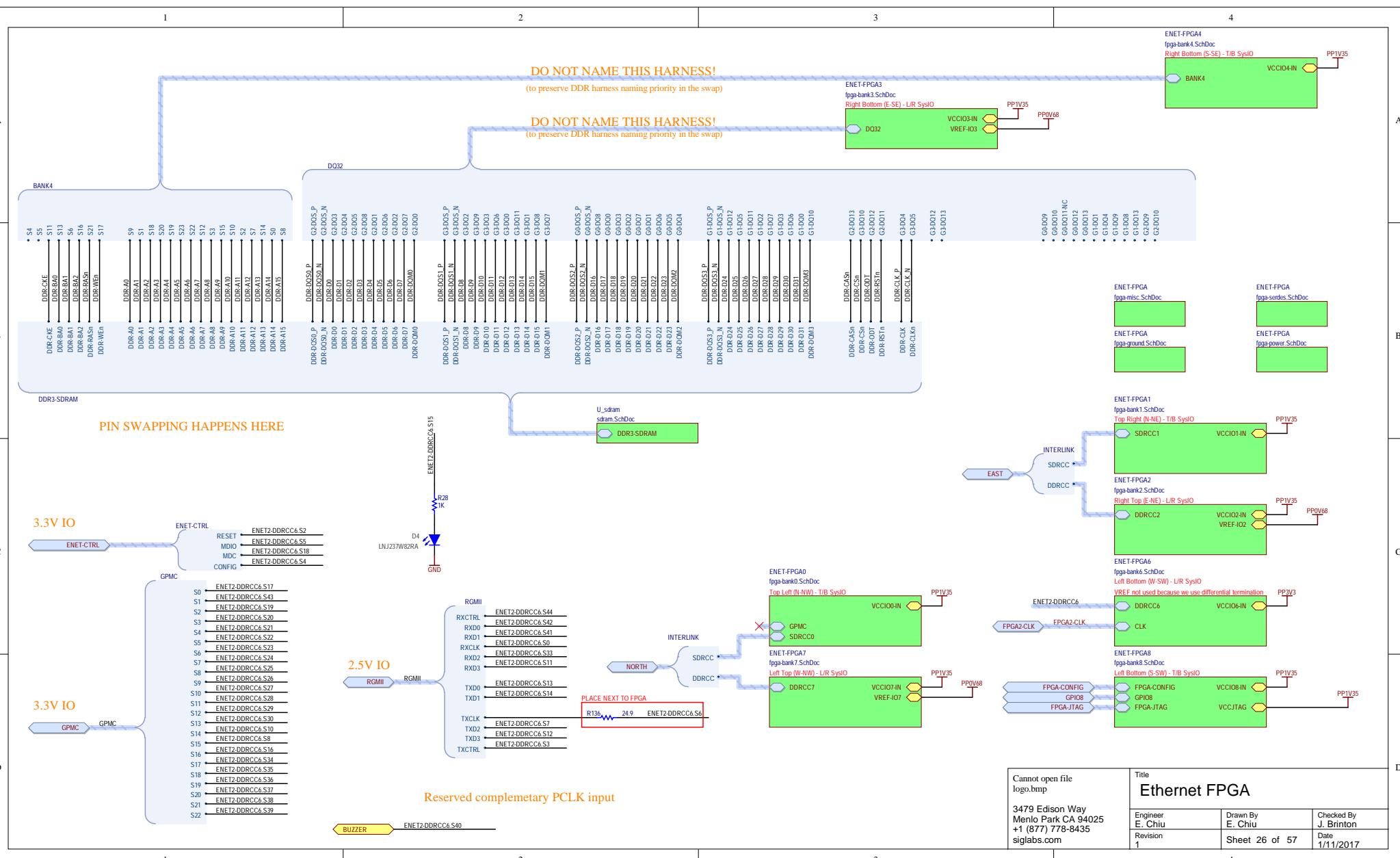
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Cannot open file
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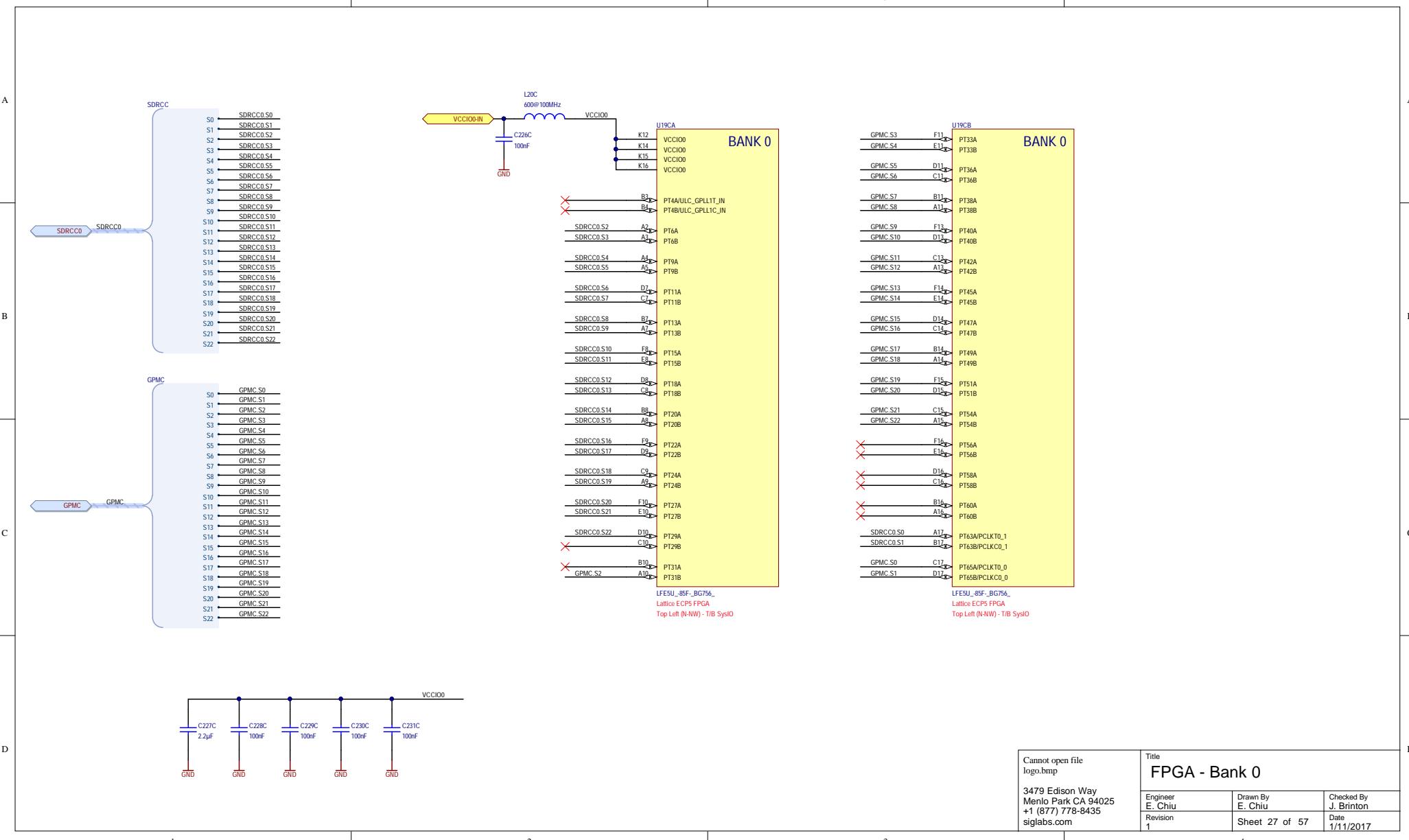
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Engineer	Drawn By	Checked By
E. Chiu	E. Chiu	J. Brinton
Revision 1	Sheet 24 of 57	Date 1/11/2017

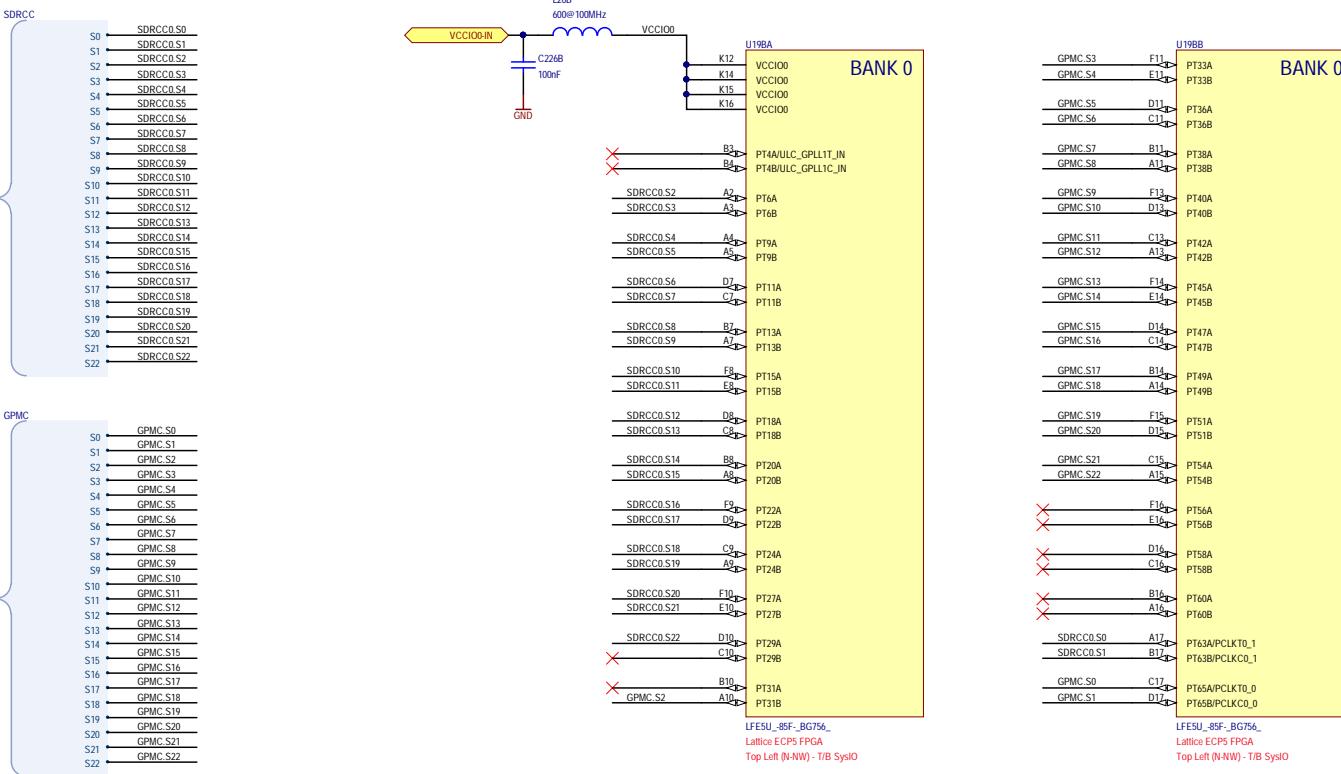




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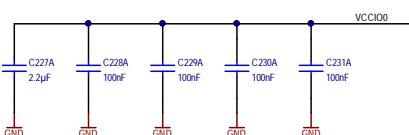
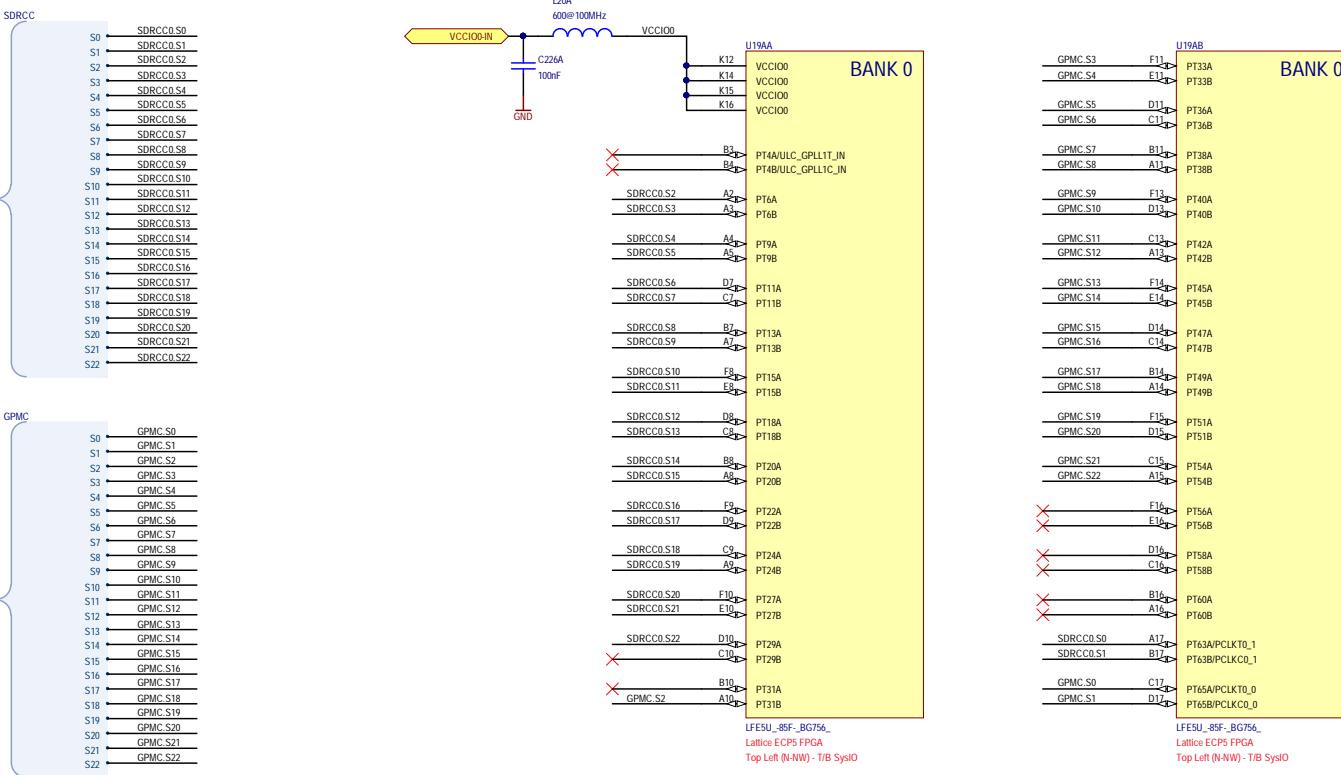
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Cannot open file logo.bmp	Title FPGA - Bank 0		
3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com	Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 27 of 57	Date 1/11/2017	

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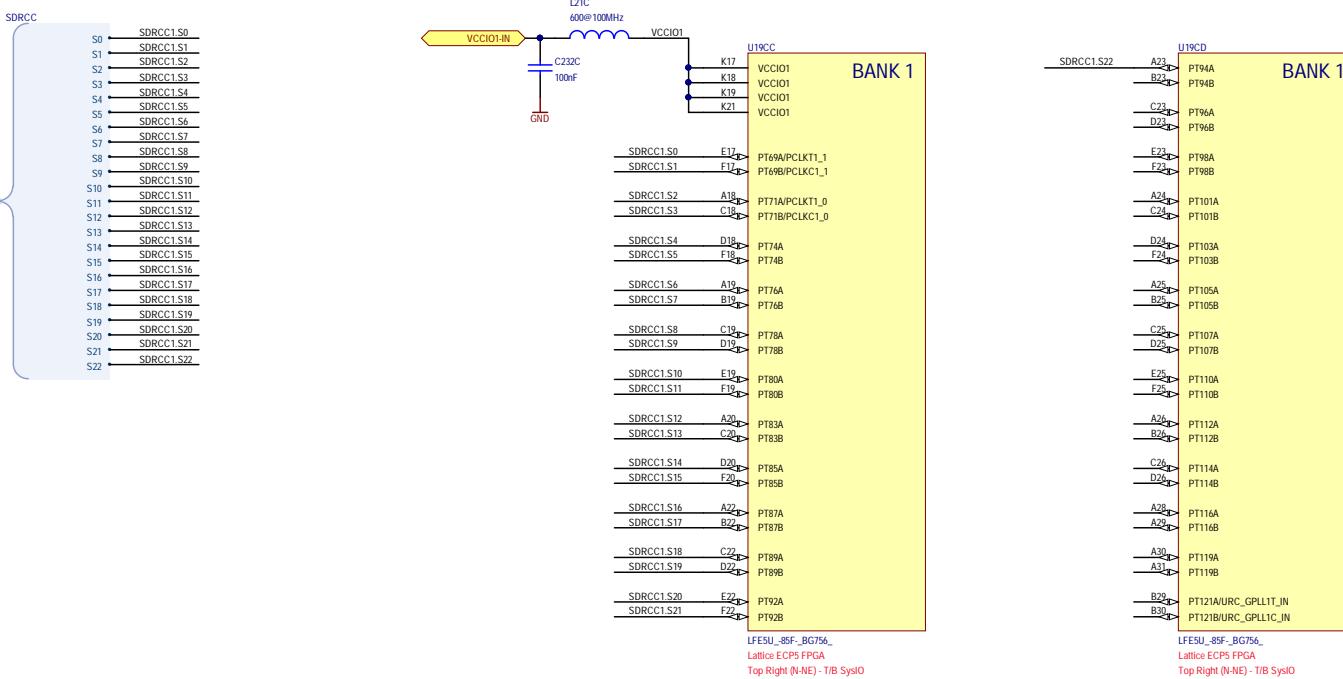


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Cannot open file logo.bmp		Title FPGA - Bank 0		
Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton		
Revision 1	Sheet 27 of 57	Date 1/11/2017		

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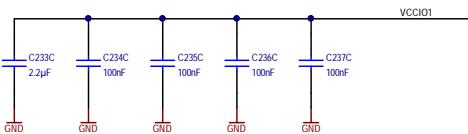
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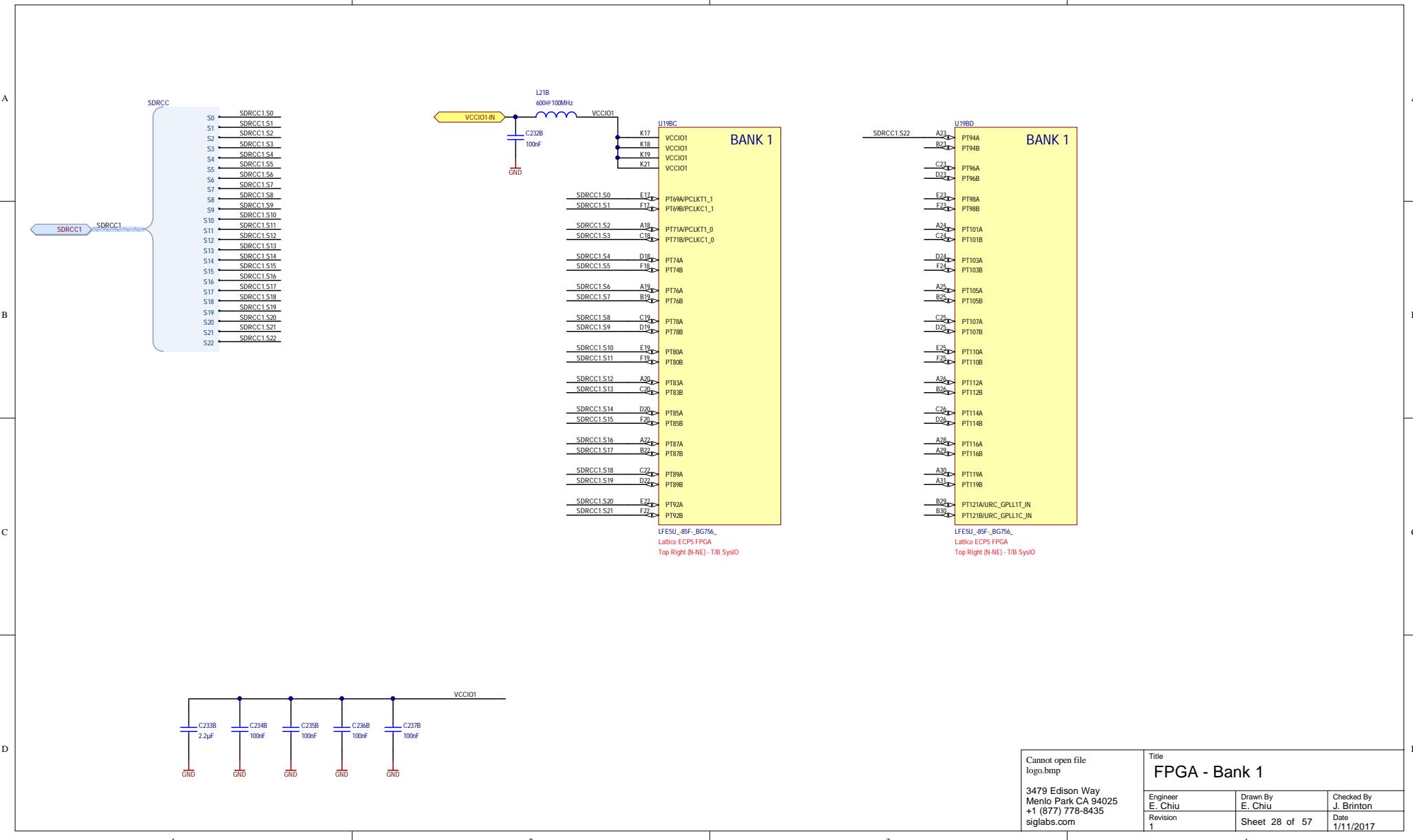
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FPGA - Bank 1

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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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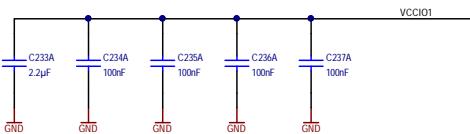
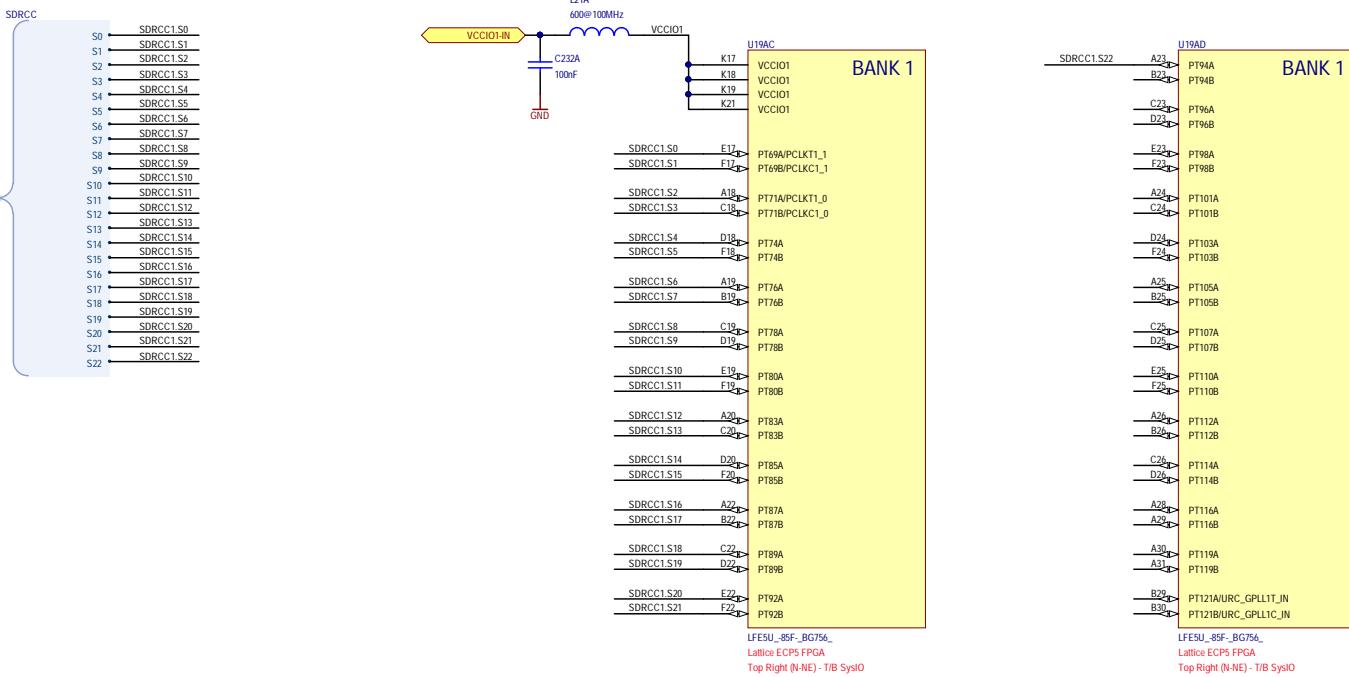
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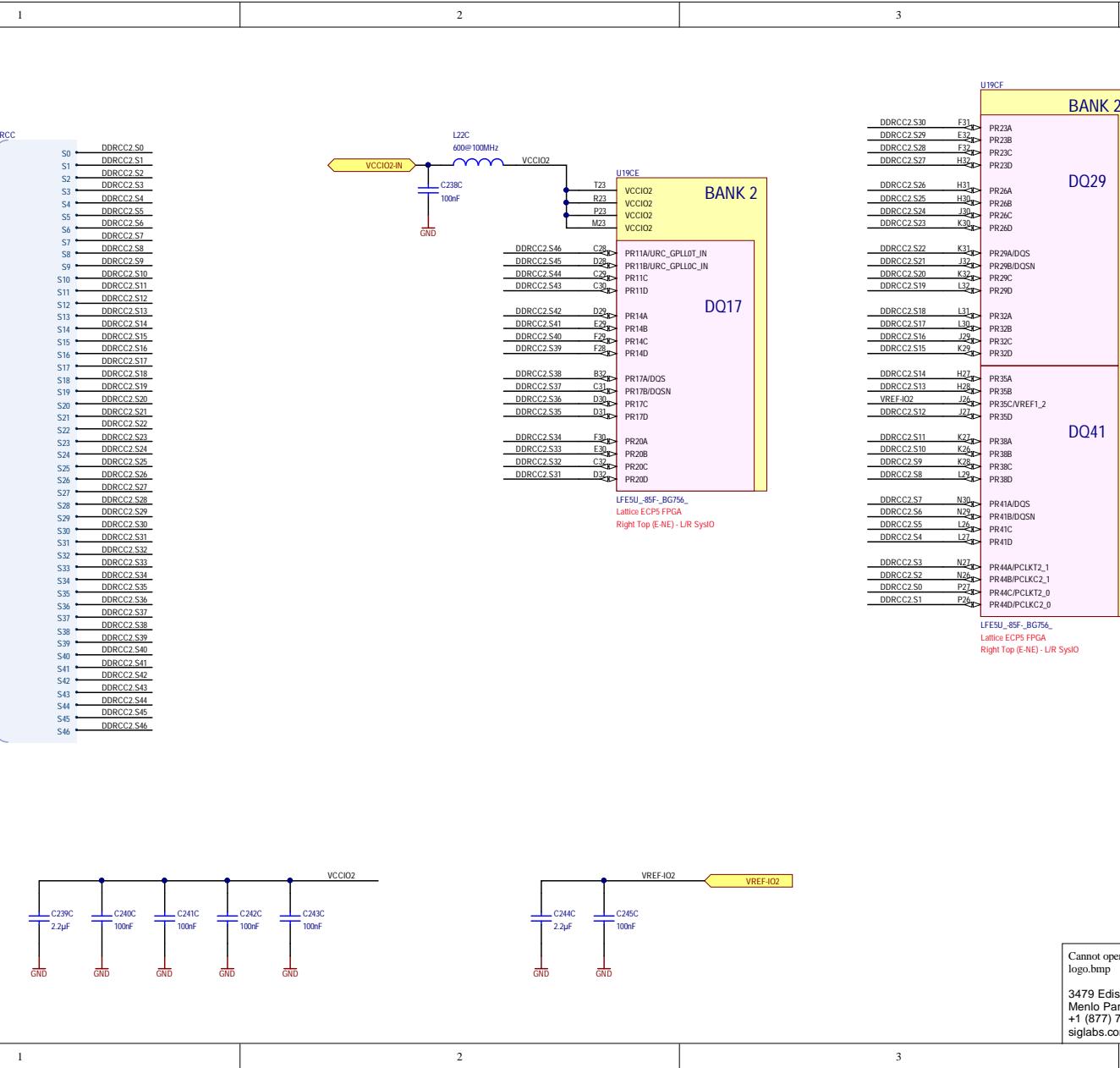
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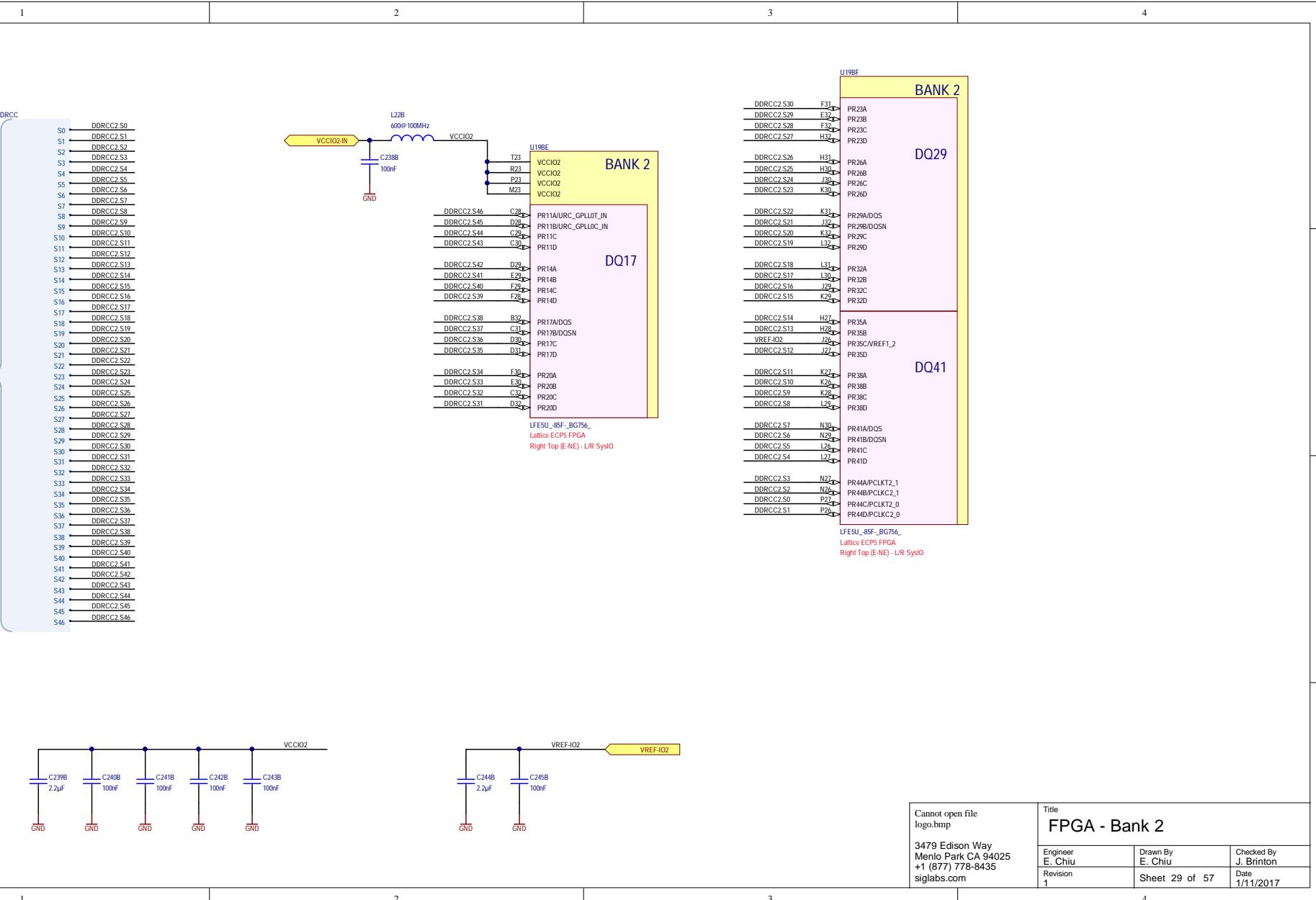
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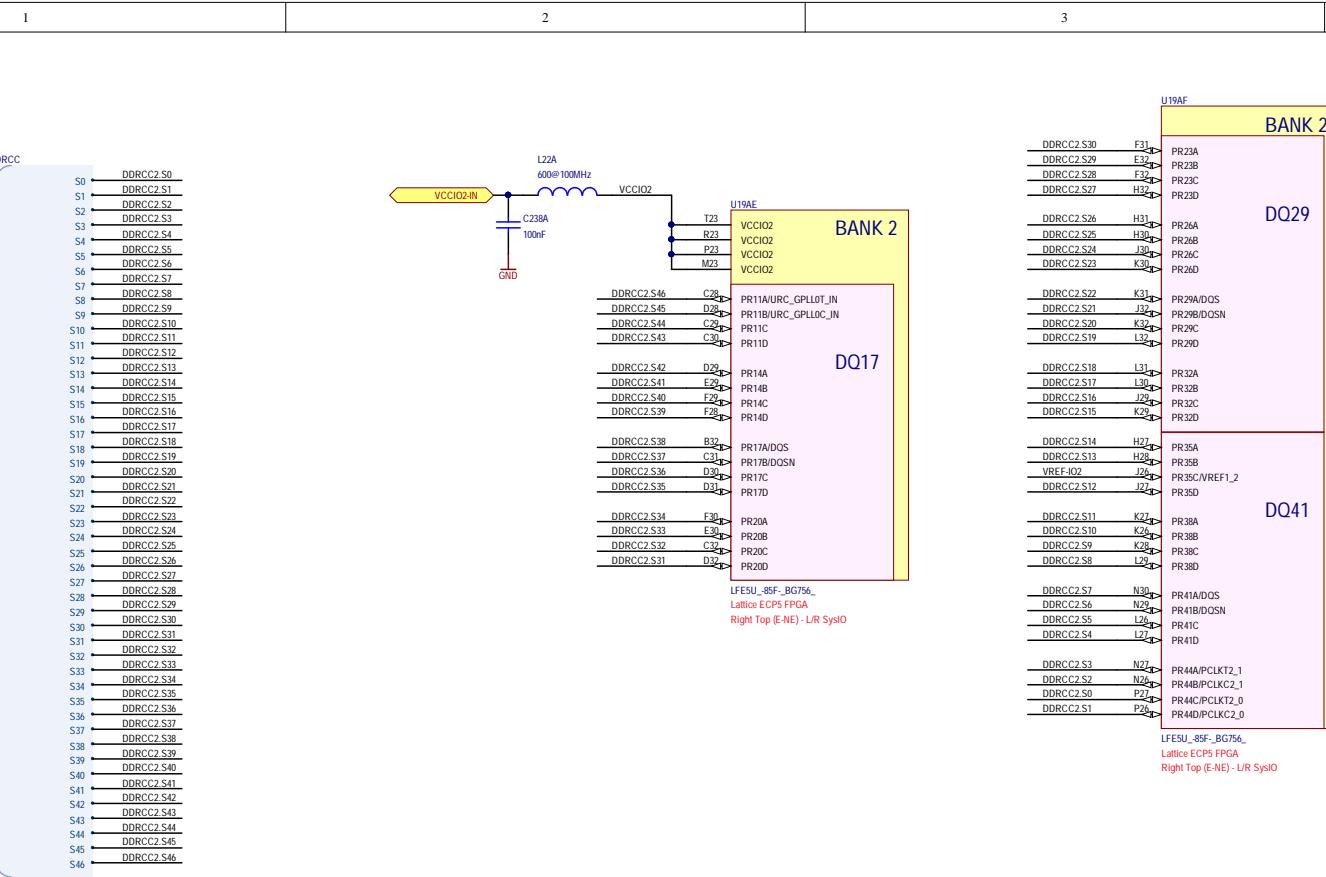


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Title		
FPGA - Bank 1		
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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Title
FPGA - Bank 2

Engineer

E. Chiu

Drawn By

E. Chiu

Checked By

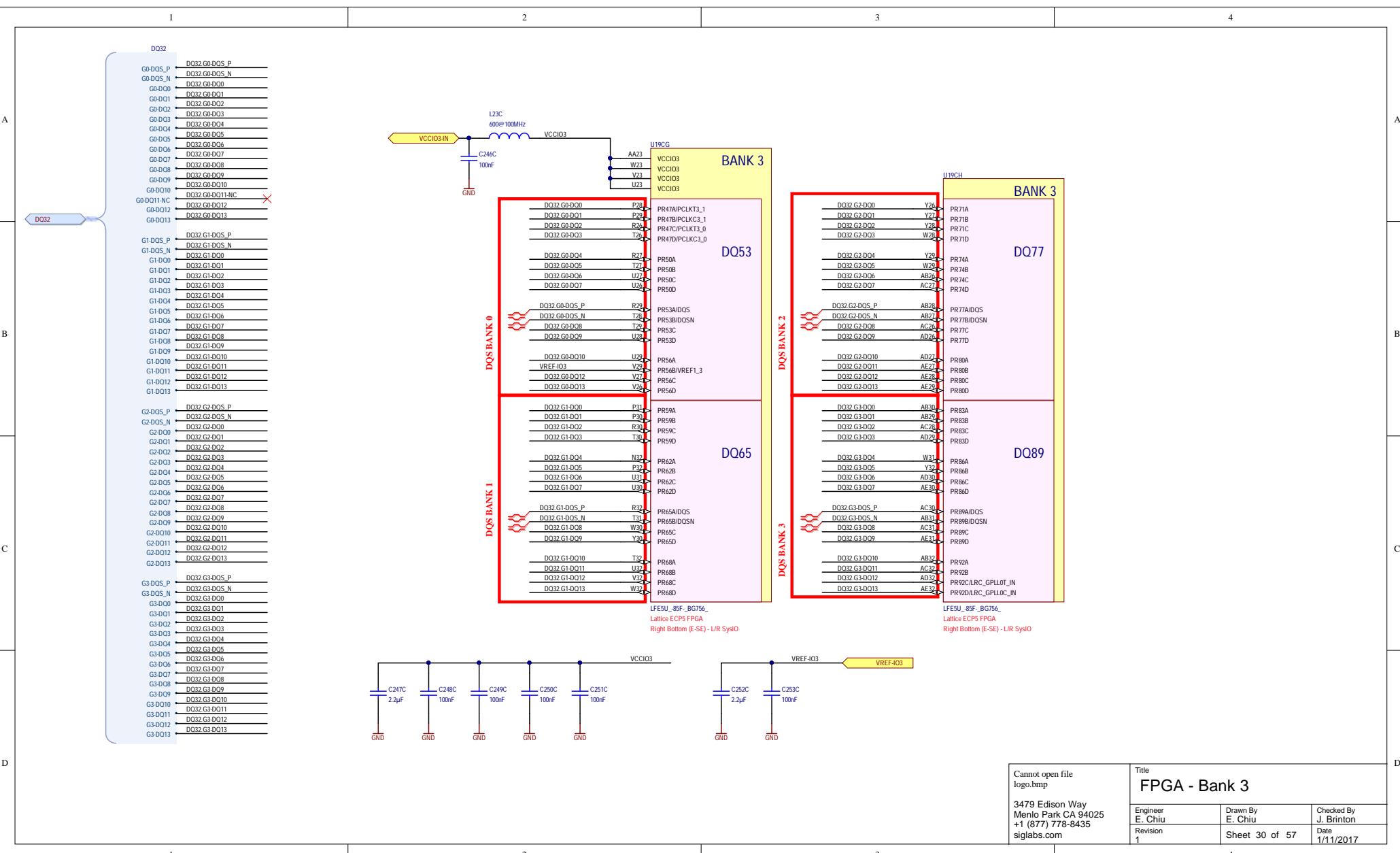
J. Brinton

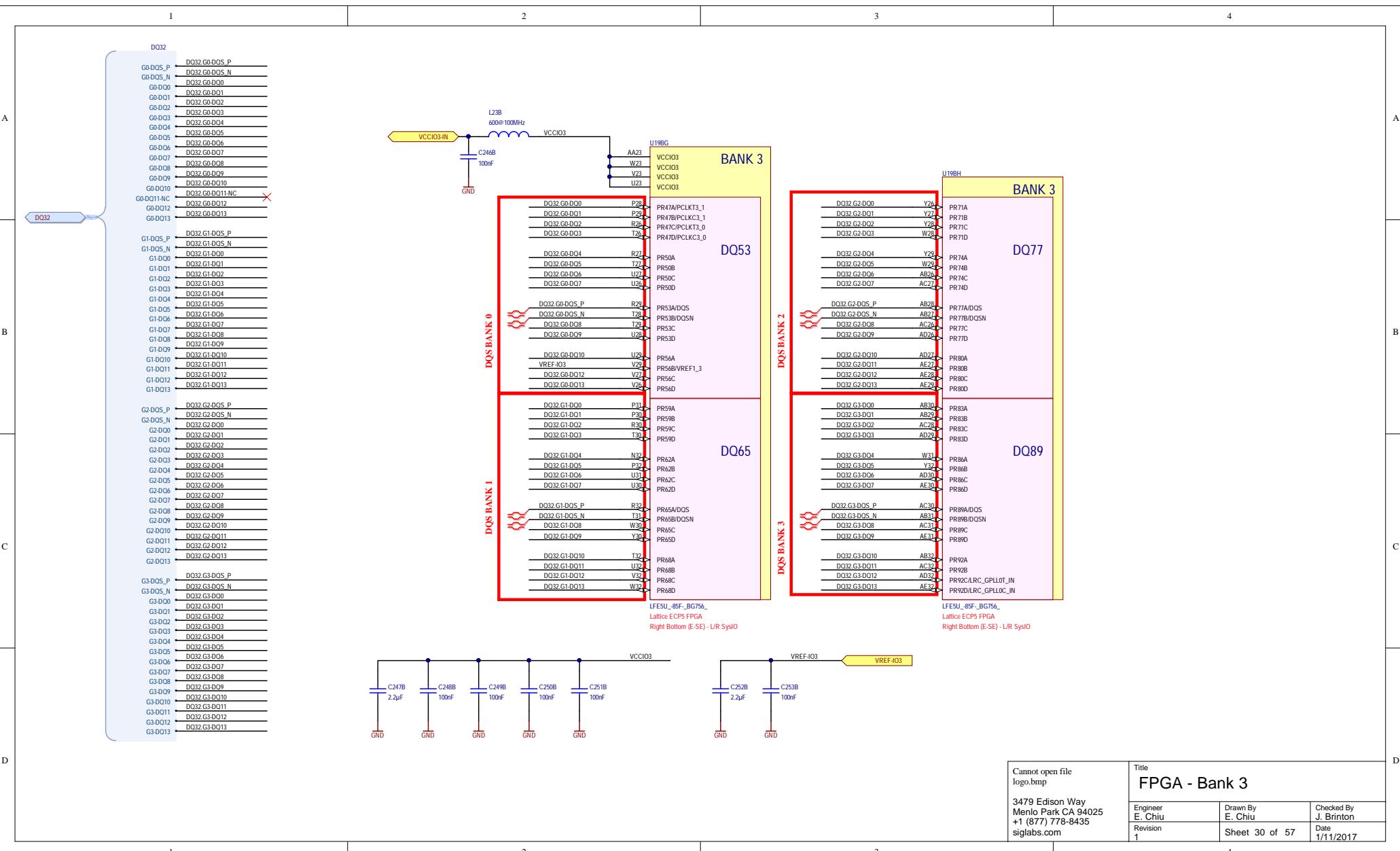
Revision

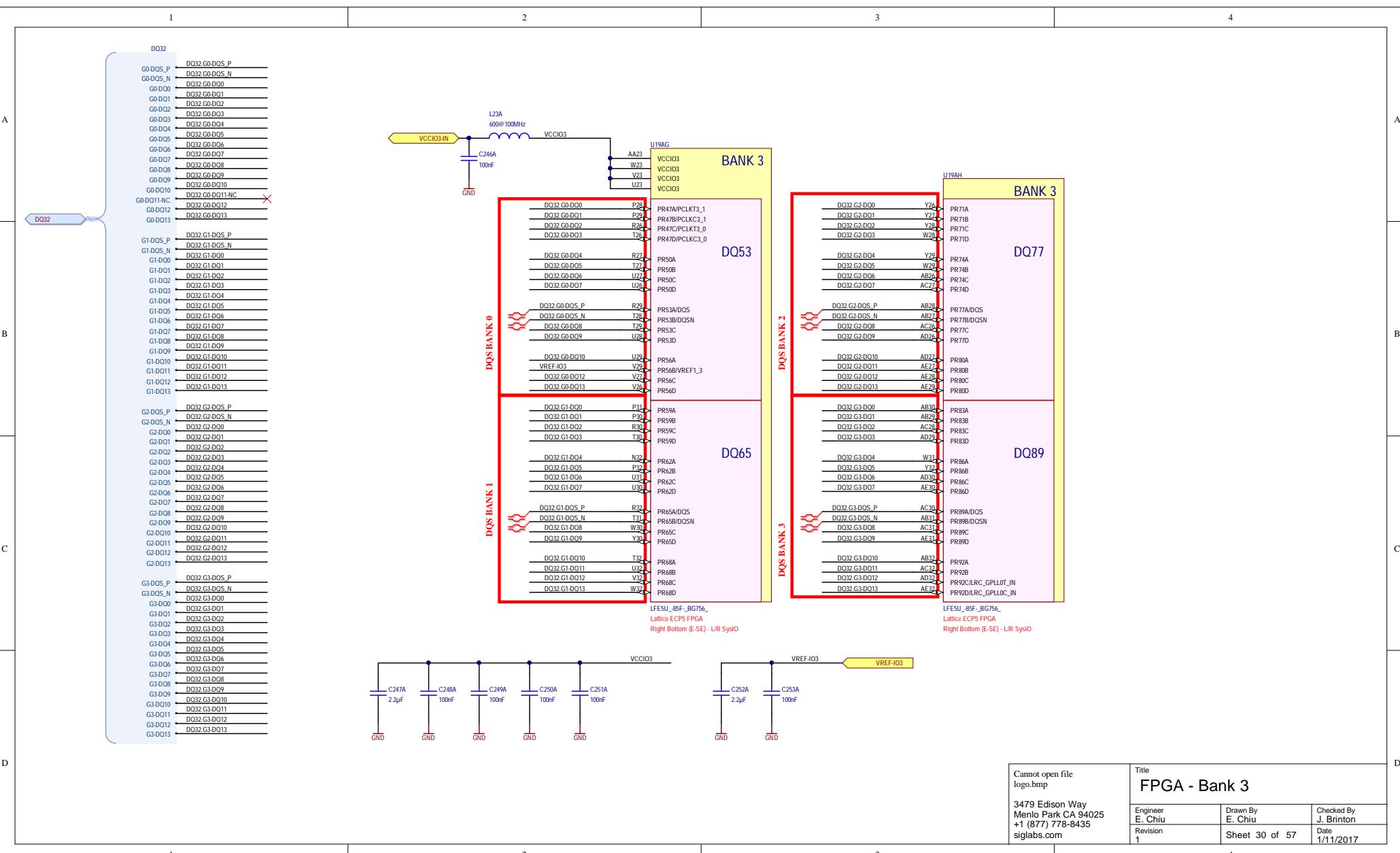
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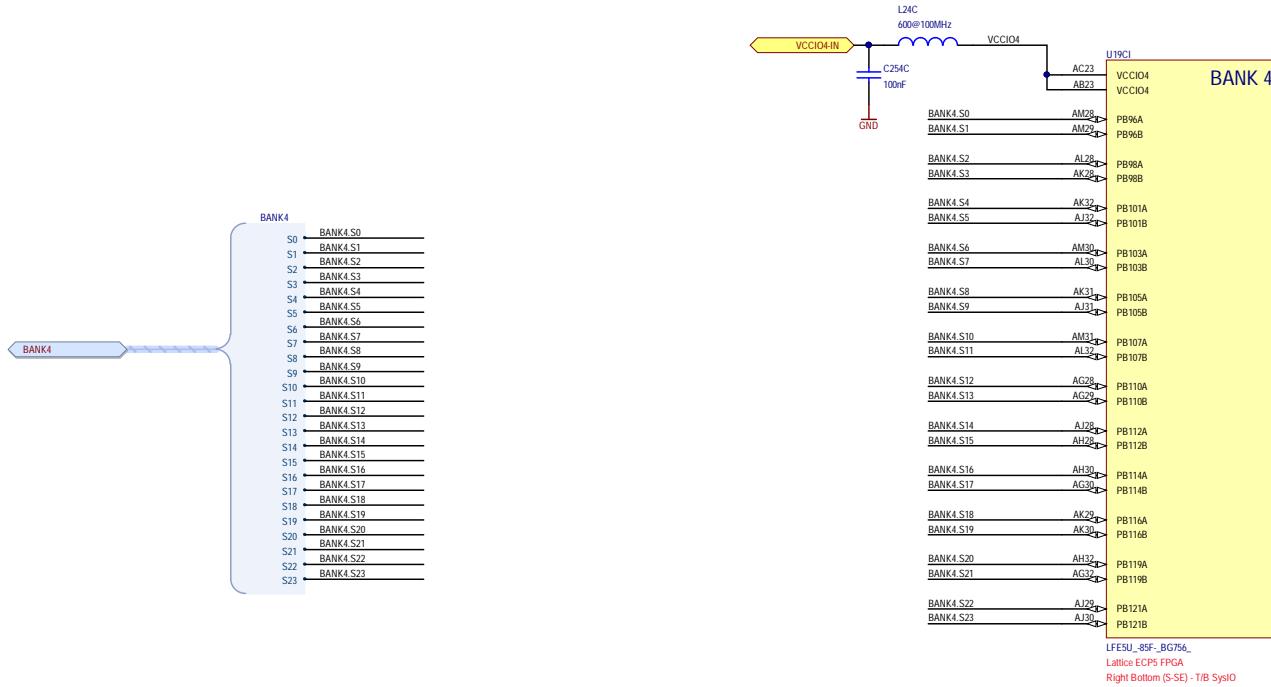
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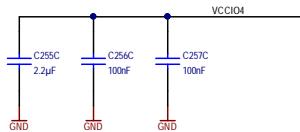
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FPGA - Bank 4

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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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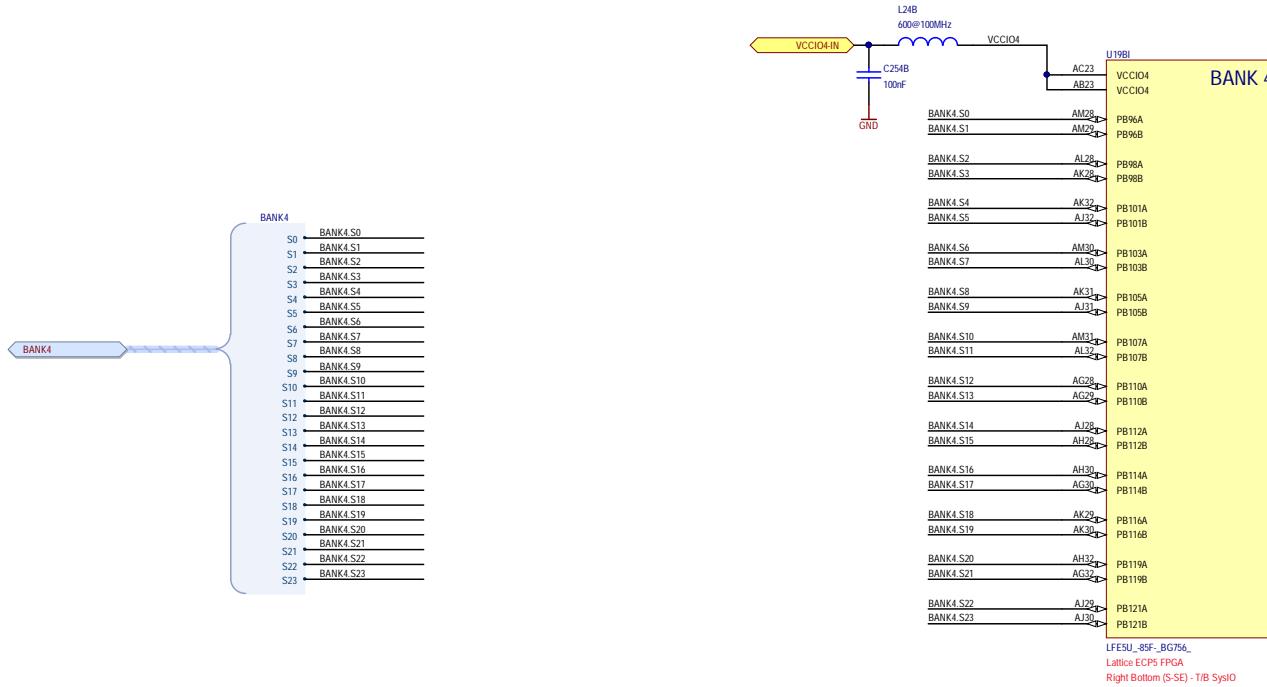
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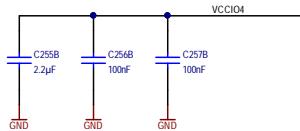
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 31 of 57	Date 1/11/2017

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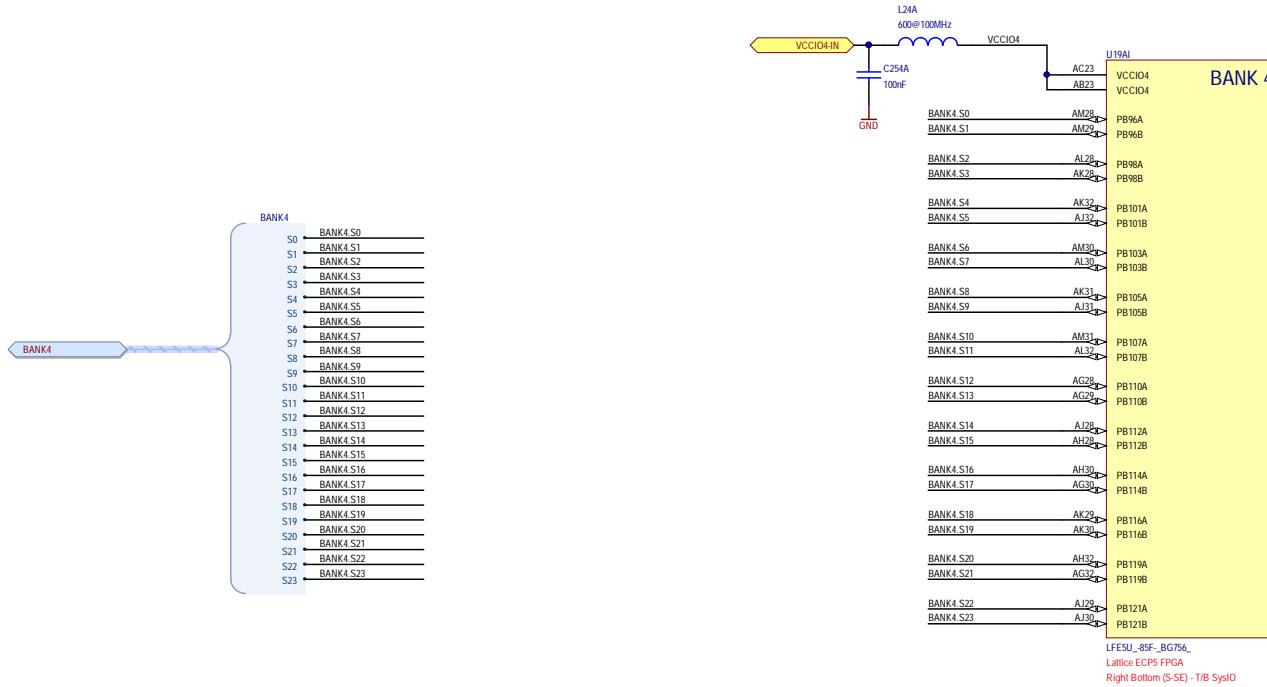
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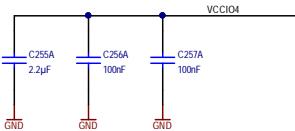
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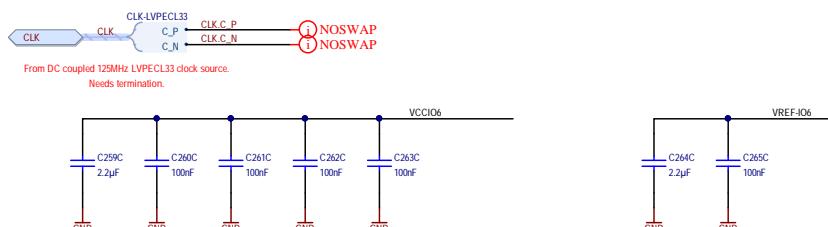
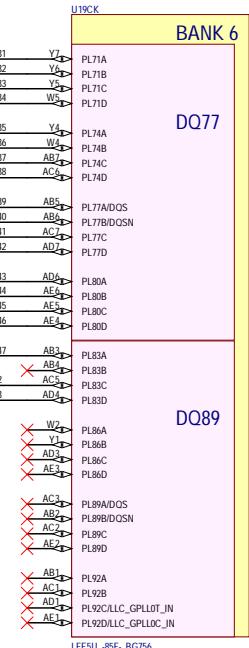
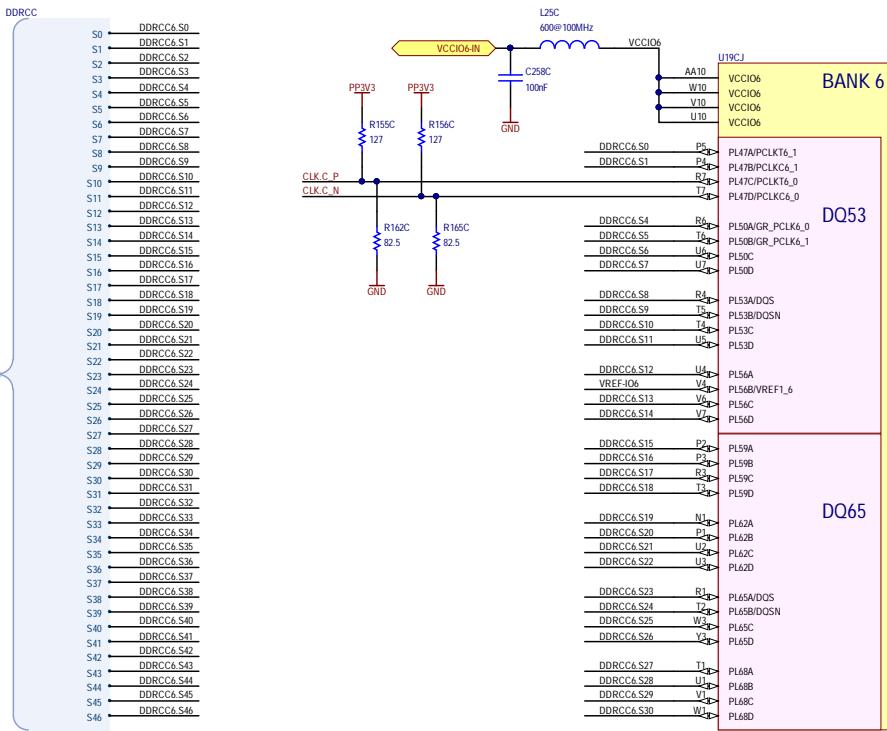


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FPGA - Bank 4
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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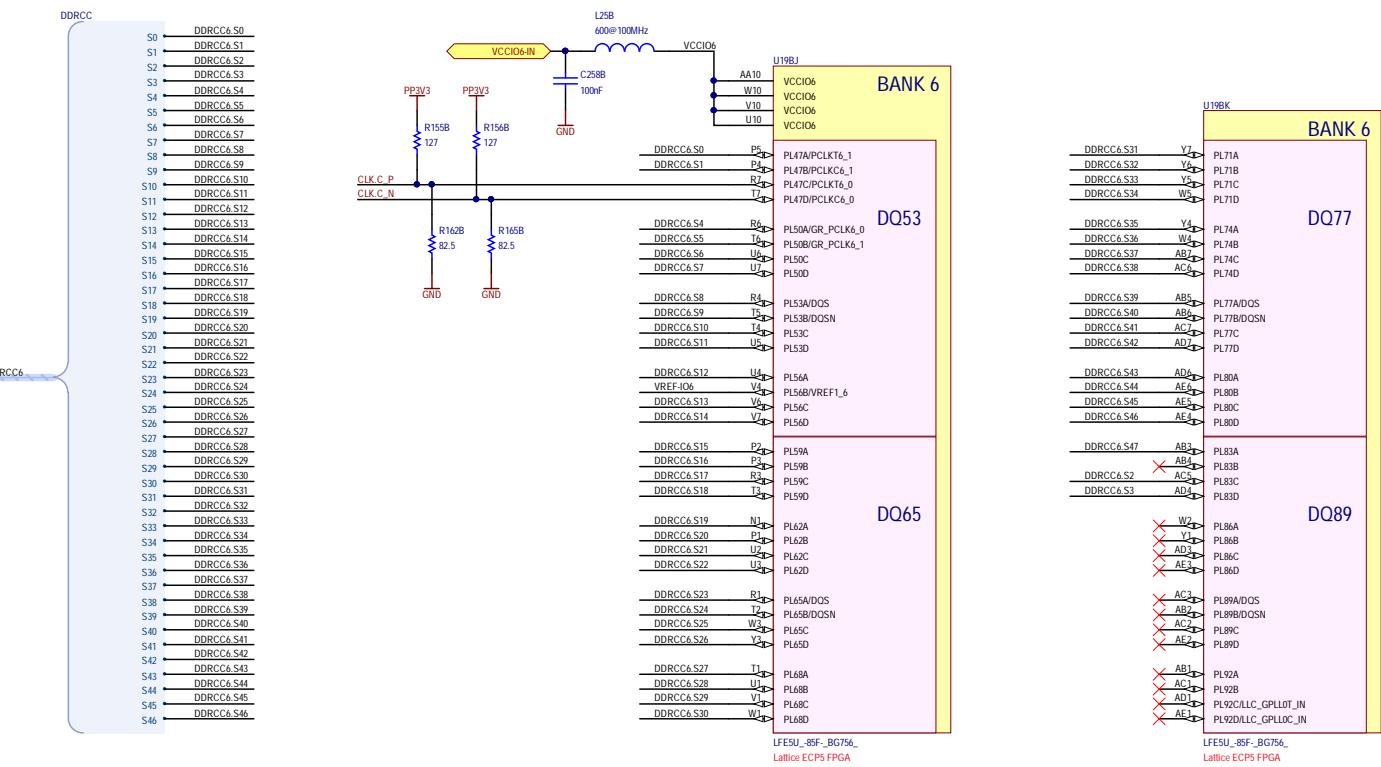
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3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com	Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 32 of 57	Date 1/11/2017	

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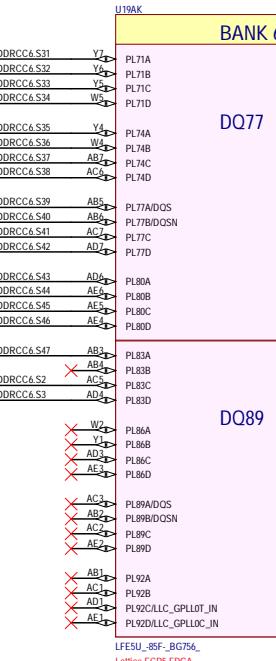
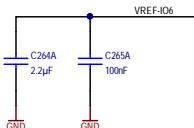
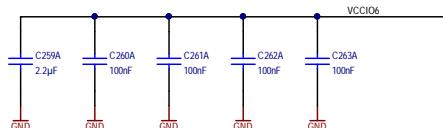
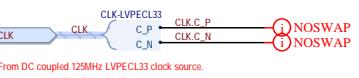
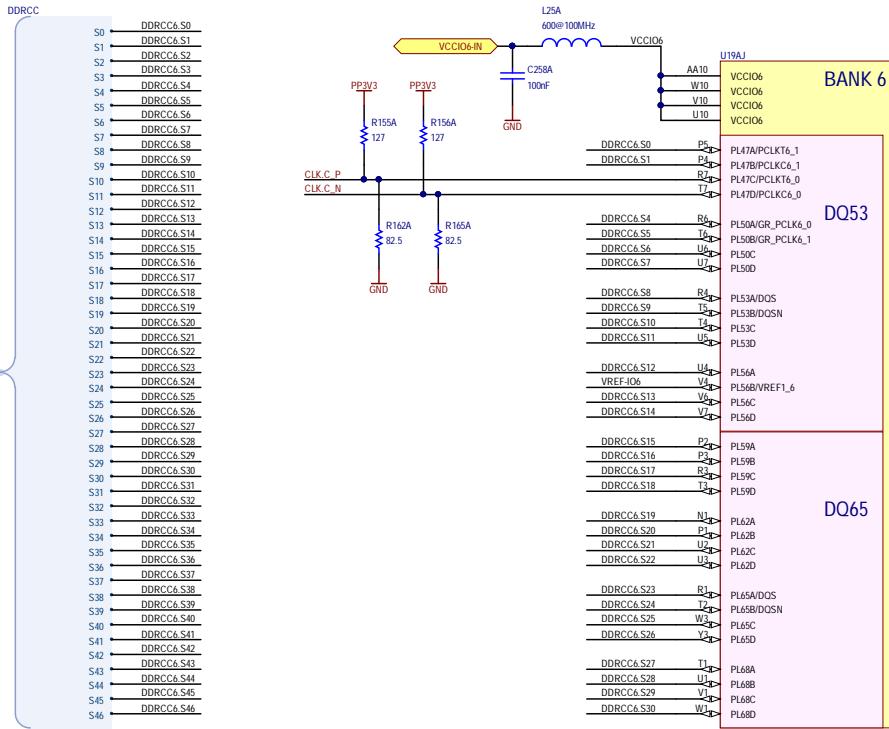
Cannot open file logo.bmp	Title FPGA - Bank 6		
3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com	Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 32 of 57	Date 1/11/2017	4

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Title
FPGA - Bank 6

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Engineer
E. Chiu

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E. Chiu

Checked By
J. Brinton

Revision
1

Sheet 32 of 57

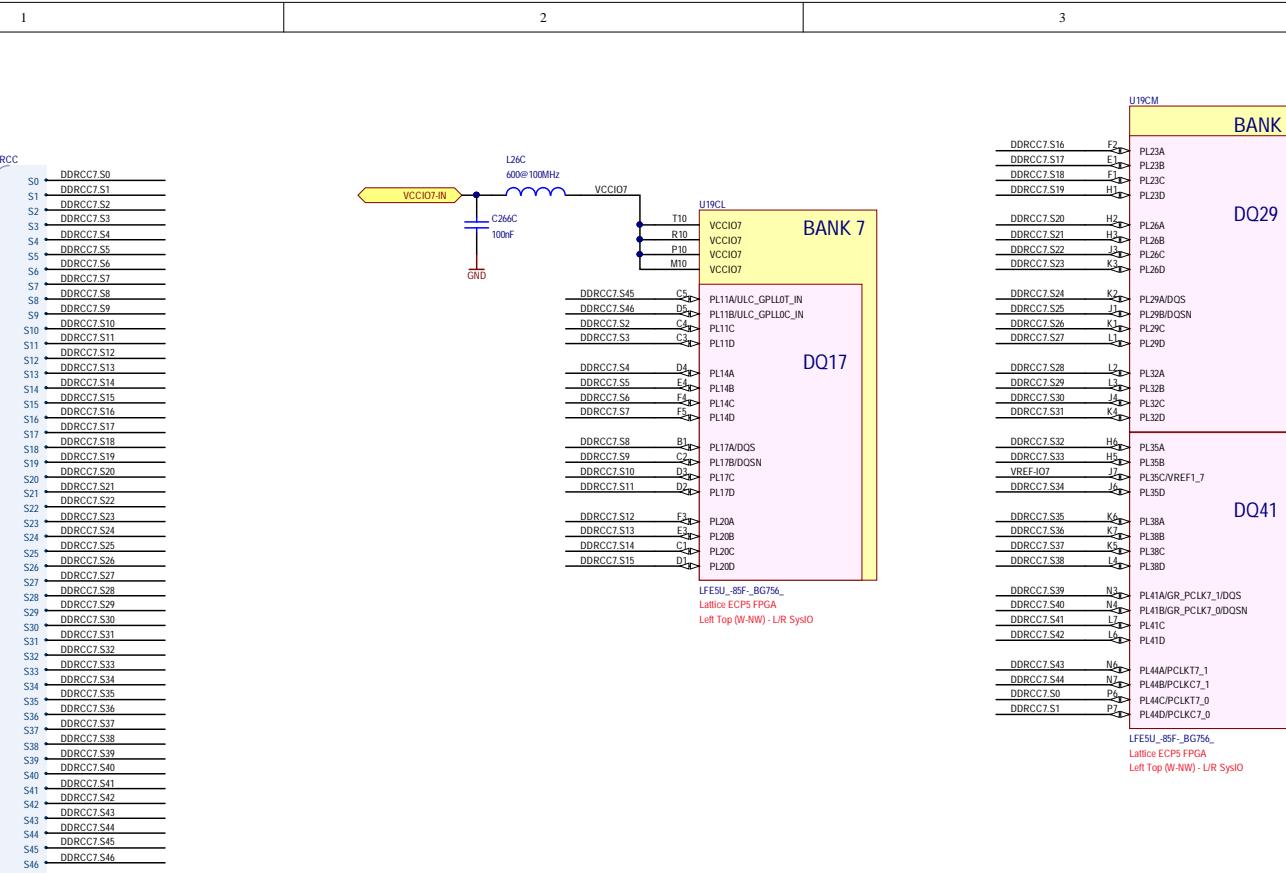
Date
1/11/2017

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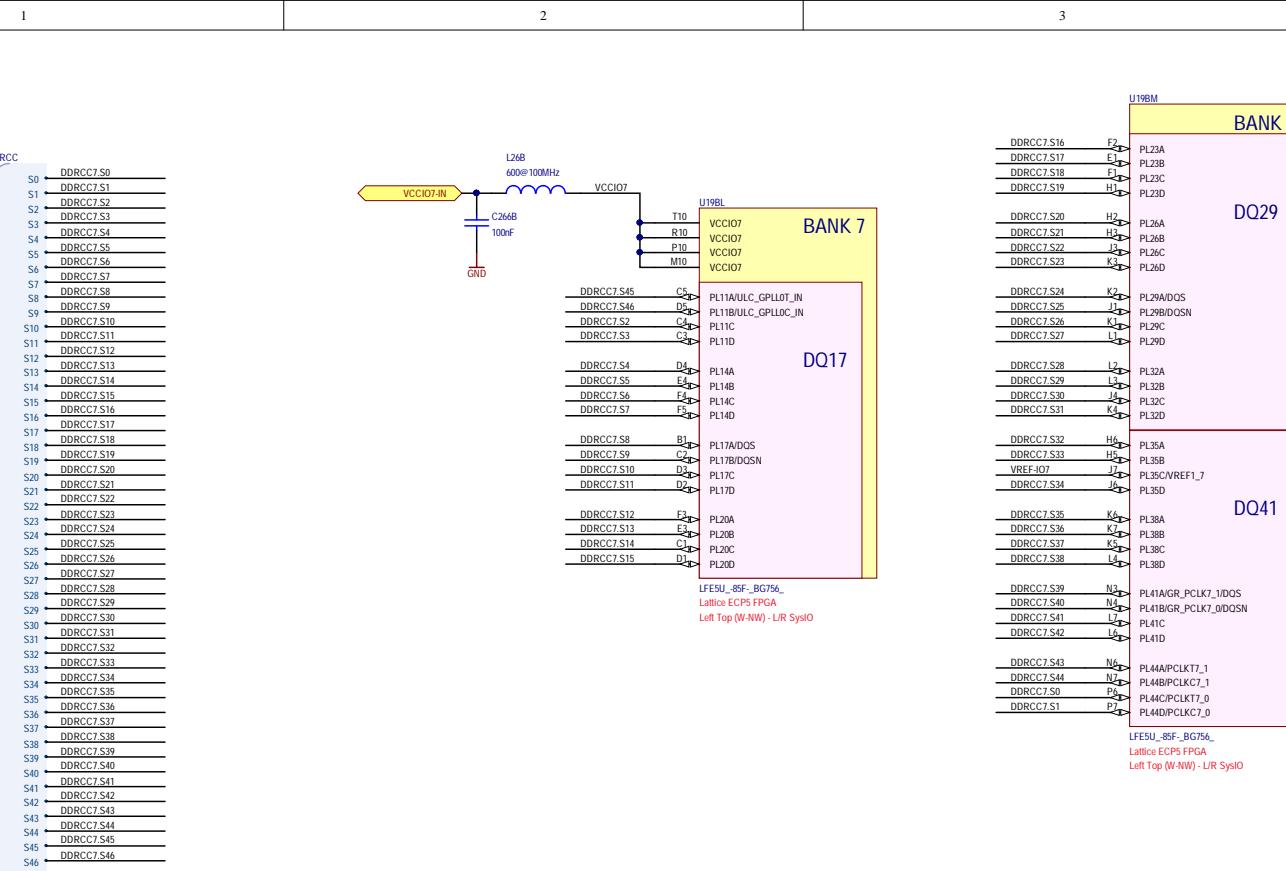
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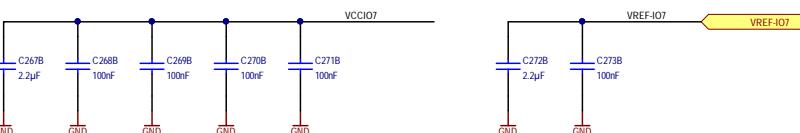
4

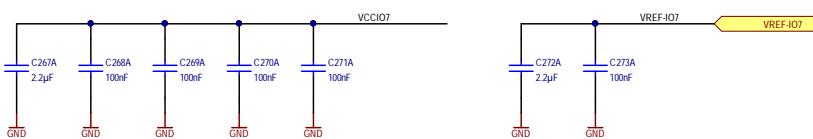
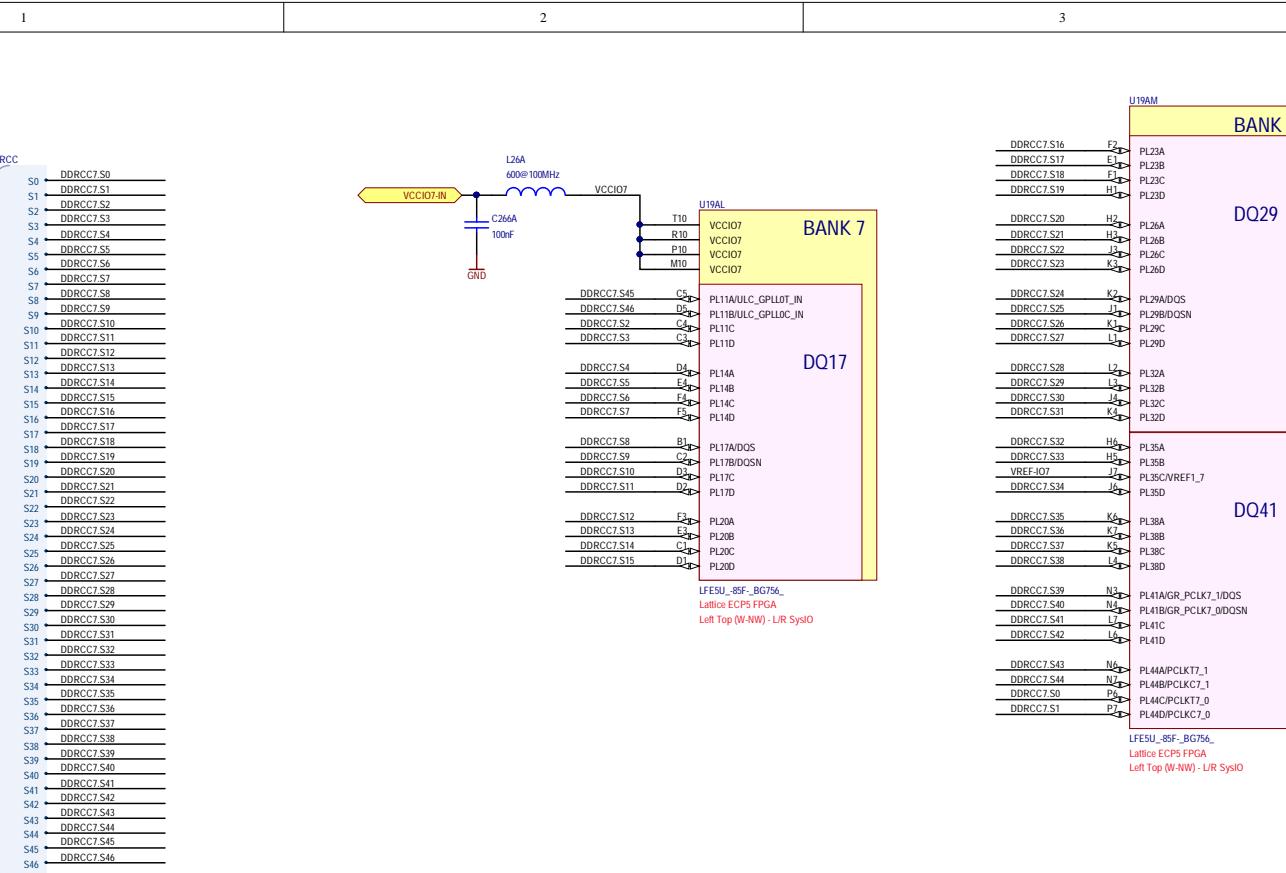


Cannot open file logo.bmp		Title FPGA - Bank 7		
3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com		Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton	Date 1/11/2017	
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Title
FPGA - Bank 7

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Engineer

E. Chiu

Drawn By

E. Chiu

Checked By

J. Brinton

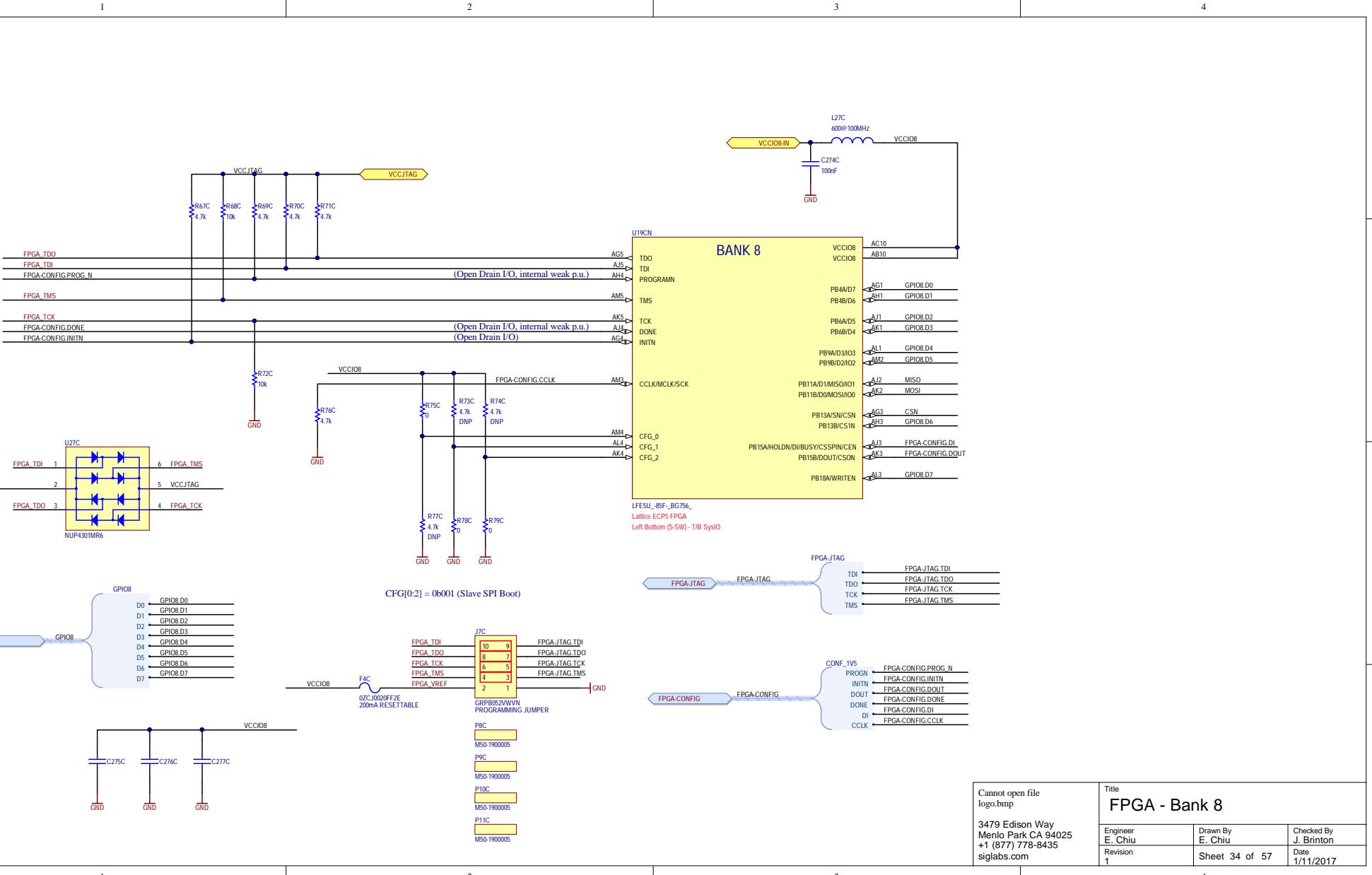
Revision

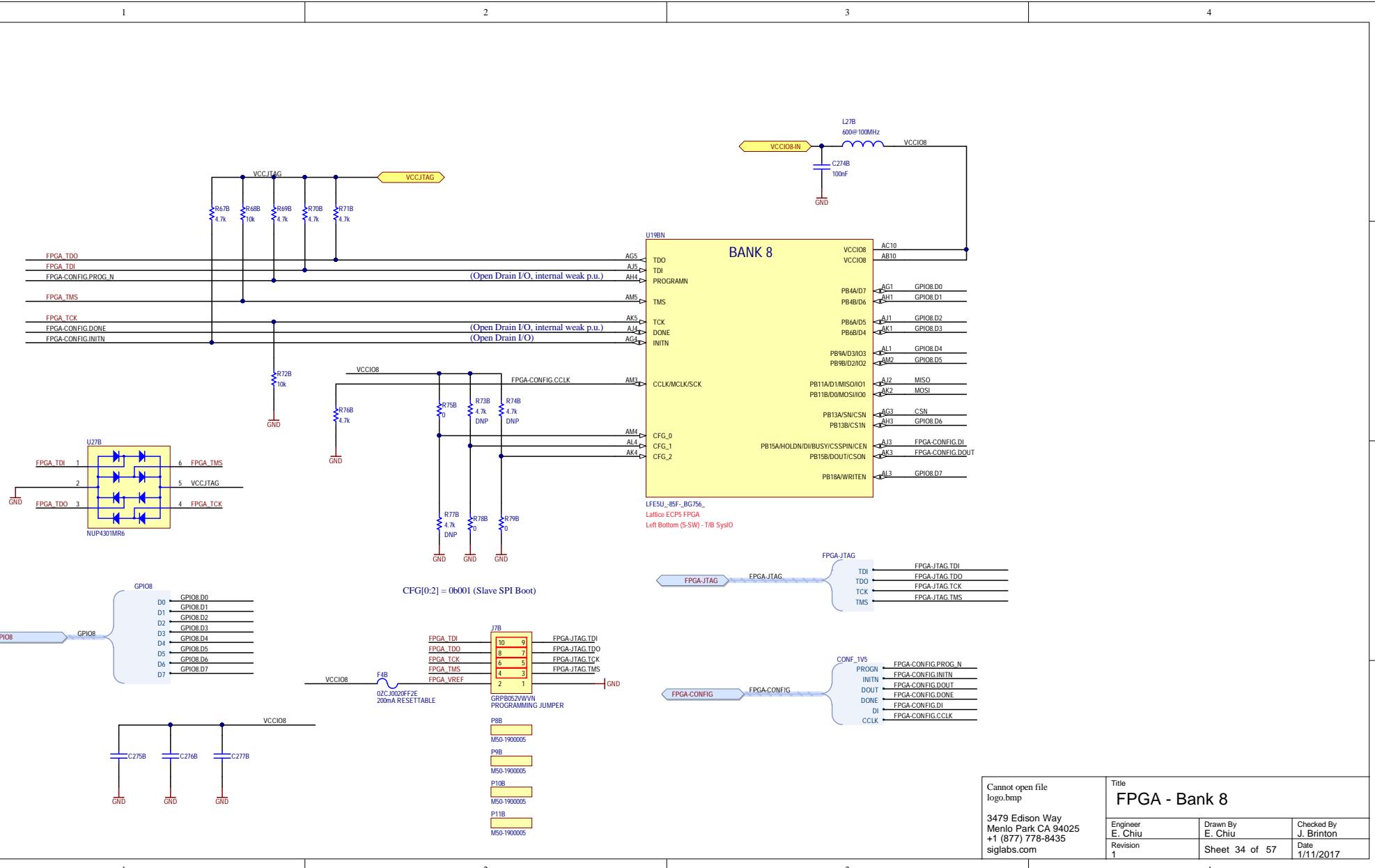
1

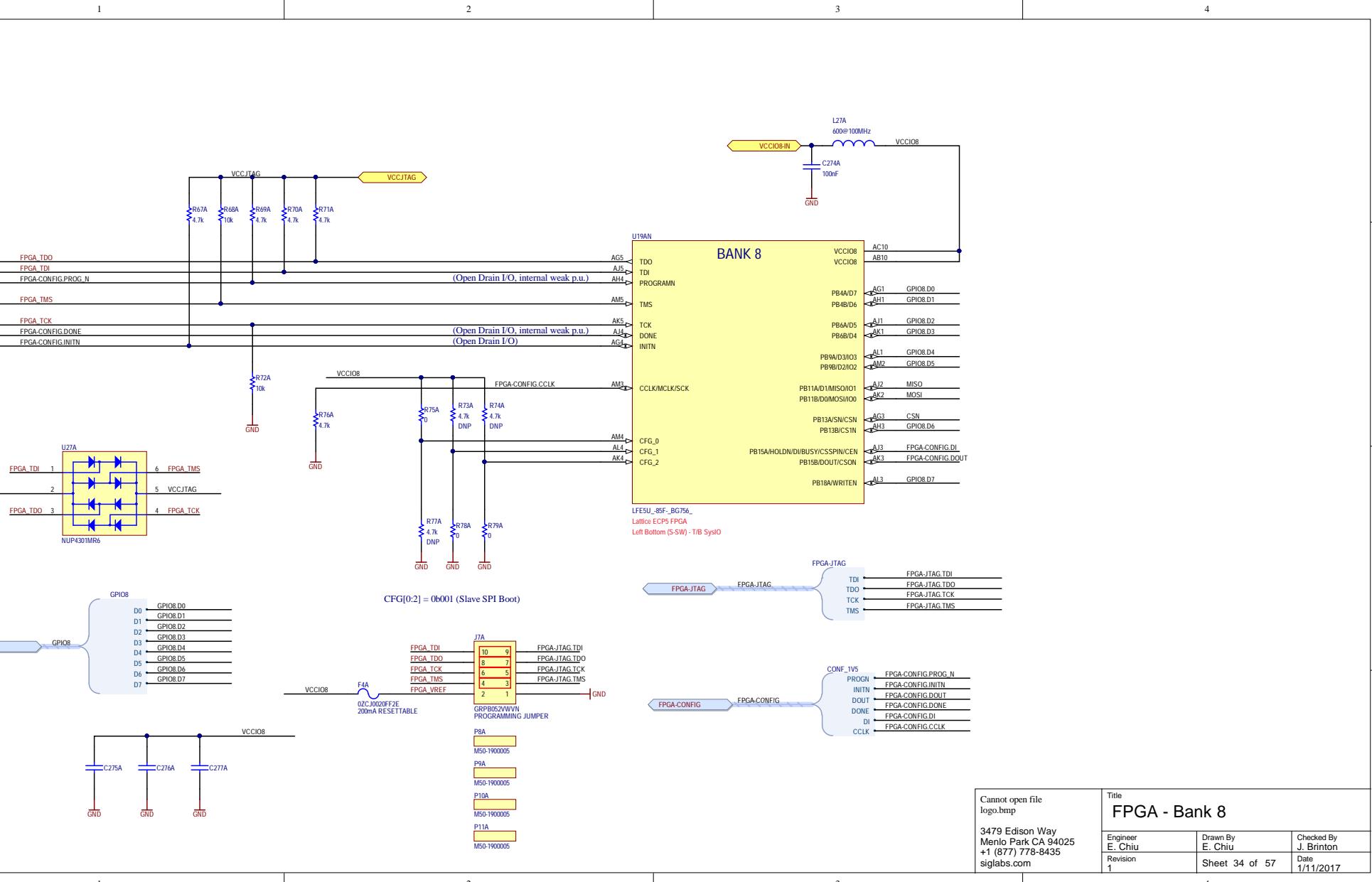
Sheet 33 of 57

Date

1/11/2017







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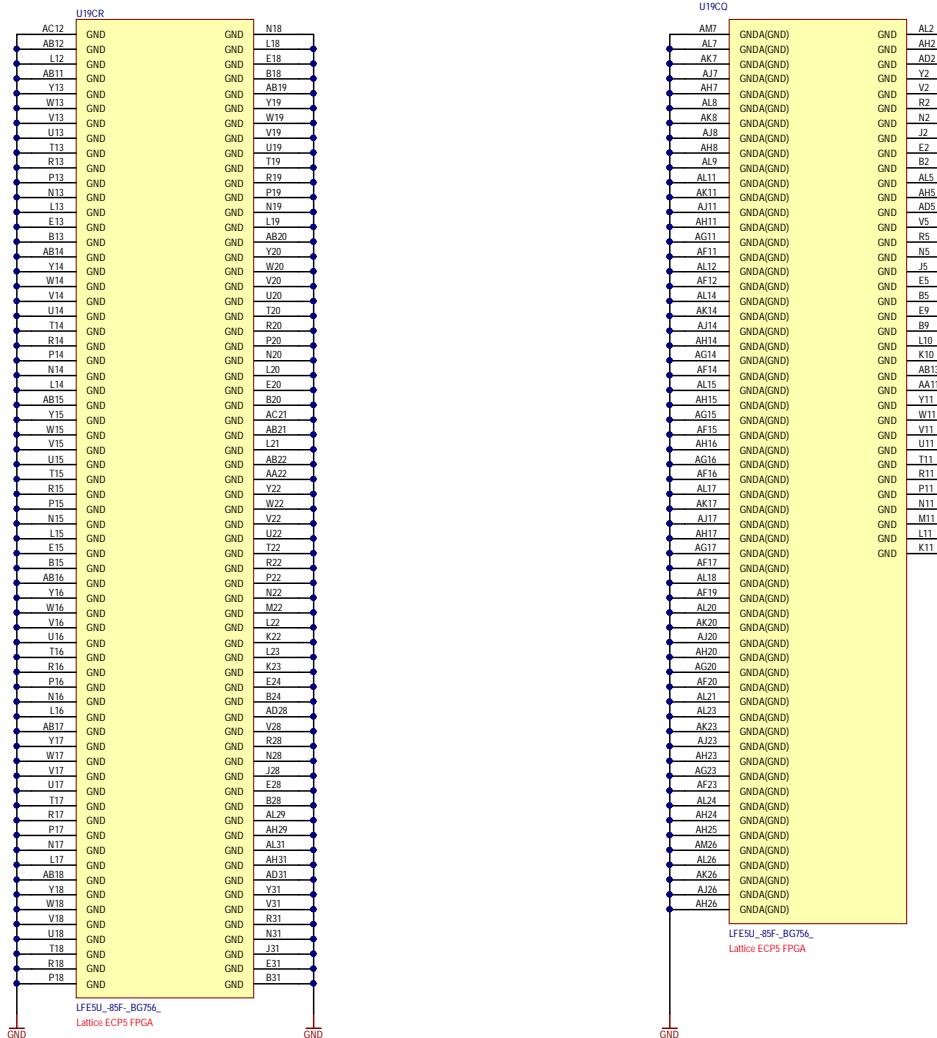
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Engineer
E. Chiu
Revision
1

Drawn By
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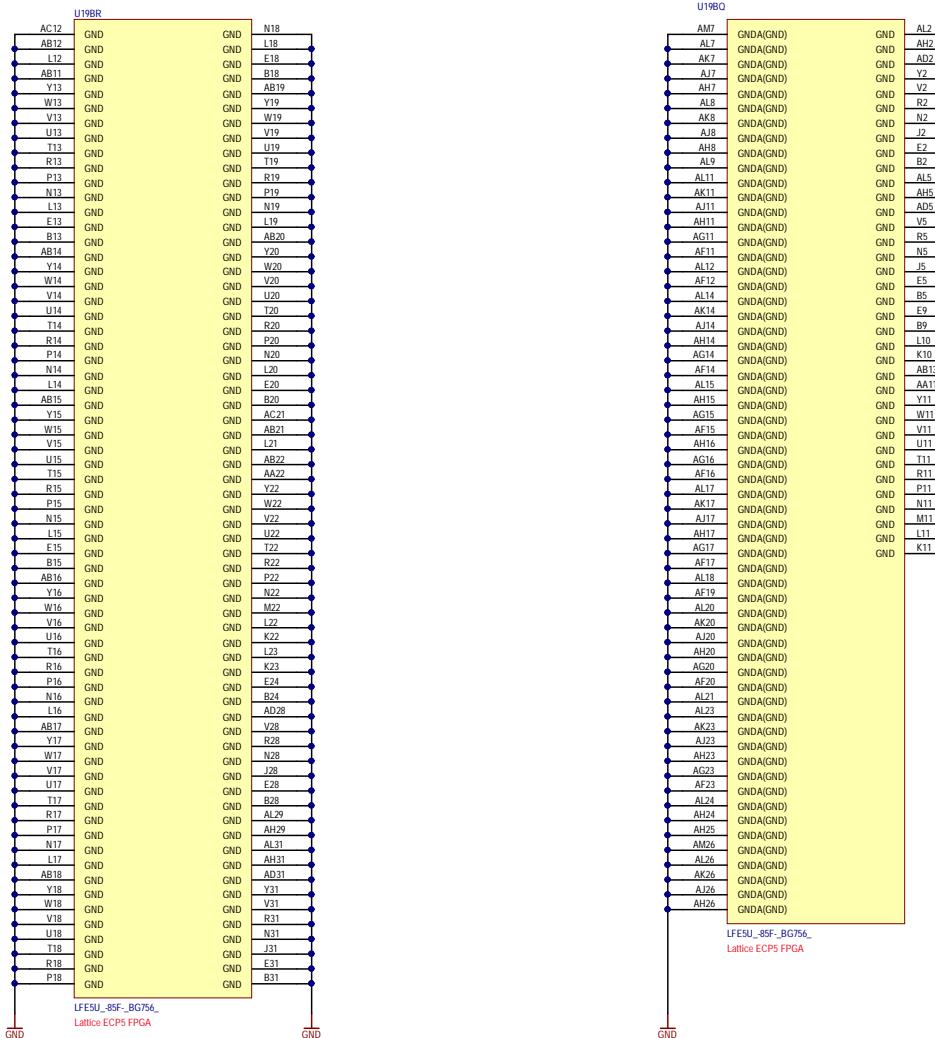
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E. Chiu
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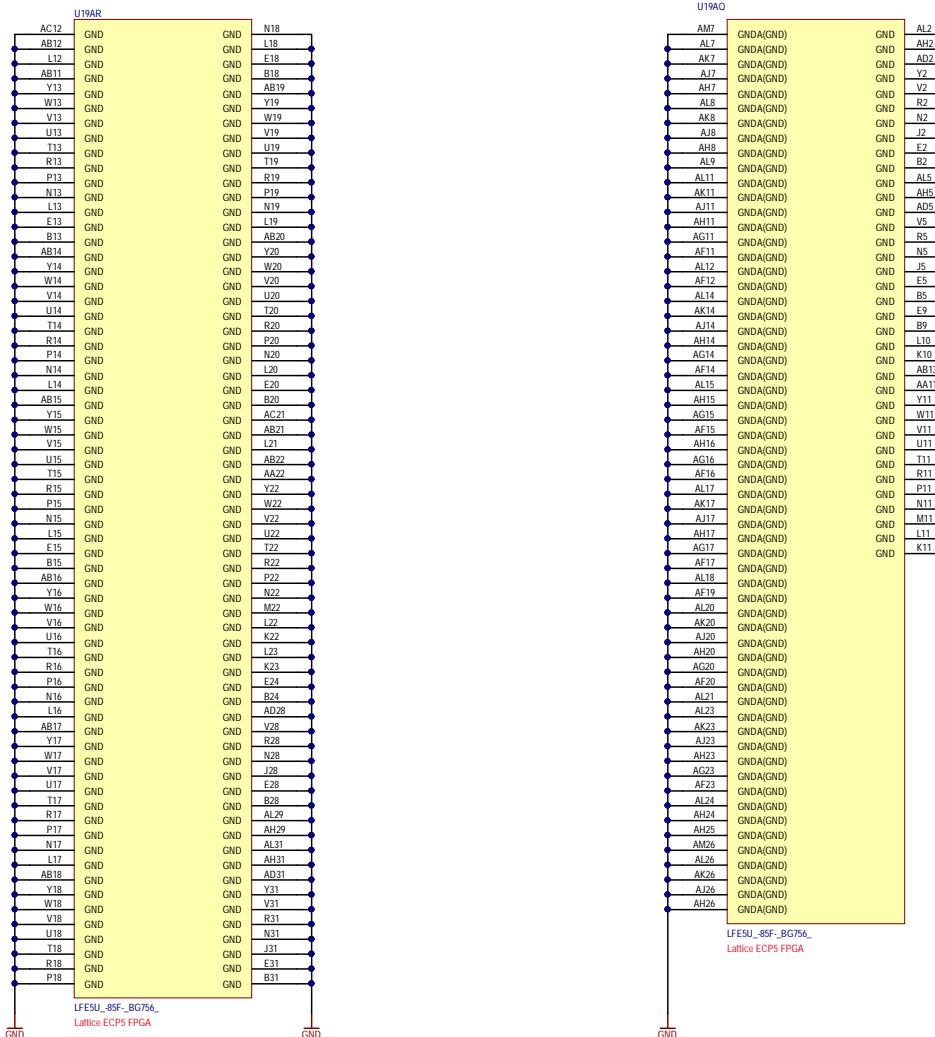
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Title
FPGA - Misc

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Title
FPGA - Misc

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Title
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Engineer	Drawn By	Checked By
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Revision 1	Sheet 36 of 57	Date 1/11/2017

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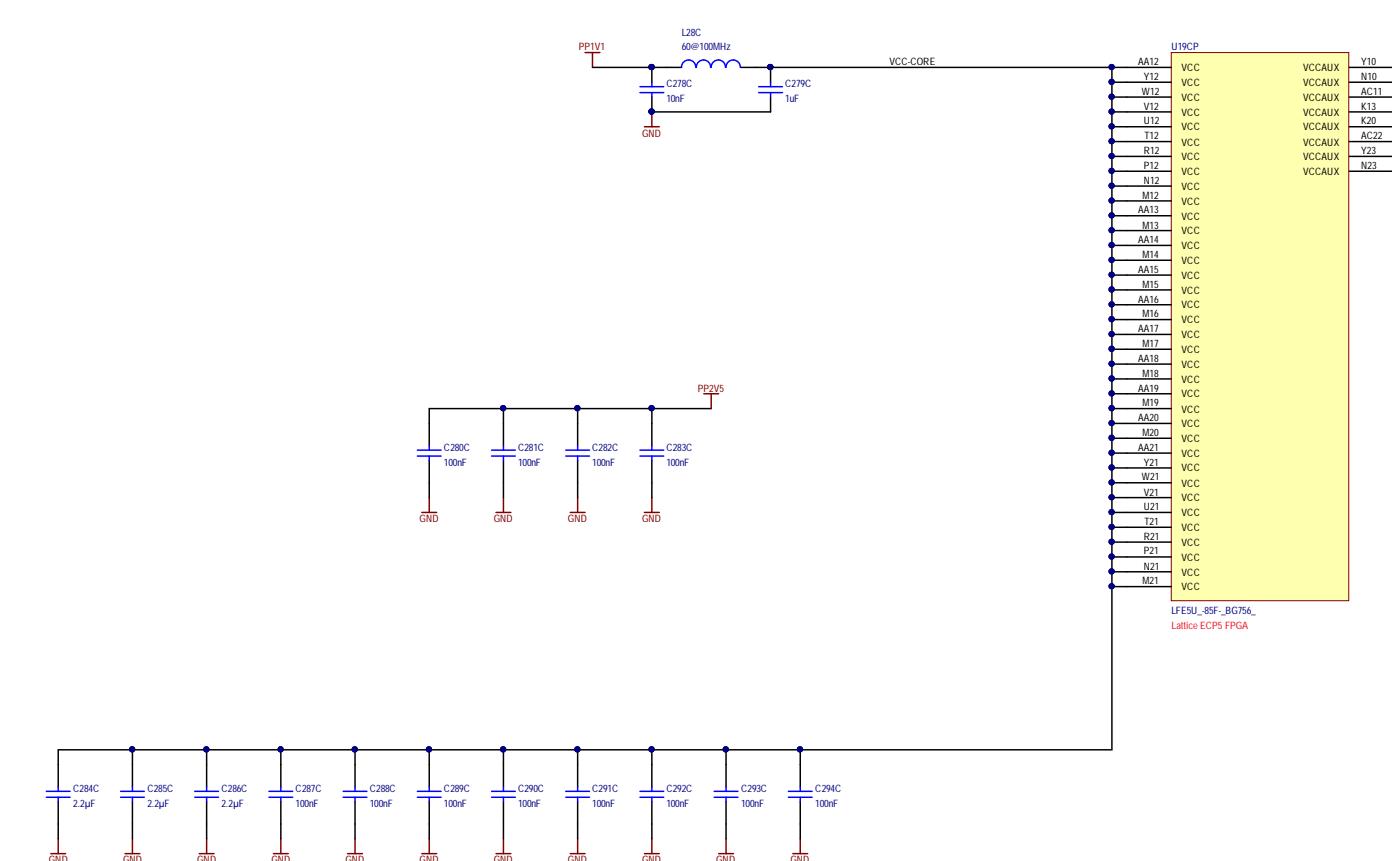
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FPGA - Power

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Engineer
E. Chiu

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1

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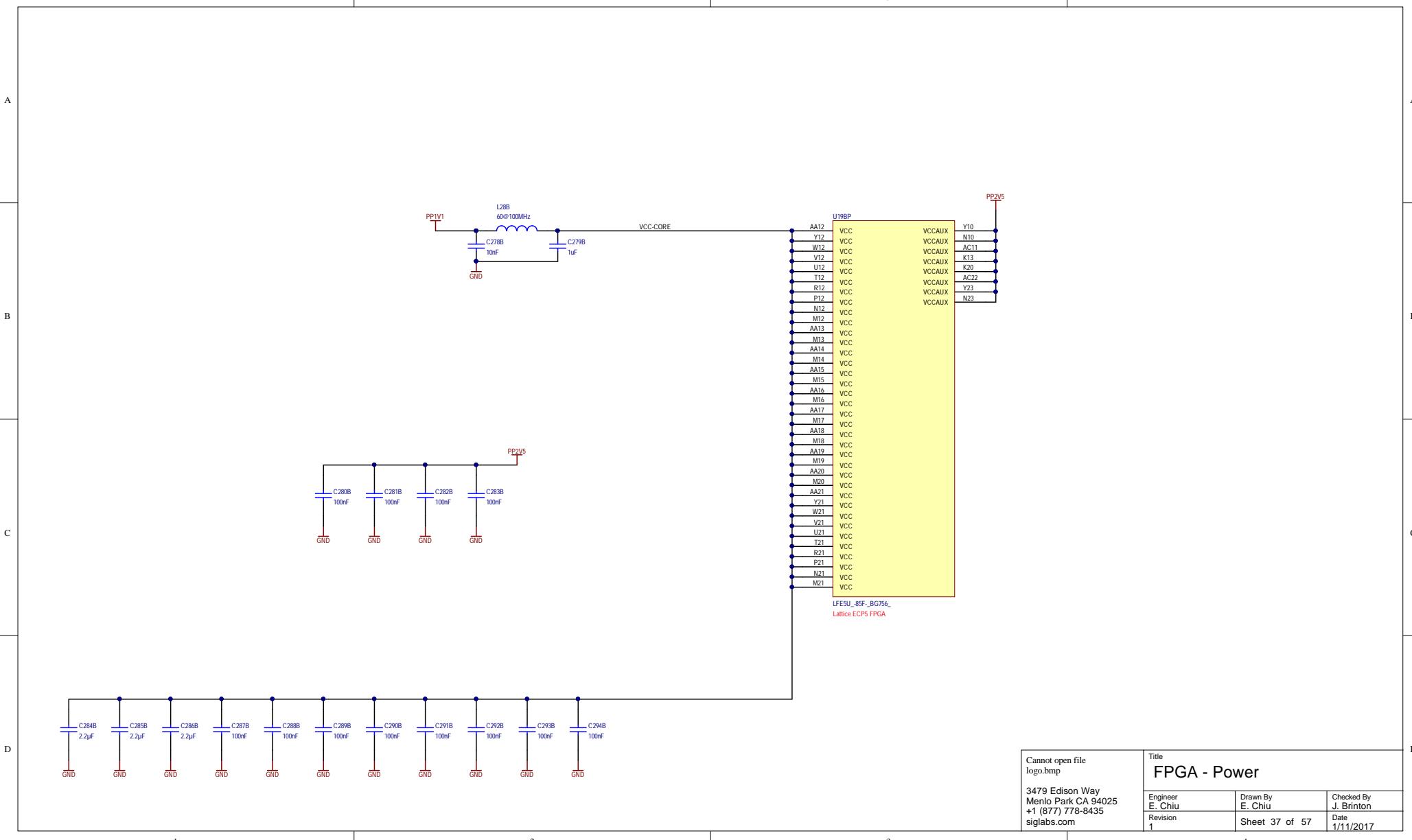
Date
1/11/2017

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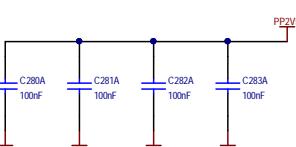
4



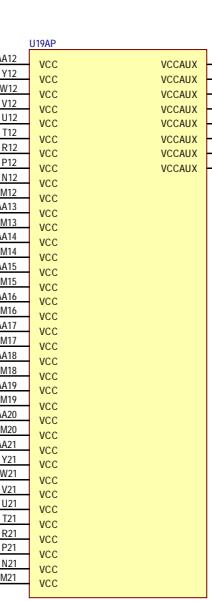
A



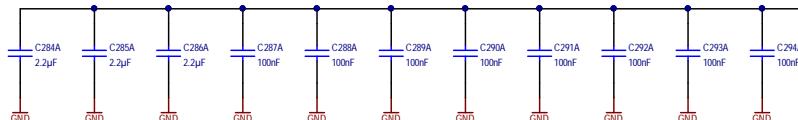
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Engineer
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Revision
1

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Date
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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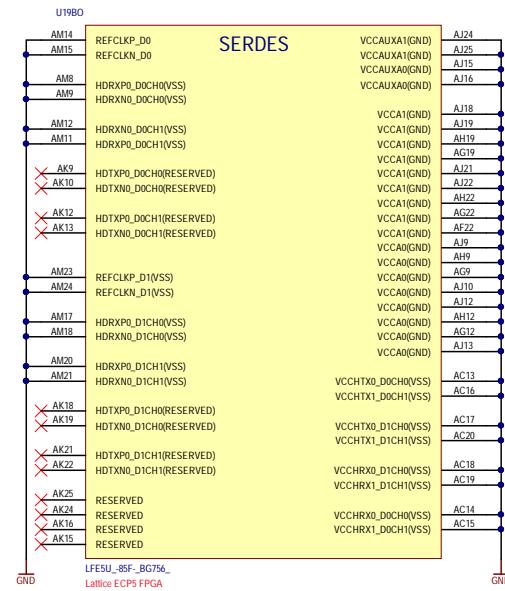
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Title
FPGA - Serdes

Engineer	Drawn By	Checked By
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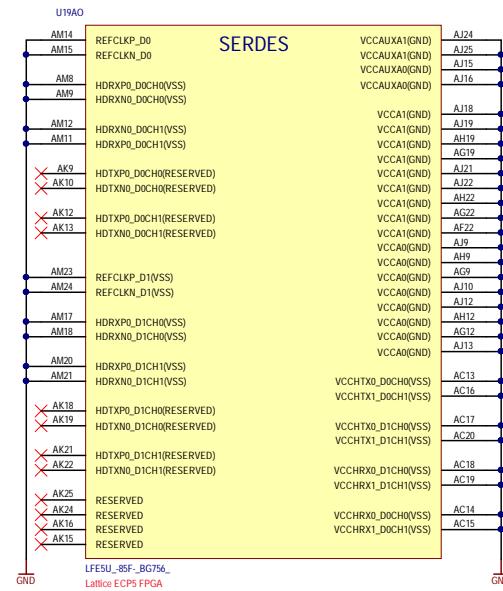
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logo.bmpTitle
FPGA - Serdes3479 Edison Way
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Engineer

Drawn By

Checked By

E. Chiu

E. Chiu

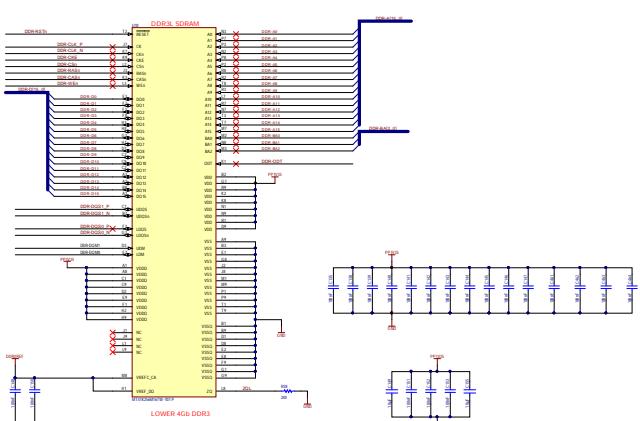
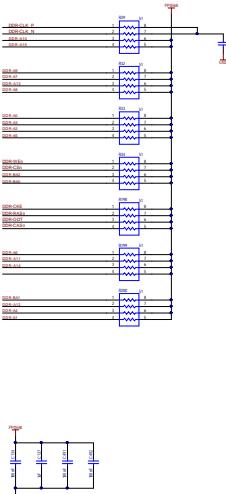
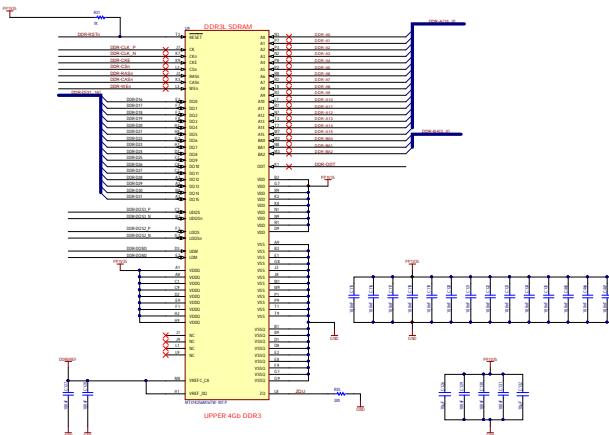
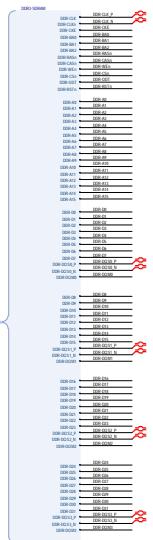
Revision

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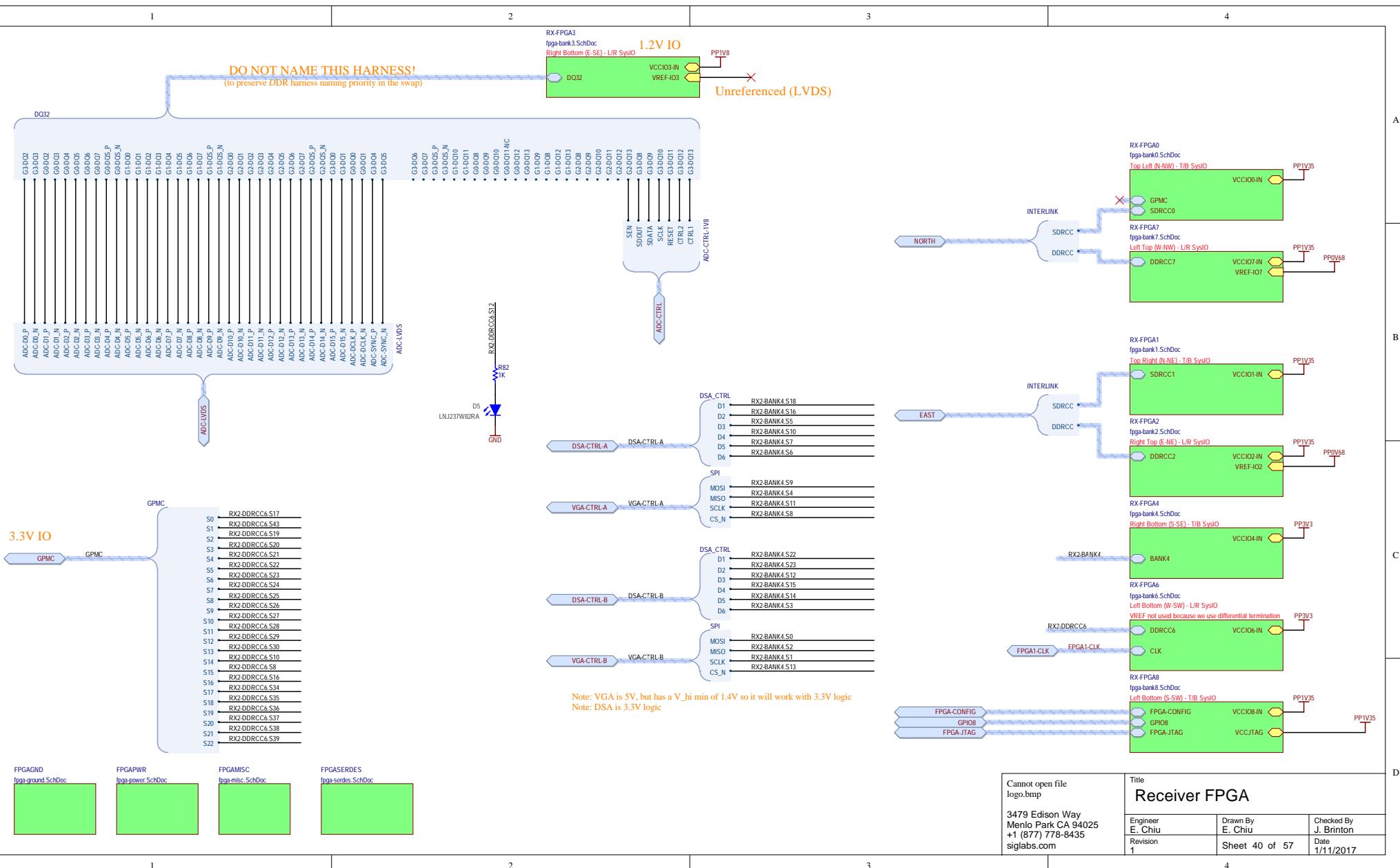
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LAYOUT NOTE: FLY-BY ROUTING

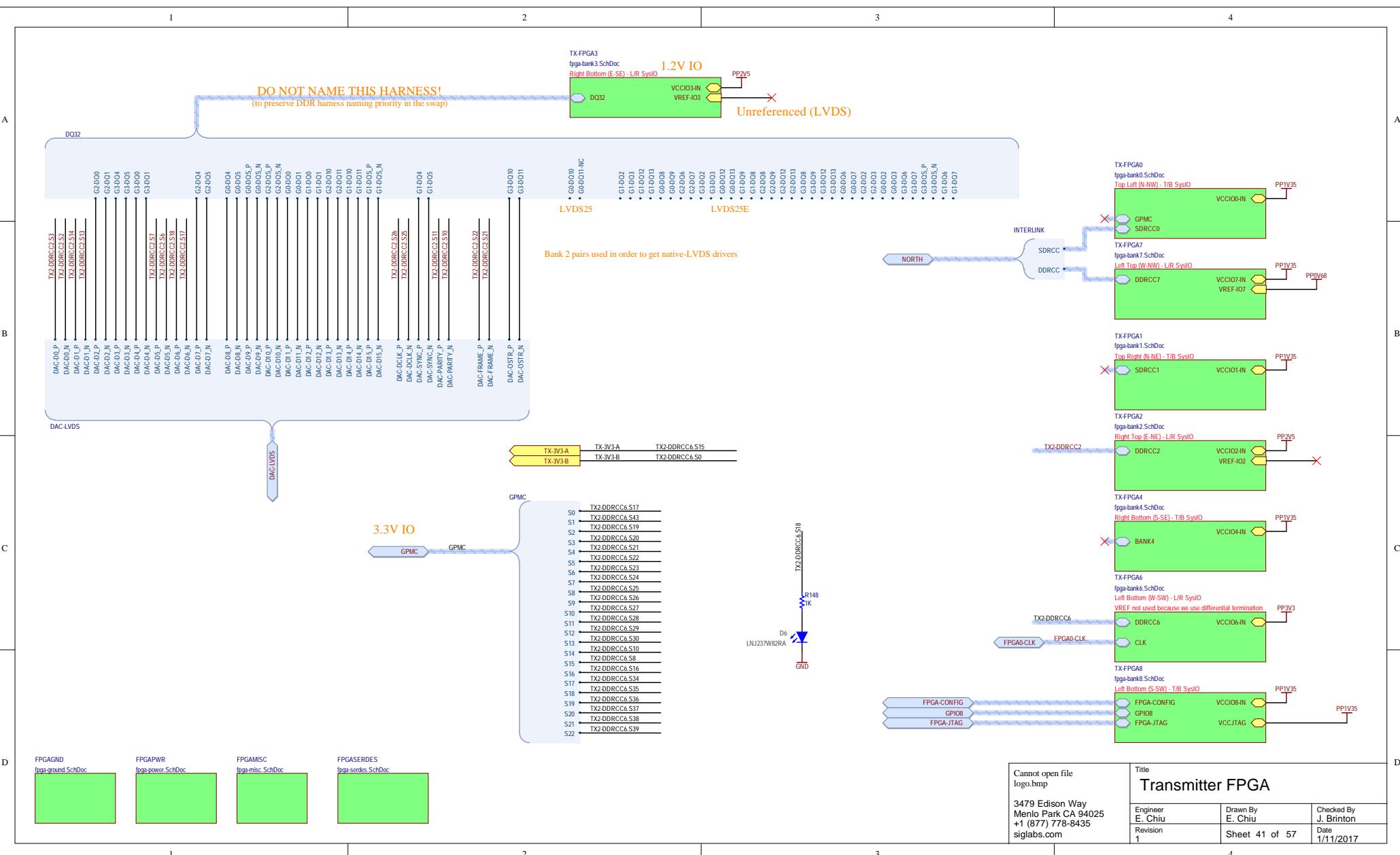
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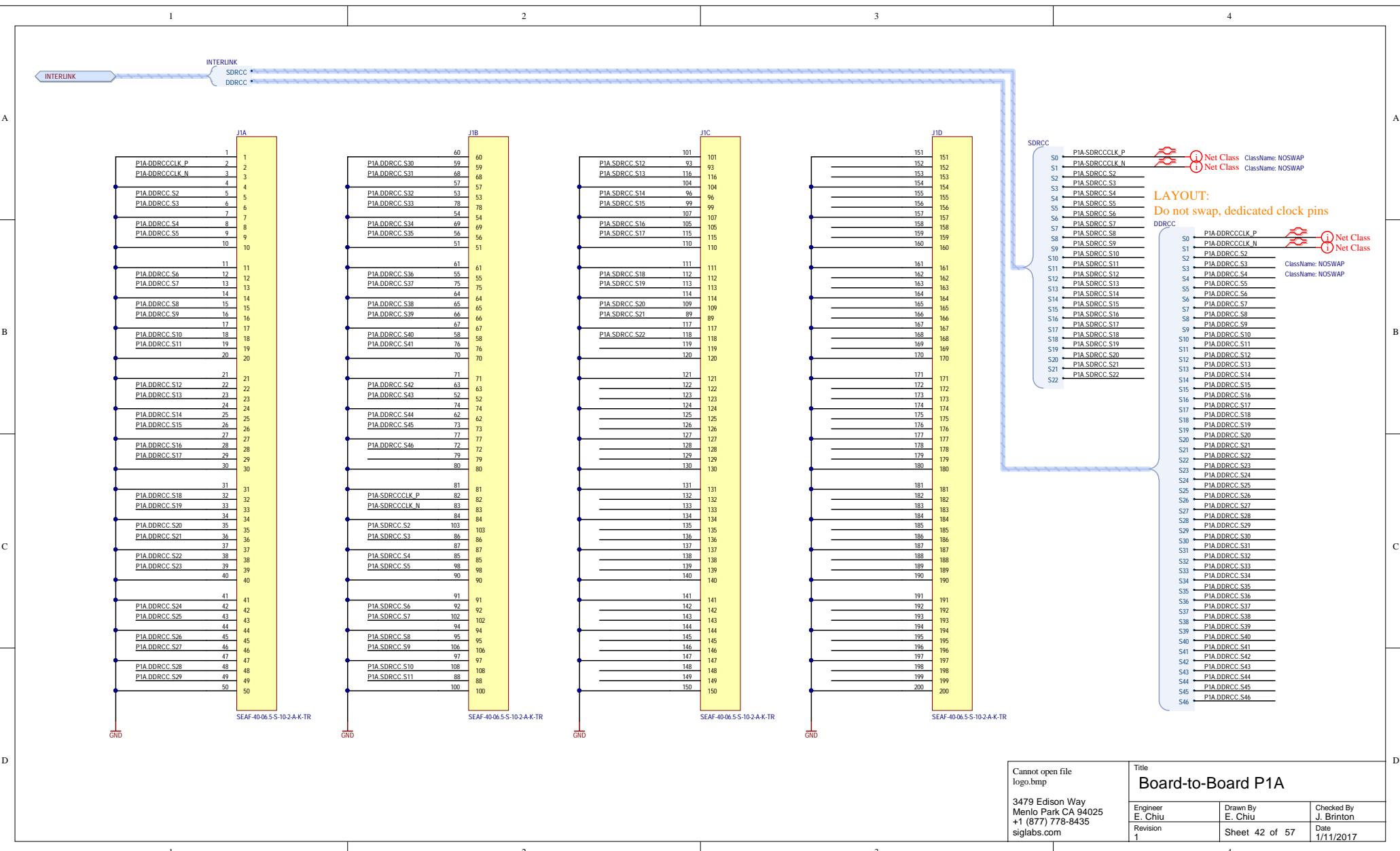
Title:
DDR3 SDRAM
Date: 09/01/2017
Version: 1.0
Created By: E. Chiu
Checked By: E. Chiu
Reviewed By: J. Brown
Approved By: J. Brown

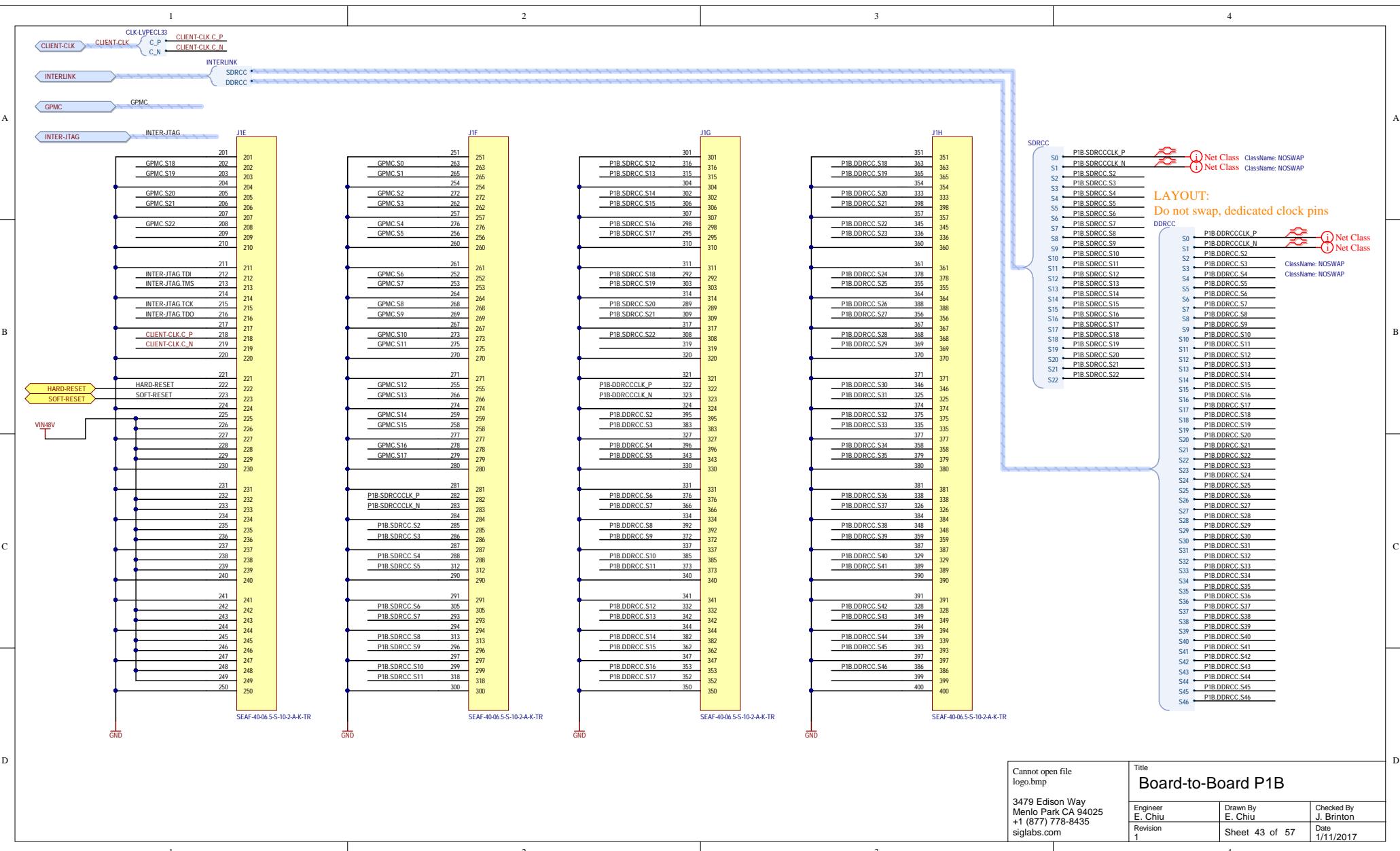
Control
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Search help
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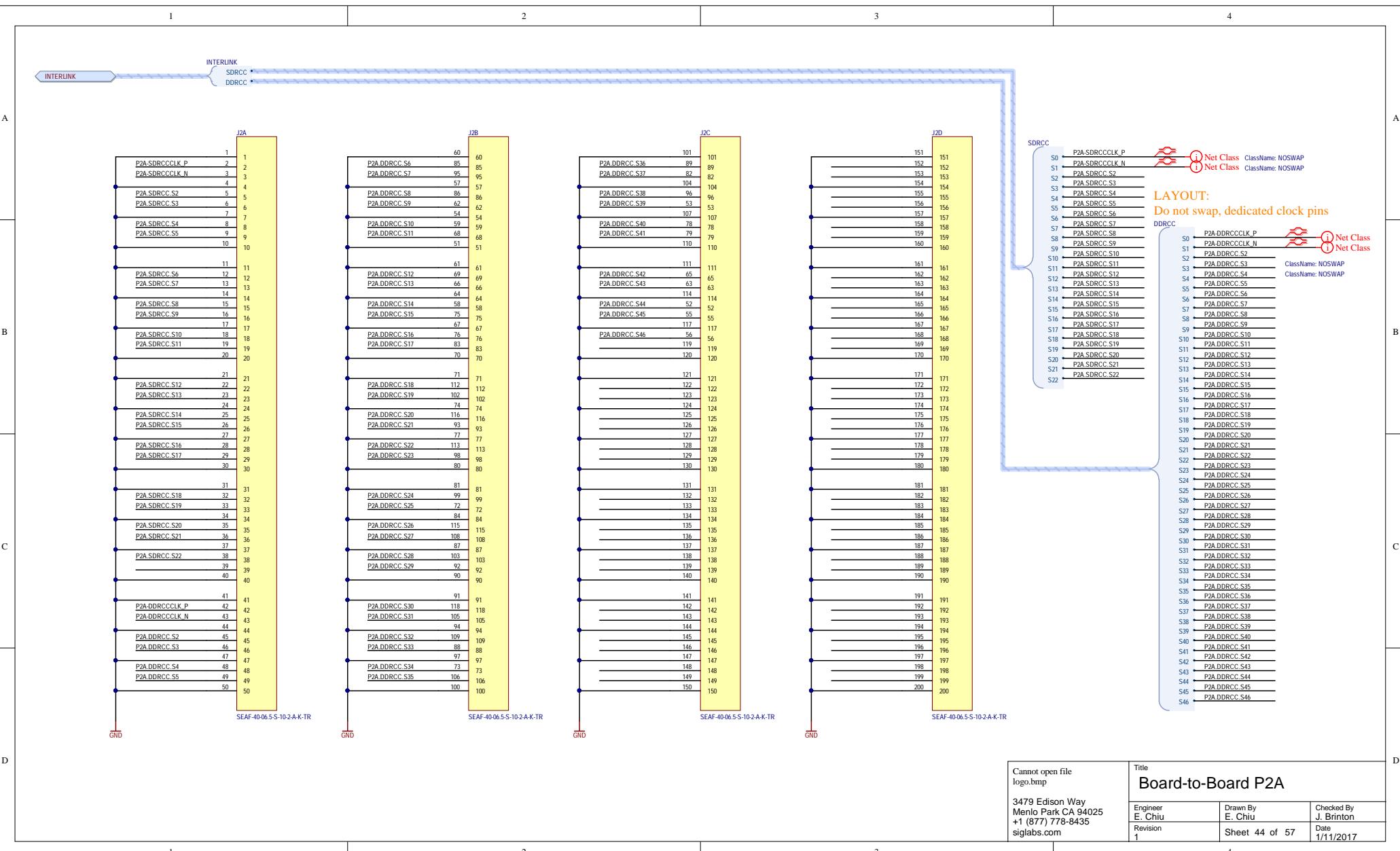
Title:
DDR3 SDRAM
Date: 09/01/2017
Version: 1.0
Created By: E. Chiu
Checked By: E. Chiu
Reviewed By: J. Brown
Approved By: J. Brown

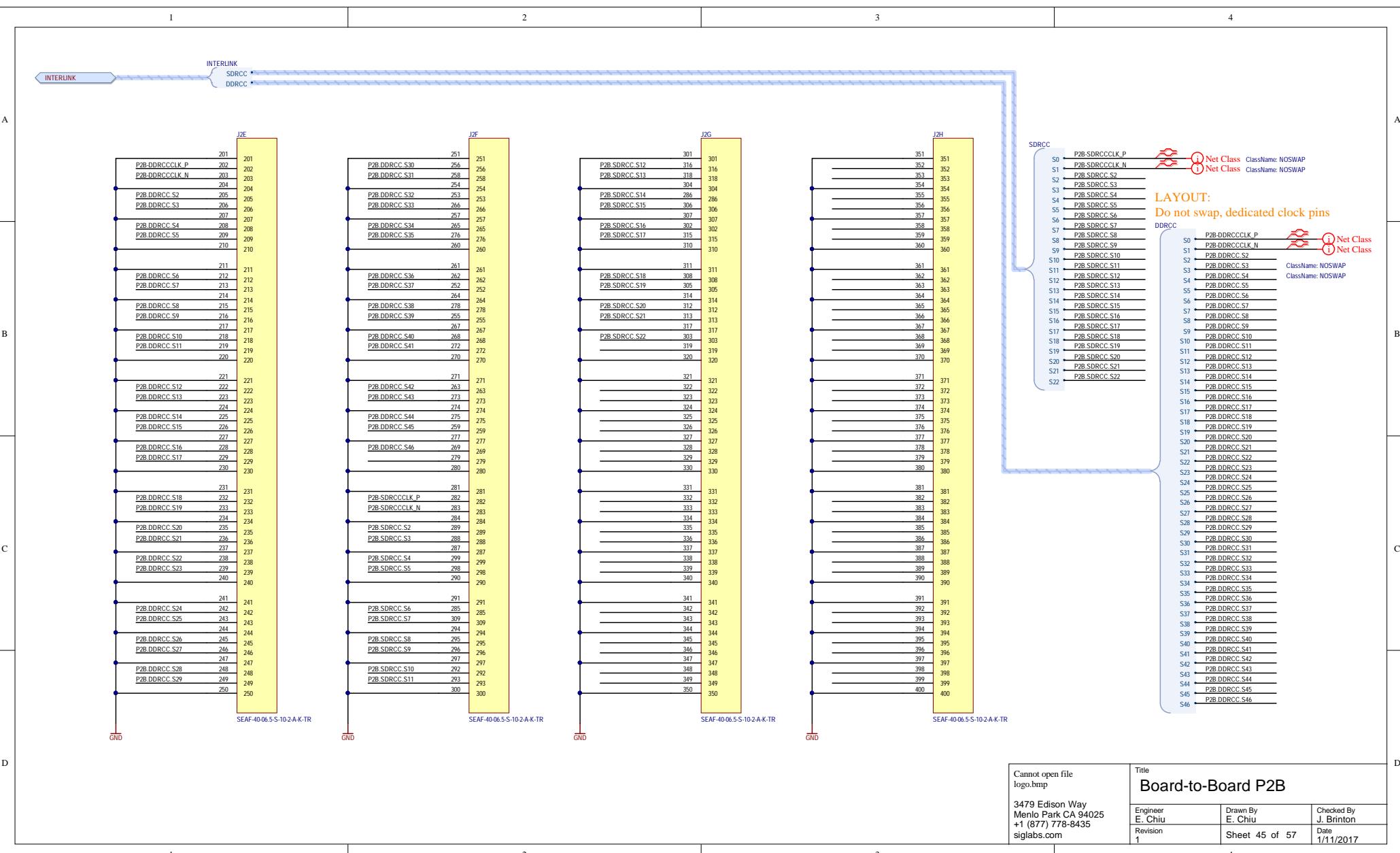


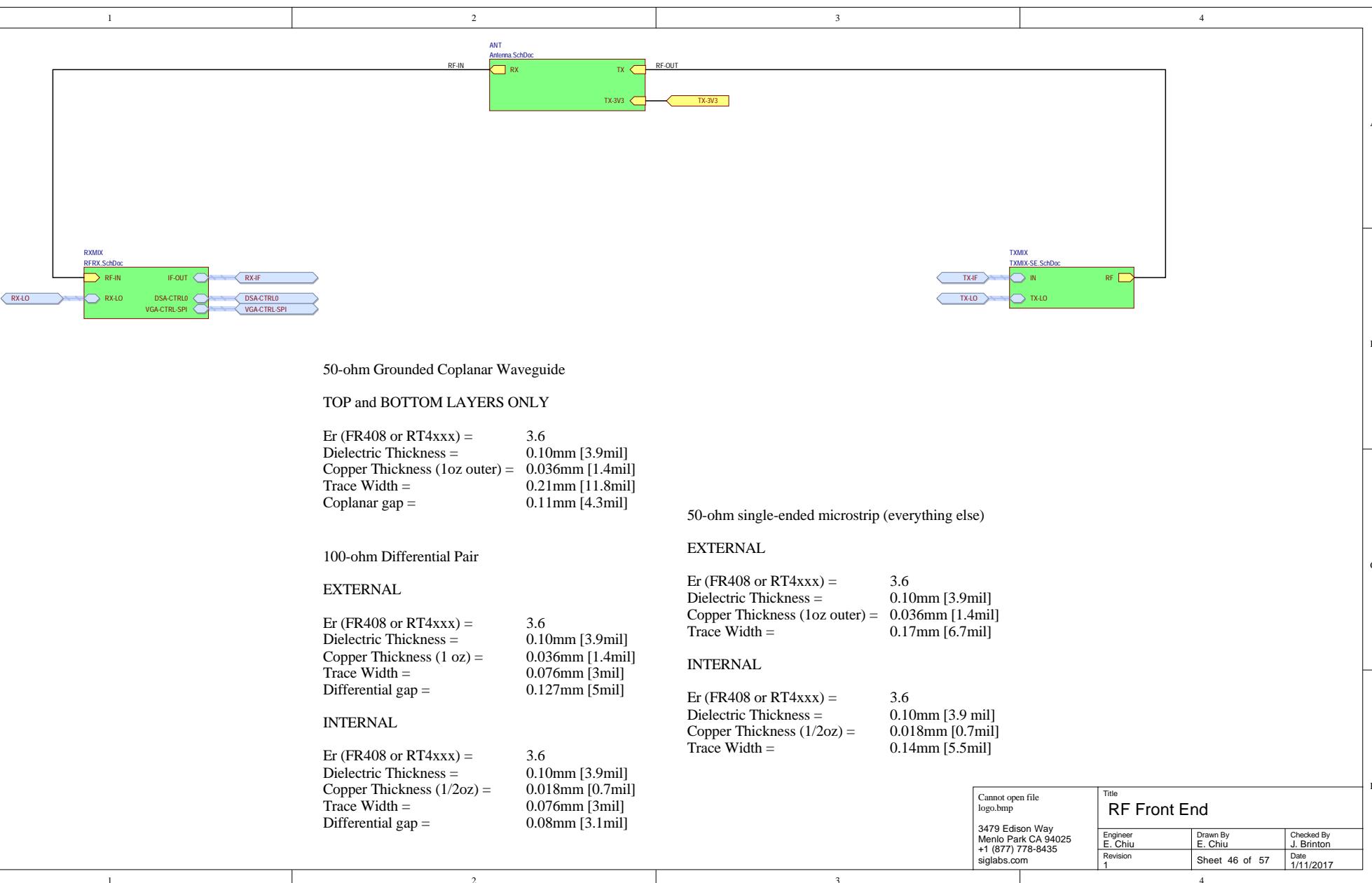


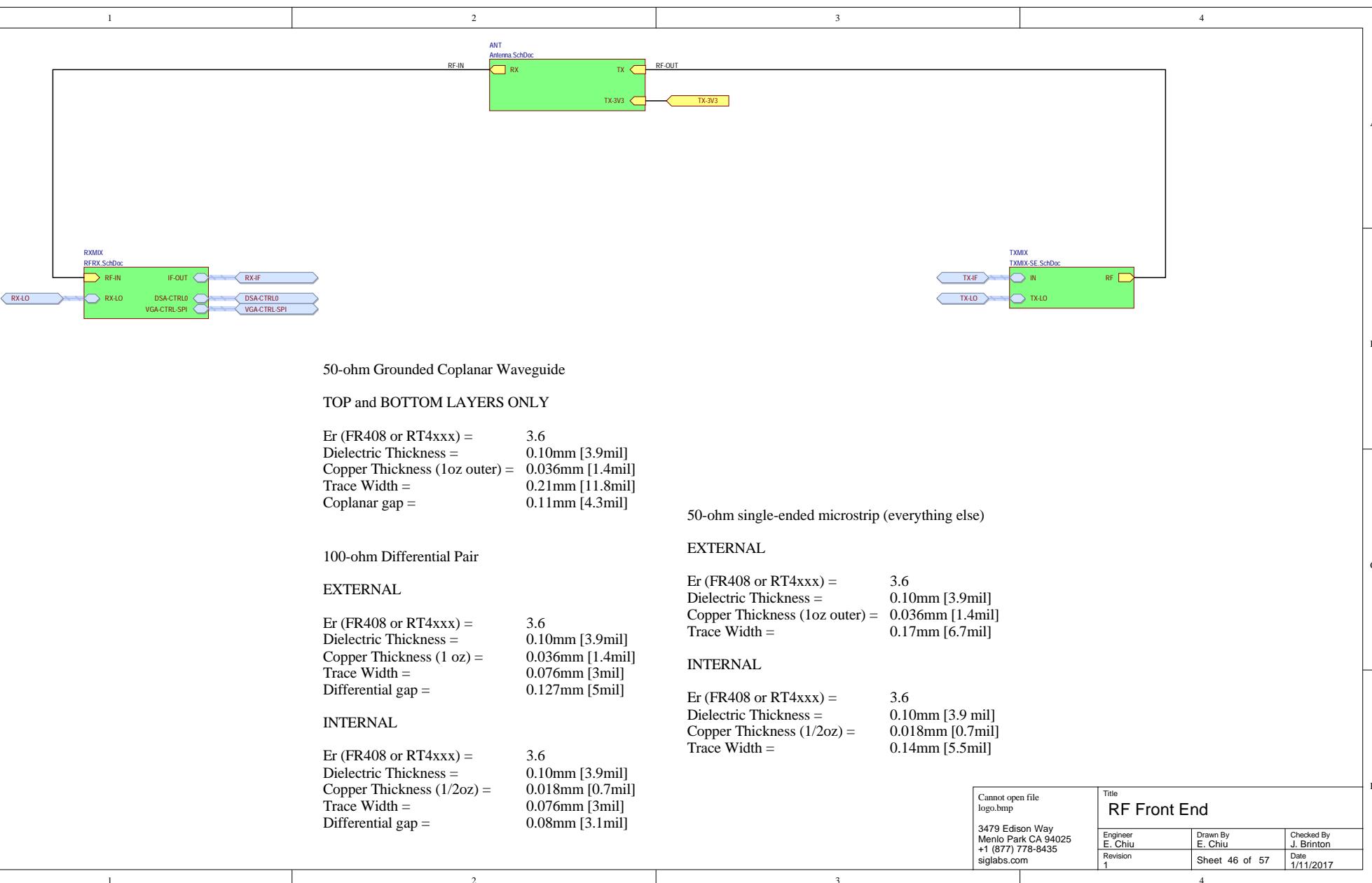


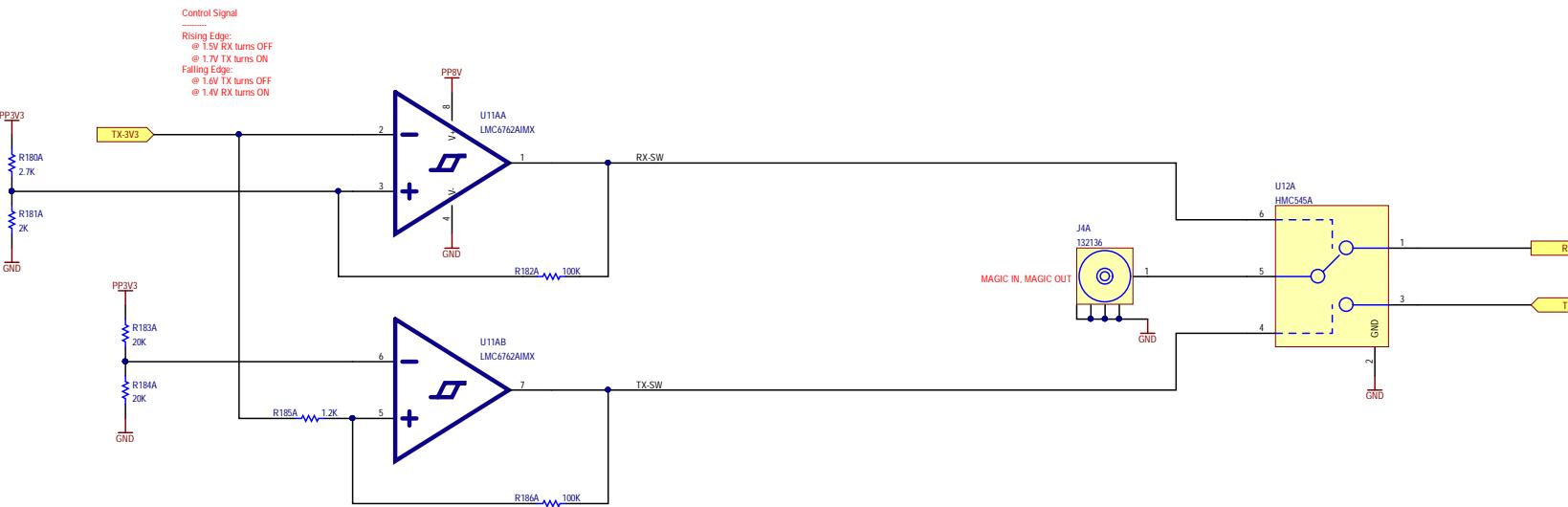










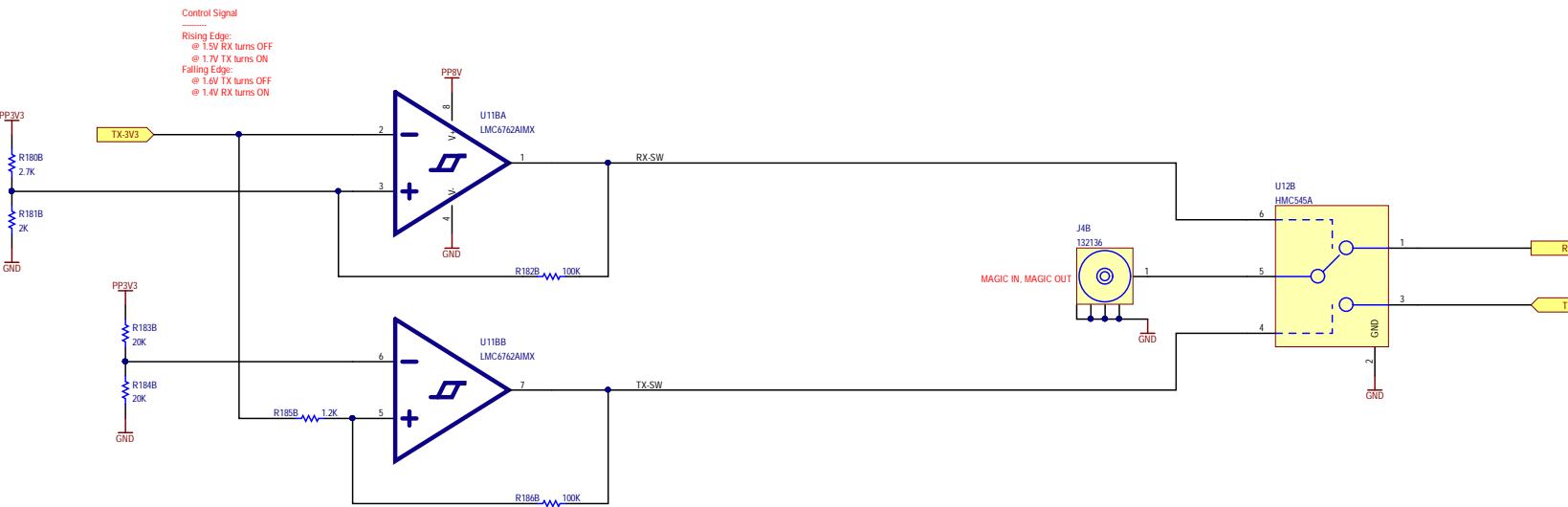


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Title
Antenna Switch

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Revision 1	Sheet 47 of 57	Date 1/11/2017



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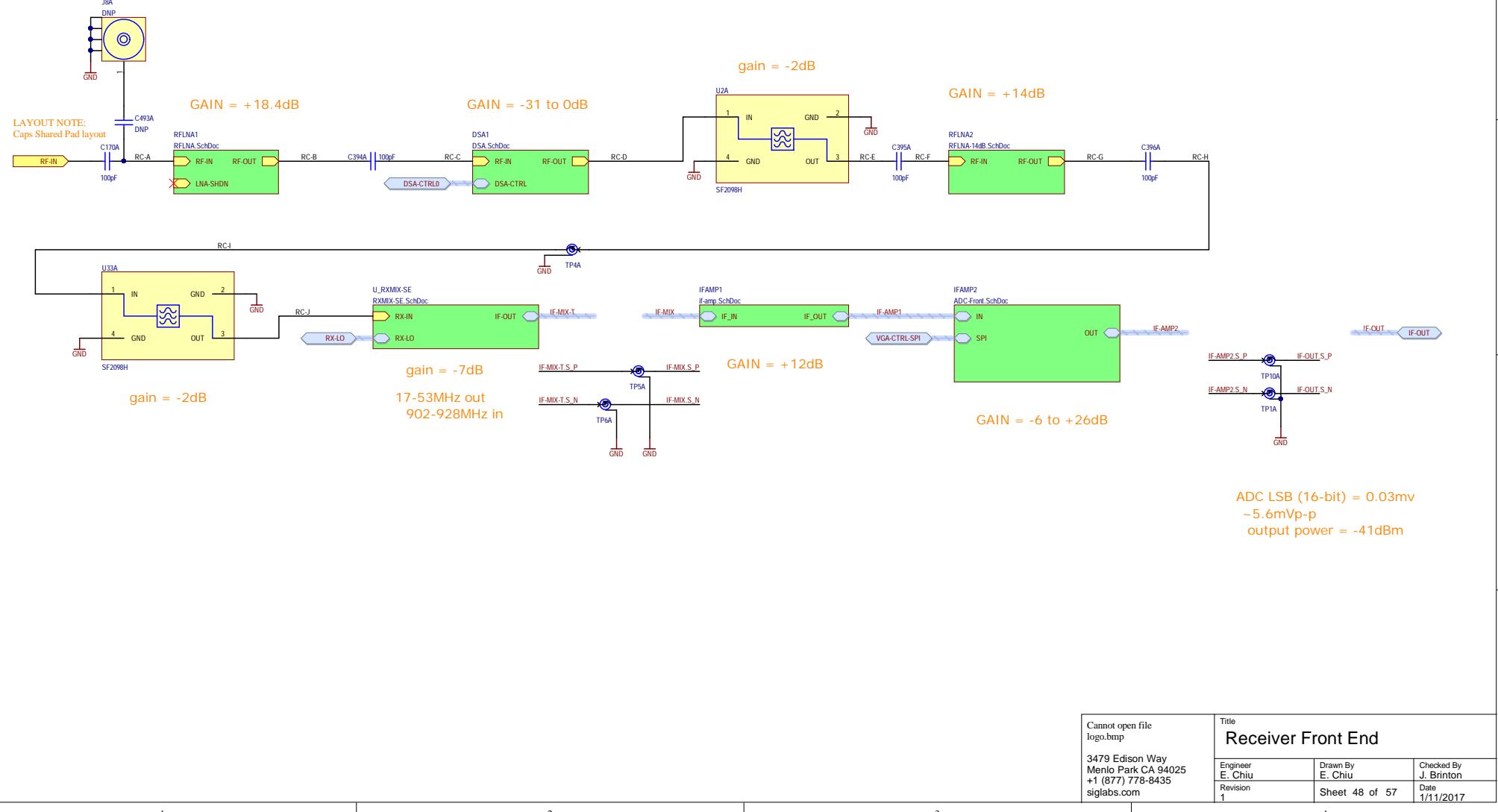
Title
Antenna Switch

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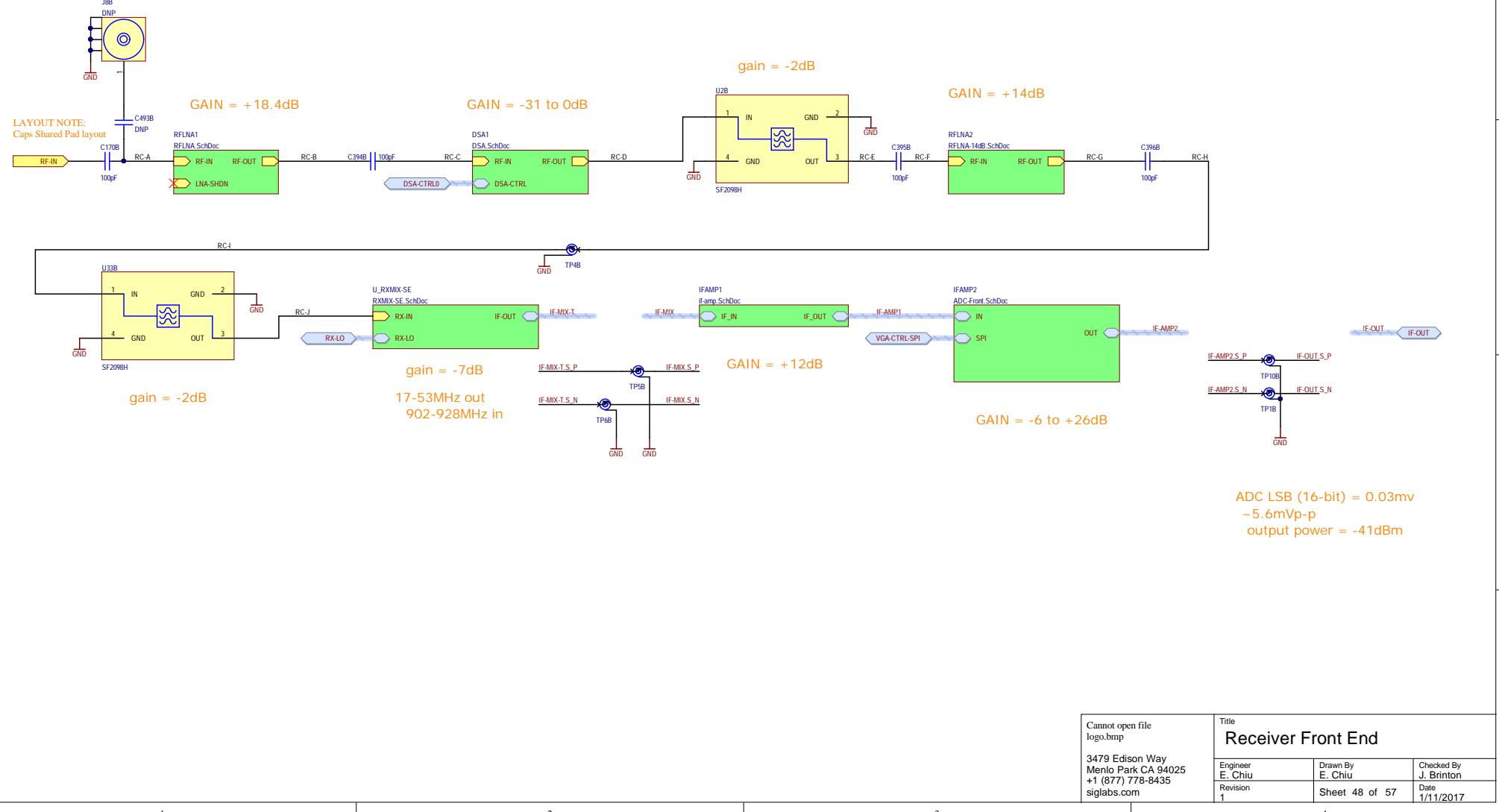
noise floor power @ 26MHz BW = -100dBm
loudest received signal - 16dBm



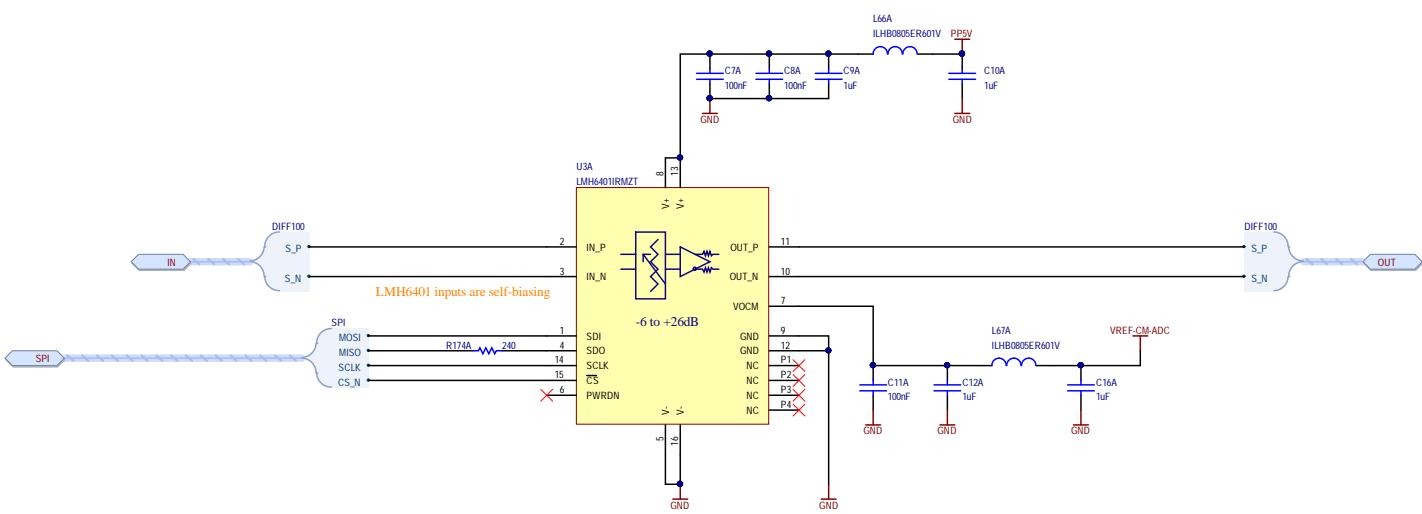
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noise floor power @ 26MHz BW = -100dBm
loudest received signal - 16dBm



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logo.bmp

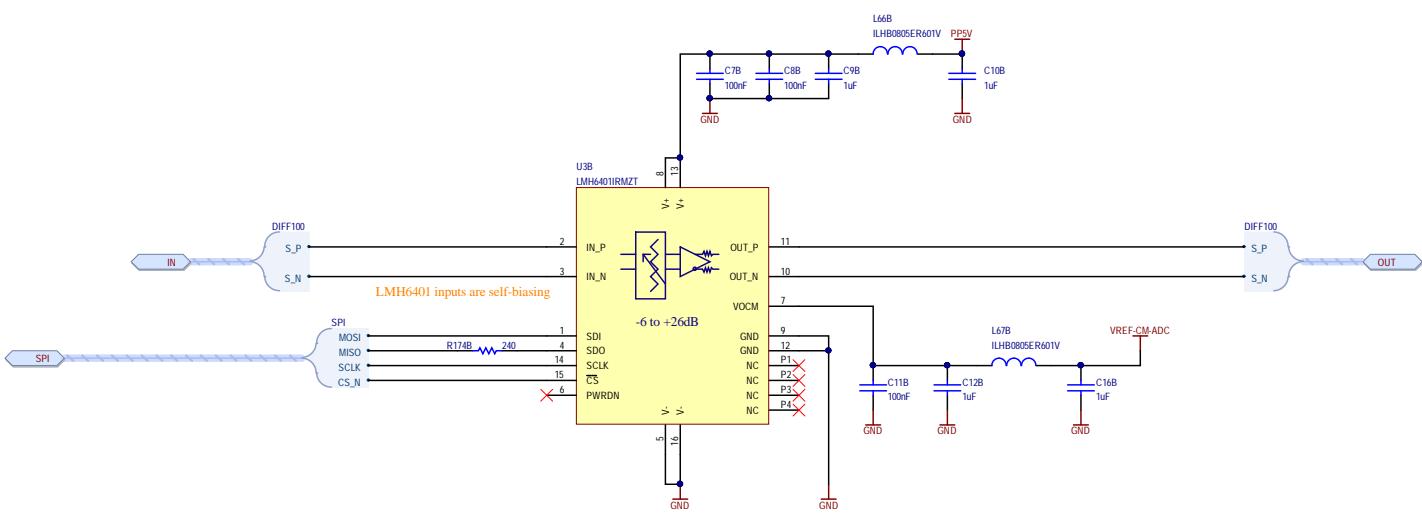
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ADC Frontend

Engineer
E. Chiu
Revision
1

Drawn By
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Date
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ADC Frontend

Engineer
E. Chiu

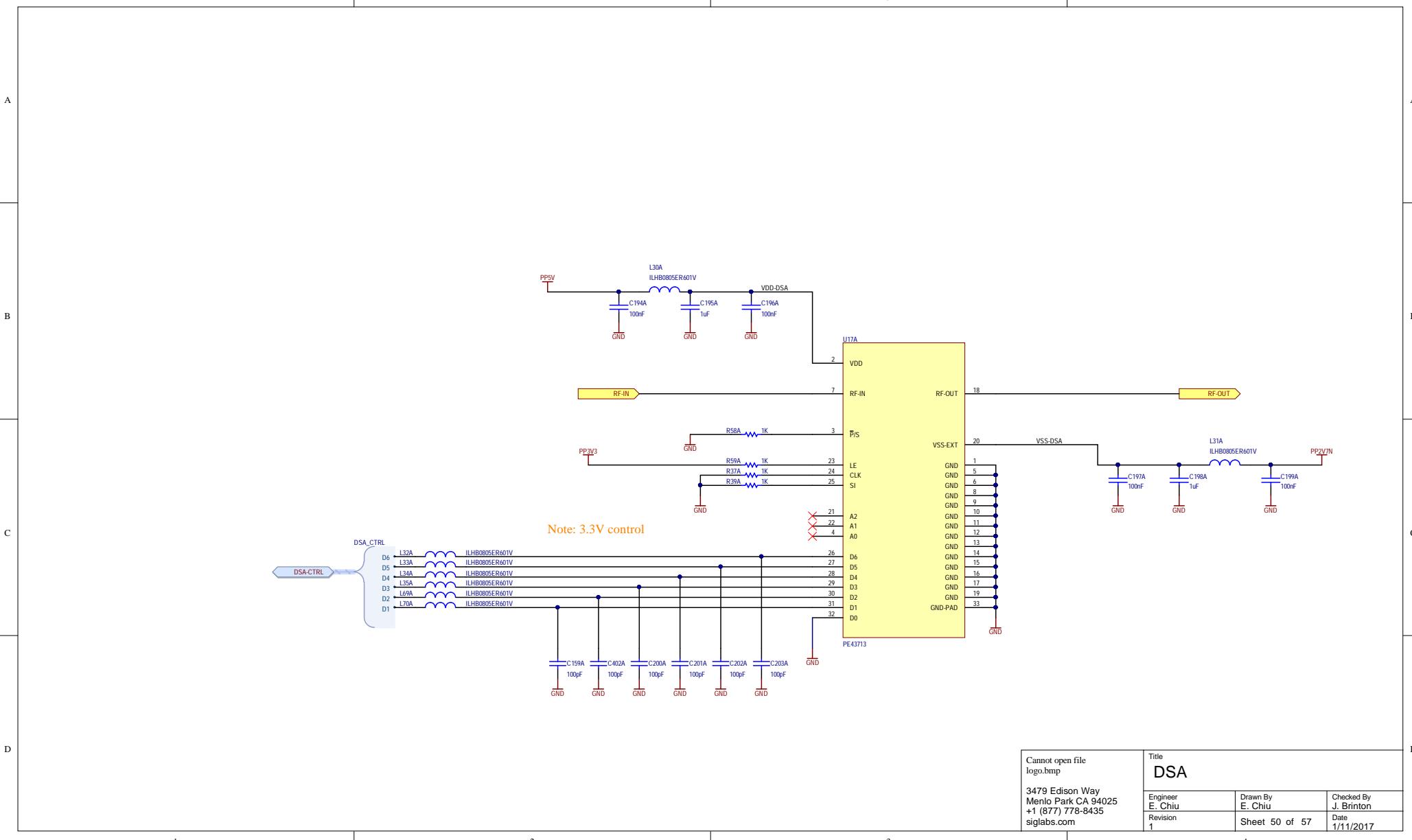
Drawn By
E. Chiu

Checked By
J. Brinton

Revision
1

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Date
1/11/2017



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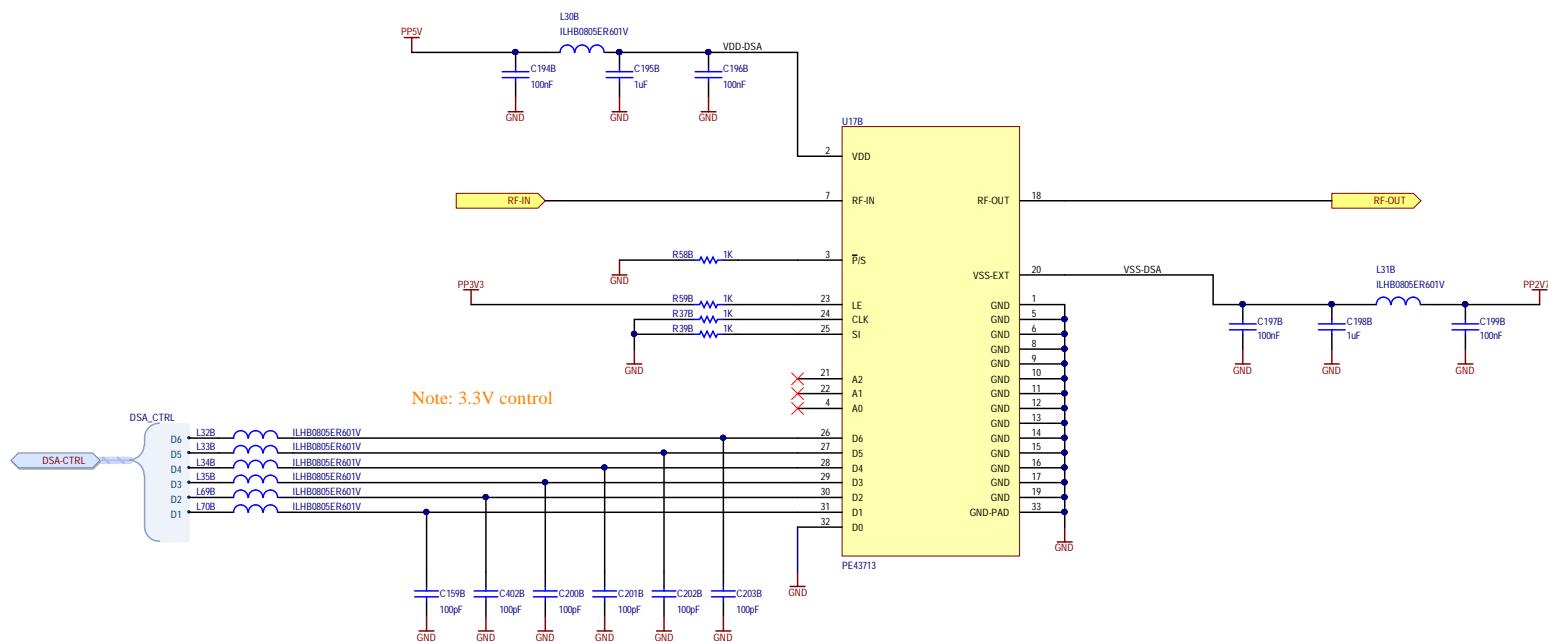
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Title
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Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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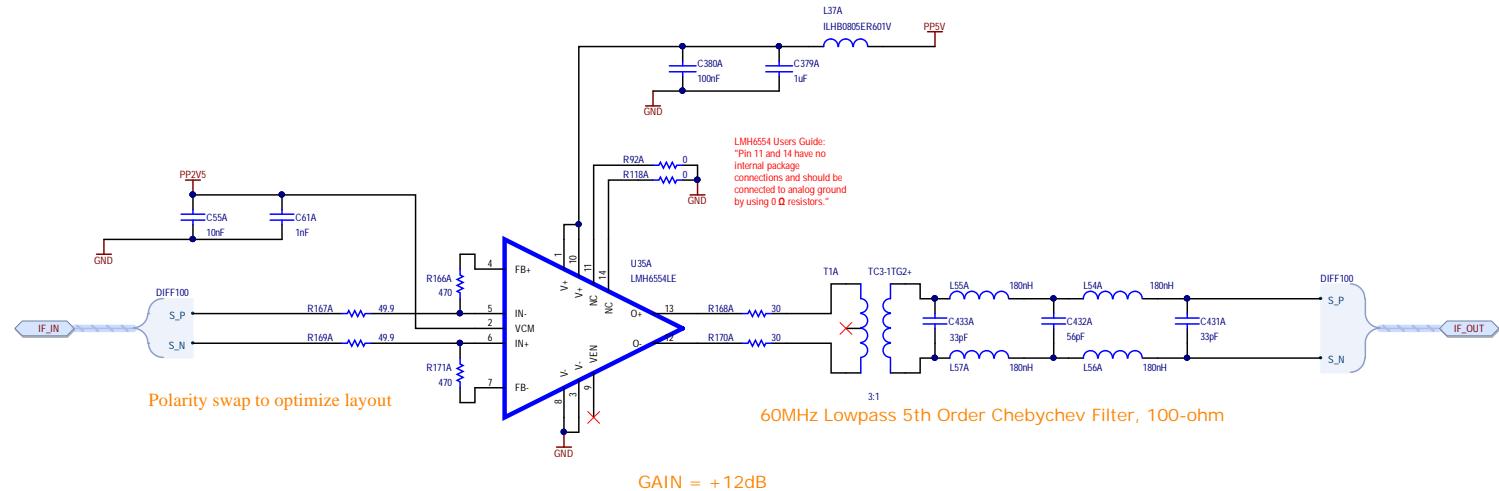
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Title
IF Amplifier

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Engineer
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Drawn By
E. Chiu

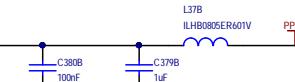
Checked By
J. Brinton

Revision
1

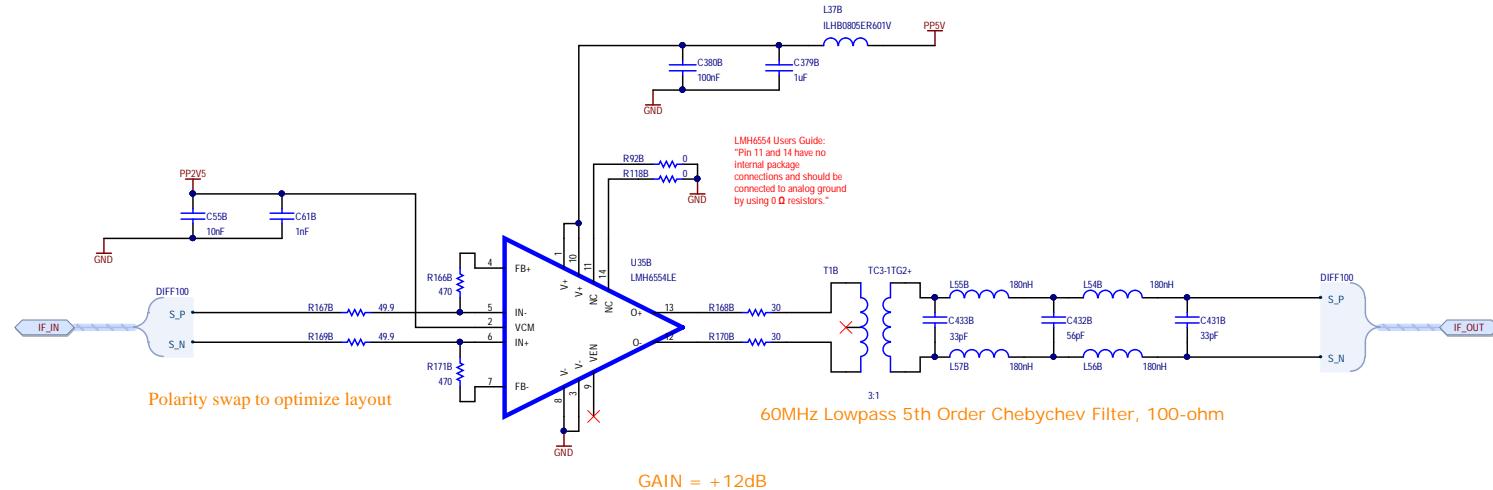
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A



LMH6554 Users Guide:
"Pin 11 and 14 have no
internal package
connections and should be
connected to analog ground
by using 0 Ω resistors."



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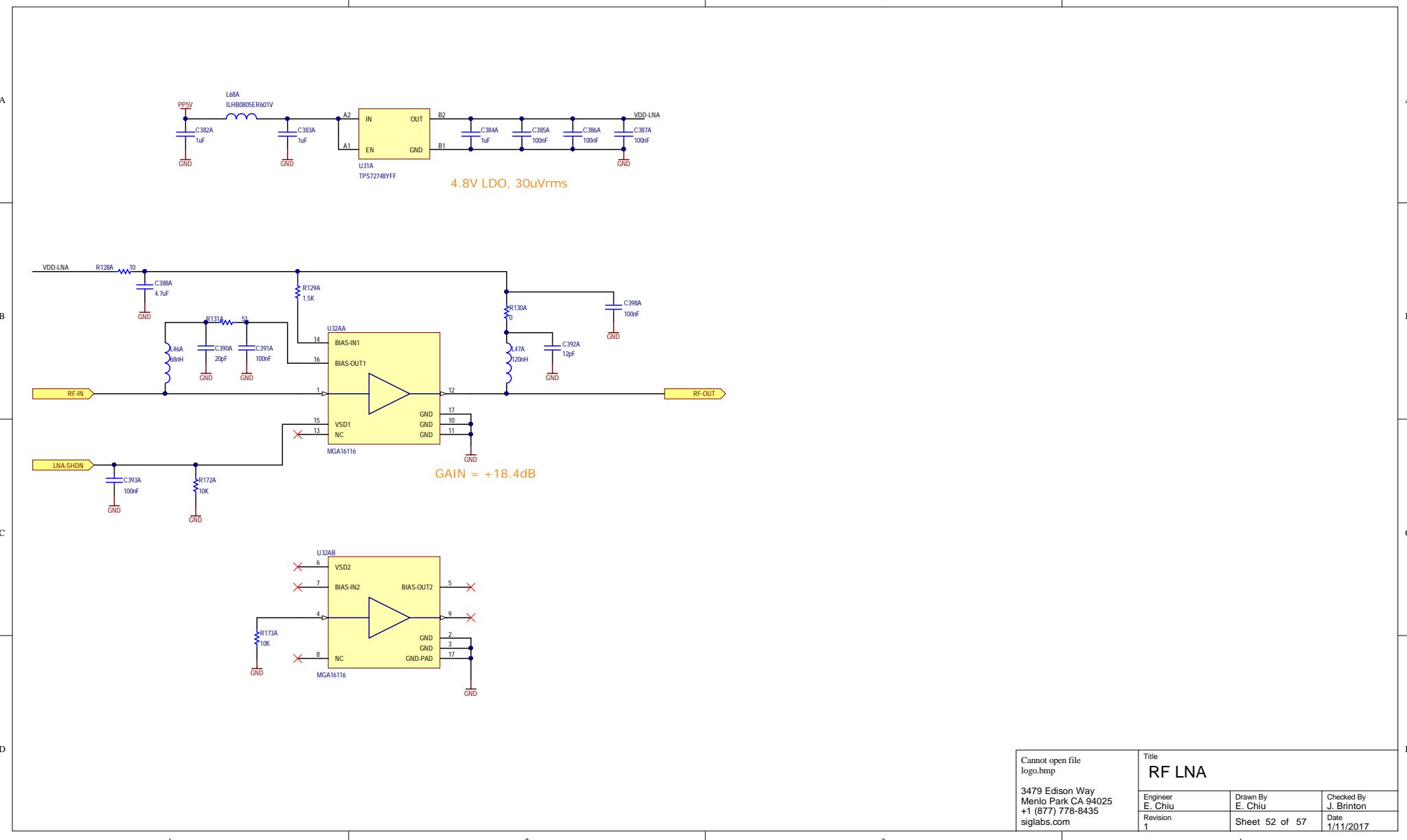
Cannot open file
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IF Amplifier

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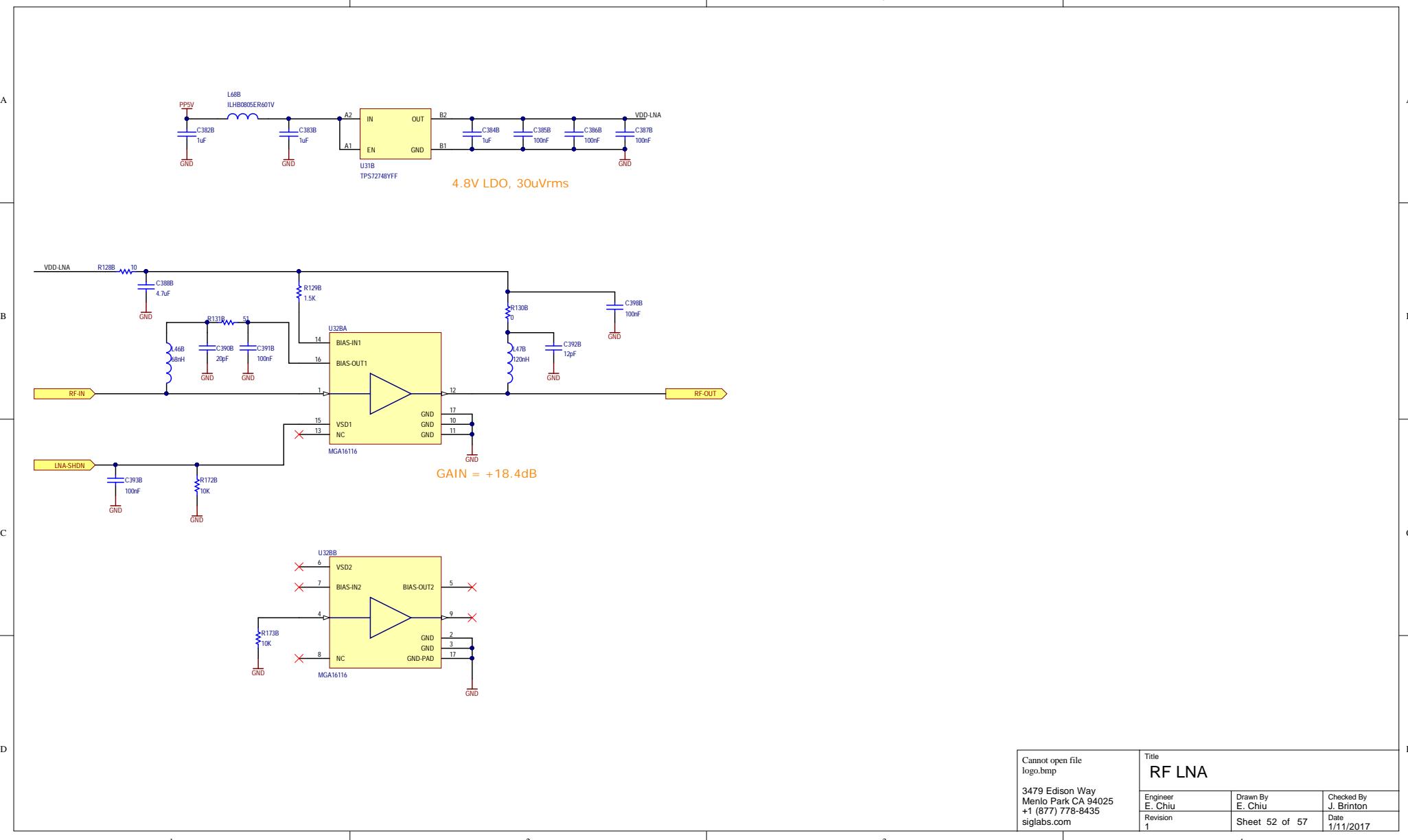
Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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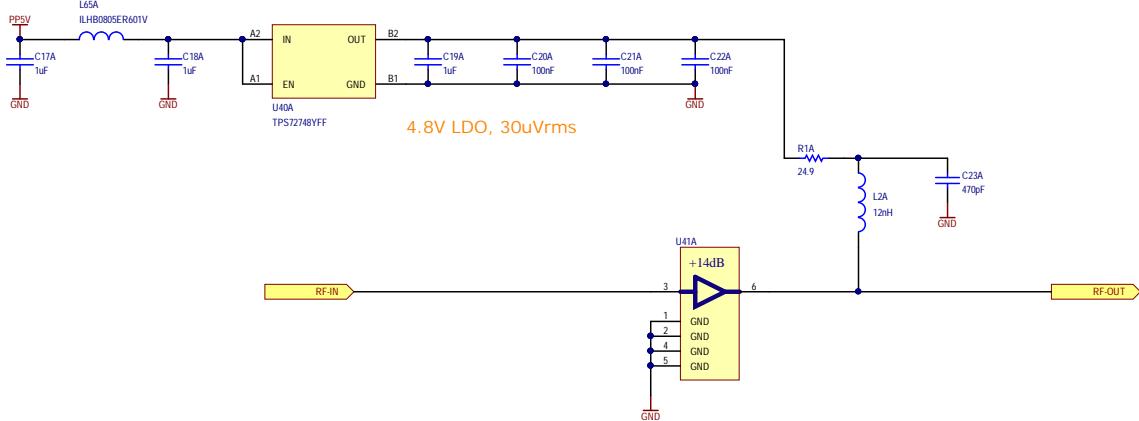
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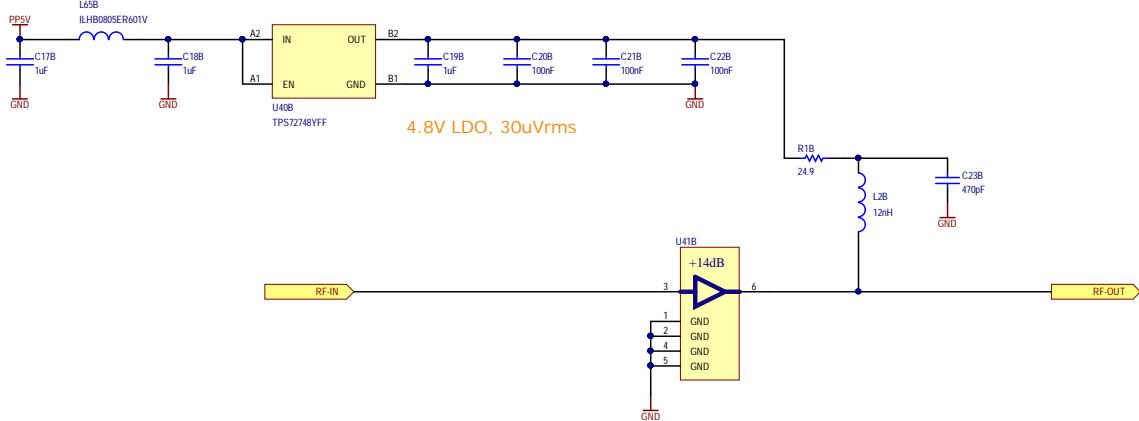


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Title
14dB LNA

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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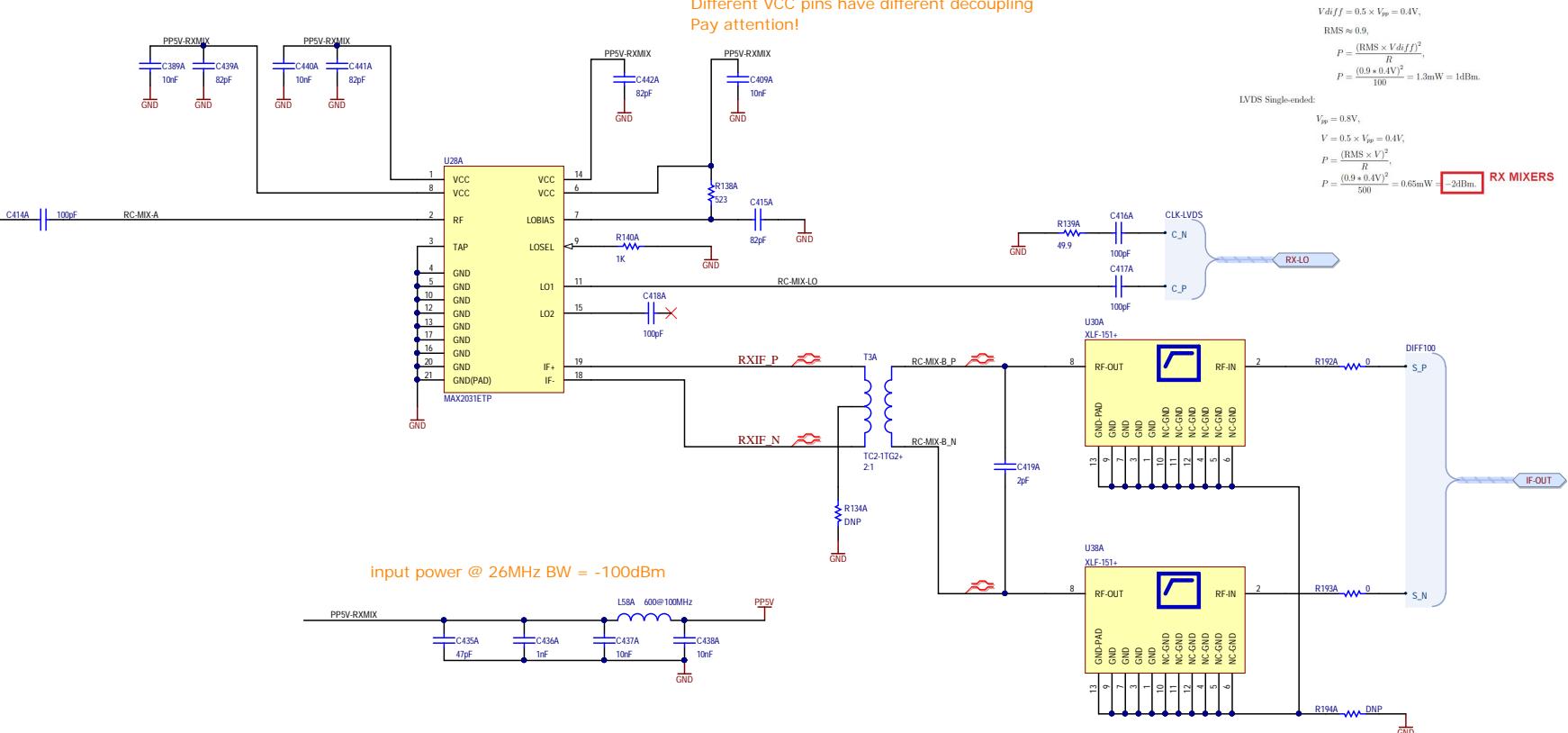
Cannot open file logo.bmp	Title 14dB LNA	
3479 Edison Way Menlo Park CA 94025 +1 (877) 778-8435 siglabs.com	Engineer E. Chiu	Drawn By E. Chiu
Revision 1	Sheet 53 of 57	Date 1/11/2017

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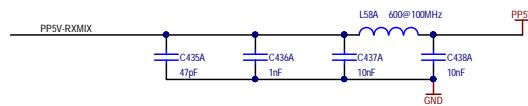
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input power @ 26MHz BW = -100dBm



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Receiver Mixer

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 54 of 57	Date 1/11/2017

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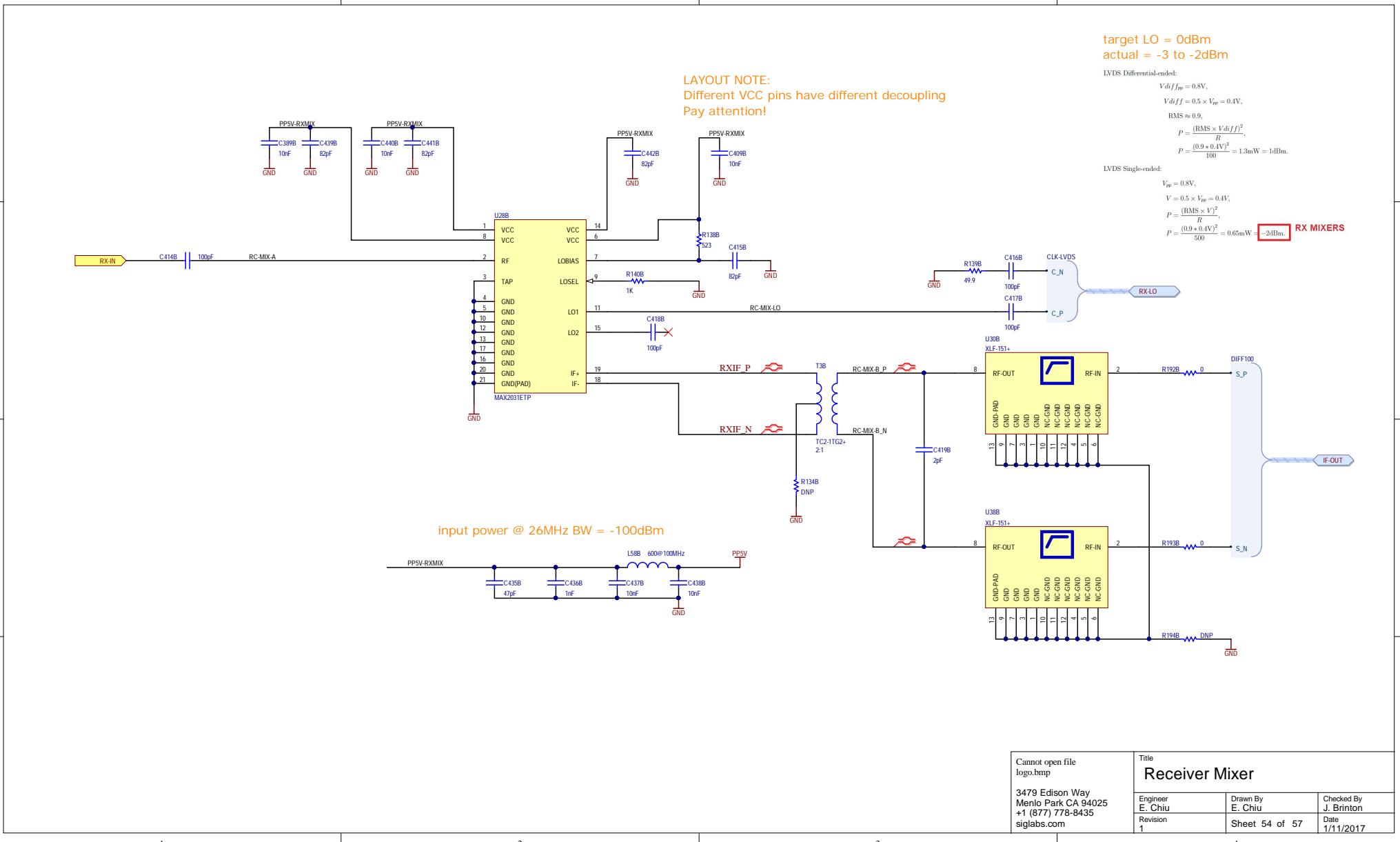
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Title
Receiver Mixer

Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
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target LO = 0dBm, actual = 3-4dBm

LVPECL33 Differential-ended:

$$V_{diff} = 1.6V,$$

$$V_{diff} = 0.5 \times V_{pp} = 0.8V,$$

RMS ≈ 0.9 ,

$$P = (\text{RMS} \times V_{diff})^2 / R,$$

$$P = \frac{(0.9 \times 0.8V)^2}{100} = 5mW = 7dBm.$$

LVPECL33 Single-ended:

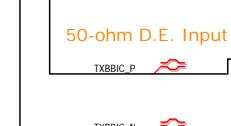
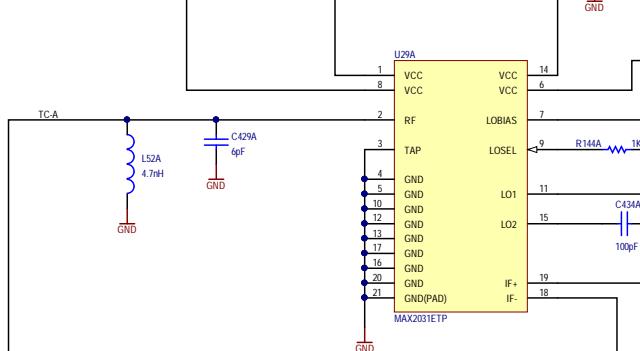
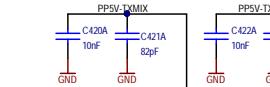
$$V_{pp} = 0.8V,$$

$$V = 0.5 \times V_{pp} = 0.4V,$$

$$P = \frac{(\text{RMS} \times V)^2}{R},$$

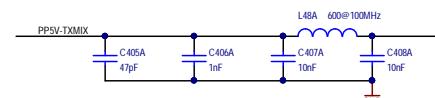
$$P = \frac{(0.9 \times 0.4V)^2}{500} = 2.5mW = 4dBm. \quad \text{TX MIXERS}$$

LAYOUT NOTE:
Different VCC pins have different decoupling
Pay attention!



Note in the PCB one of these
transformers is mirrored to optimize routing

60MHz Lowpass 5th Order Chebychev Filter, 100-ohm



input power @ 26MHz BW = -100dBm

1

2

3

4

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Title
Transmitter Mixer

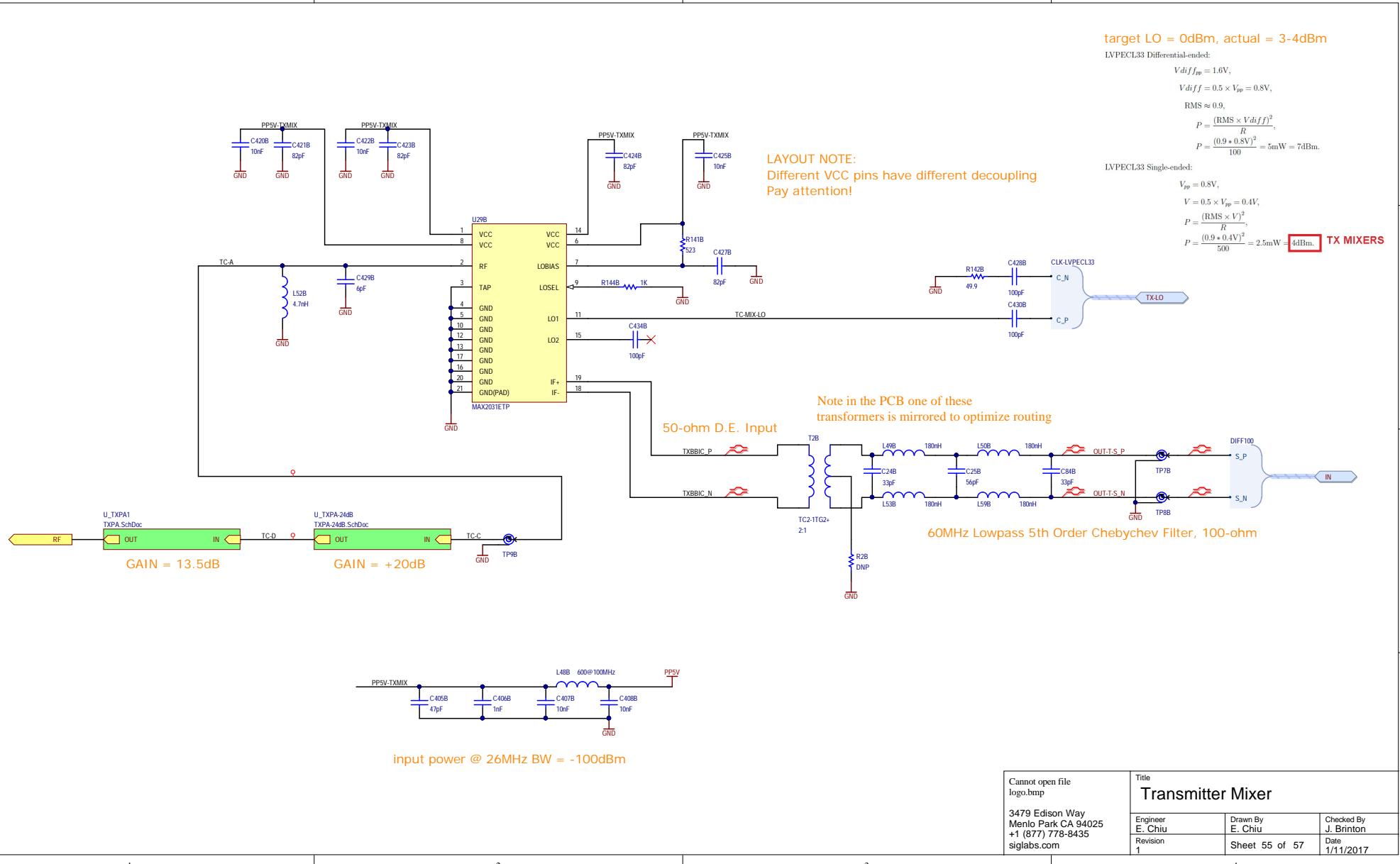
Engineer E. Chiu	Drawn By E. Chiu	Checked By J. Brinton
Revision 1	Sheet 55 of 57	
Date 1/11/2017		

1

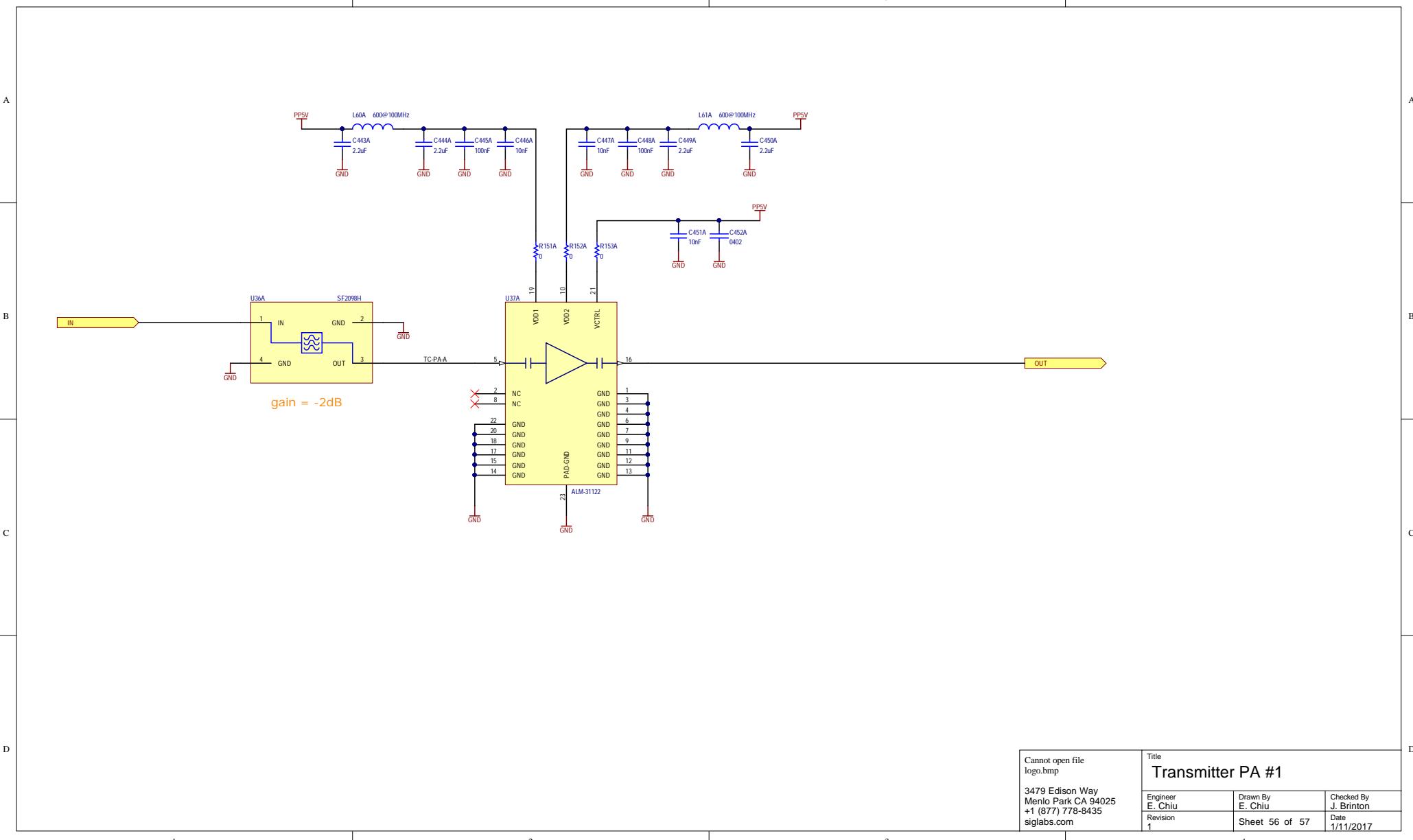
2

3

4



1 2 3 4



1 2 3 4

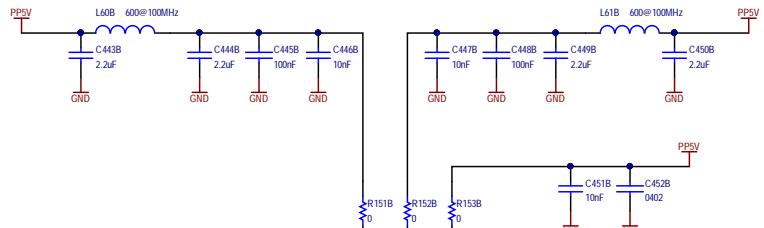
1

2

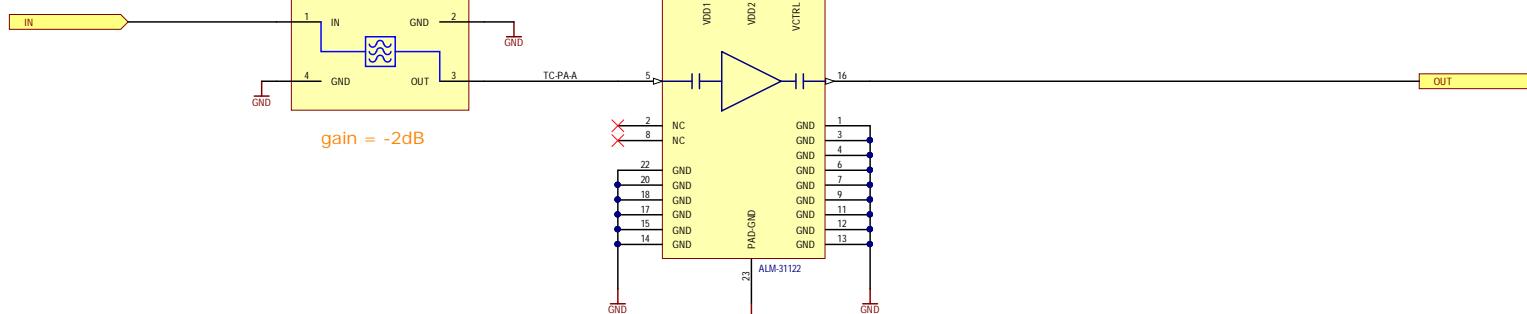
3

4

A



B



C

D

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Title
Transmitter PA #1

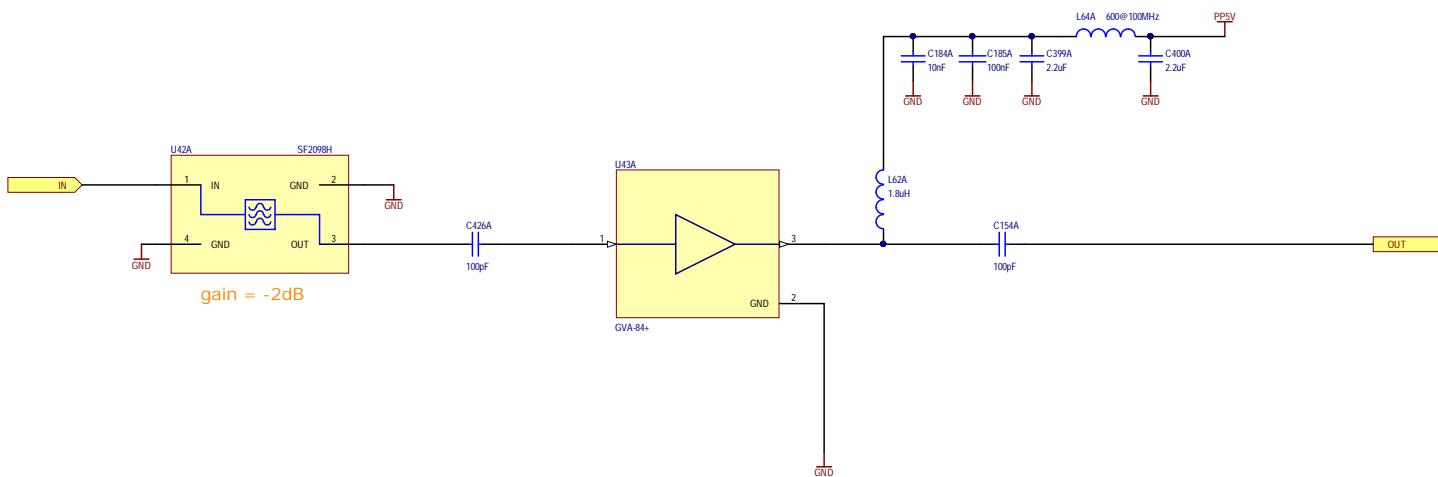
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1

2

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4



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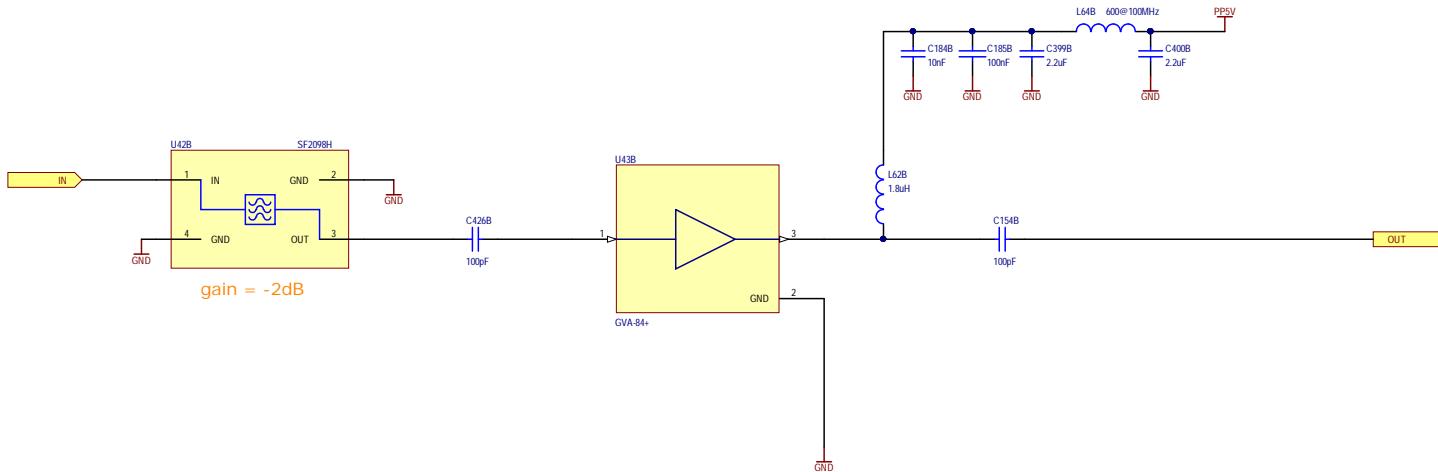
Title
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