

# Project report intergrated circuits

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Fall 2020

# 1 Introduction

“Hello Dirty talk”

## 2 Theory

### 2.1 Analog circuit

The analog circuit has input signals delivered by the digital circuitry, and delivers its output to two ADCs. The analog circuit diagram is given in figure XXX, where each of the four pixel block has the circuit given in figure XXX.

#### 2.1.1 Dimensions for switch transistors

The transistors M1, M2 and M4 all function as switches, where a digital gate input decides whether the transistor should act as a short-circuit between drain and source, or as an open circuit. Since these transistors simply should have two possible states, it is key that the leakage current is minimized. This is particularly important for M1 and M2 to ensure that the voltage over  $C_s$  is as constant as possible during readout. In *Analog Circuit Design* by Tony Chan Carusone, one can read in section 1.4.1 that the subthreshold leakage current is given by

$$I_{off} = (n - 1)\mu_n C_{ox} \left(\frac{W}{L}\right) \left(\frac{kT}{q}\right)^2 \exp(-qV_t/nkT). \quad (1)$$

In order to minimize this leakage current,  $\frac{W}{L}$  need to be as small as possible, meaning the smallest possible width  $W$  and the largest possible length  $L$  is desired. The technology used will limit the possible width and length of the transistor, thus  $W$  and  $L$  can be chosen accordingly.

#### 2.1.2 Value for $C_s$

To choose a suitable value for  $C_s$  spice simulations can be used. To know what values are the best we will look at the four corner cases for exposure time and light conditions. The corner cases will be denoted exposure-light, so for example max-min is maximum exposure time and minimum light. For each corner we will do a transient simulation. That means we will look at a graph that shows the voltage over  $C_s$  as a function of time. In each light-exposure corner, the resulting voltage value that we are interested in is the voltage over  $C_s$  at the end of the exposure. There are many possible approaches to how these corners should be tuned, and the one that we will apply is the following:

- Max-max corner should make  $C_s$  fully charged.
- Min-min corner should leave  $C_s$  uncharged.
- Min-max and max-min corners should make  $C_s$  half full charged.

The reason for why we want the corners to be like that is beyond the scope of this report.

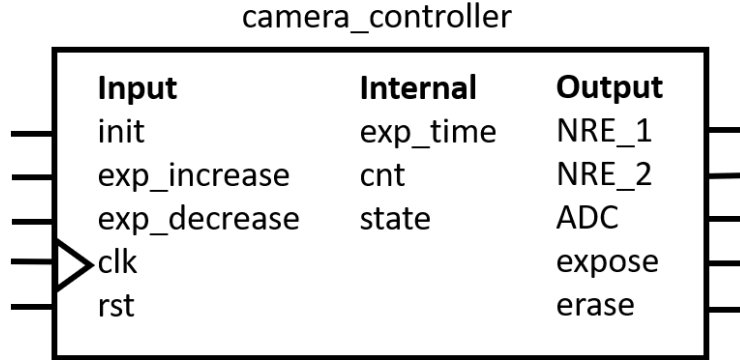


Figure 1: Overview of input and output pins as well as internal registers for the camera controller module.

### 2.1.3 Values for M3 and active load

## 2.2 Digital camera controller

### 2.2.1 Finite state machine

### 2.2.2 Output timing

### 2.2.3 Module interface

## 3 Results

### 3.1 Analog pixel circuit

#### 3.1.1 Values and dimensions

The transistor technology used limits the width to

$$1.08\mu\text{m} \leq W \leq 5.04\mu\text{m} \quad (2)$$

and limits the length to

$$0.36\mu\text{m} \leq L \leq 1.08\mu\text{m}. \quad (3)$$

As explained in section 2.1.1, the length and width of the switch transistors will be  $1.08\mu\text{m}$ .

CS was tuned to 2pF. The output from simulations with this value for the exposure-light corners min-min, min-max, max-min and max-max are shown respectively in figures 2, 3, 4 and 5.

#### 3.1.2 Analog circuit simulation

### 3.2 Digital circuit

#### 3.2.1 Digital circuit simulation

Explaining the big waveform.

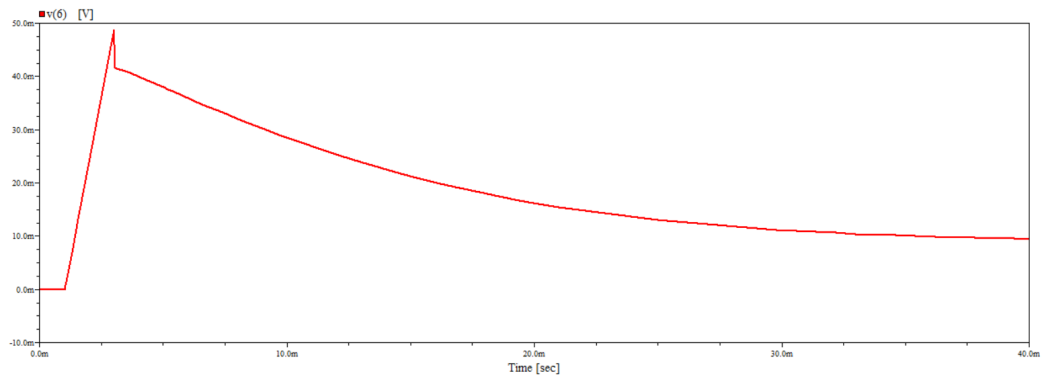


Figure 2: Minimum exposure time - minimum light

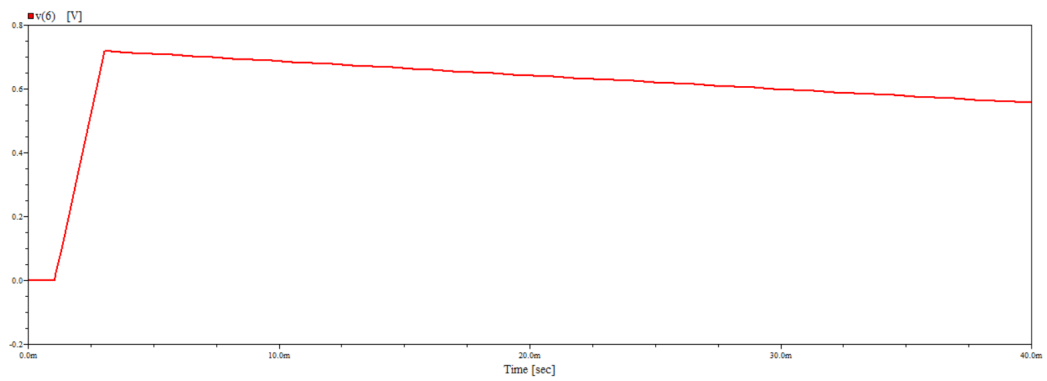


Figure 3: Minimum exposure time - maximum light

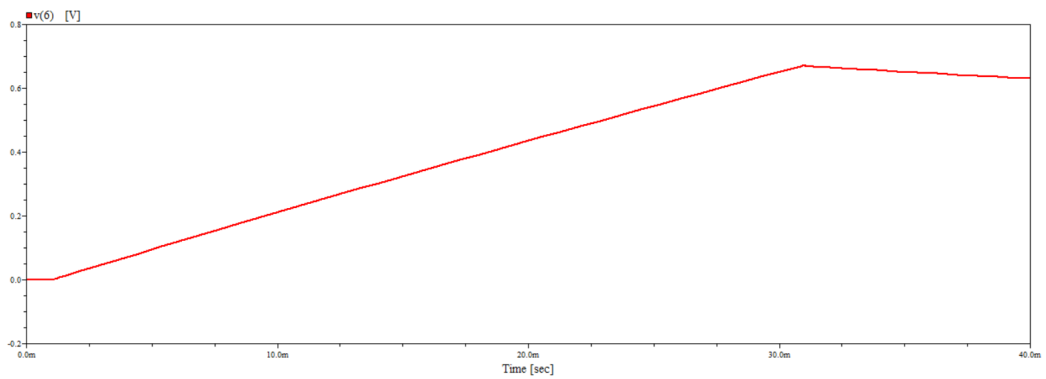


Figure 4: Maximum exposure time - minimum light

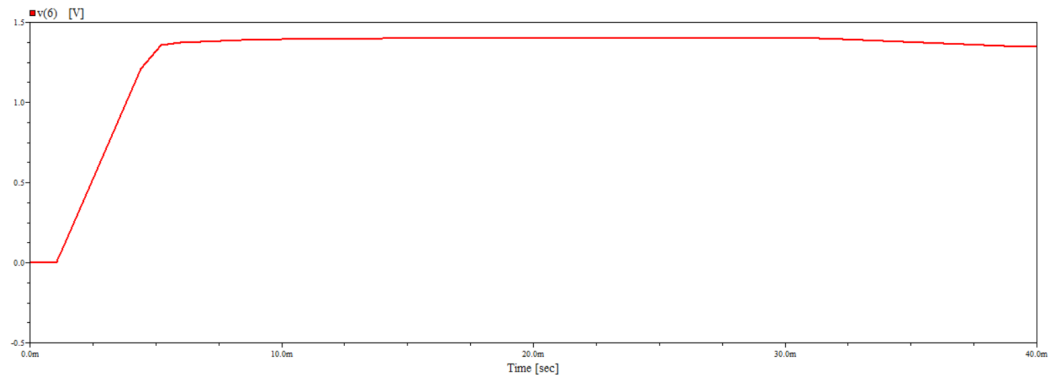
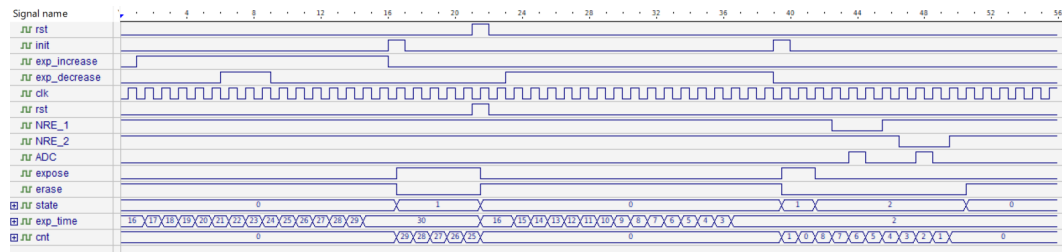


Figure 5: Maximum exposure time - maximum light



## 4 Discussion