



Sihang Wu

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Gender: Male **Date of birth**: 14/03/2001 **Nationality**: Chinese

ABOUT ME

I am currently pursuing a Master's degree in Communication and Electronics Engineering. During my undergraduate studies, I developed a solid foundation in Electrical and Electronic Engineering. I am deeply interested in analog circuit design and aspire to further enhance my expertise in this field through advanced learning and practical experience.

EDUCATION AND TRAINING

[01/09/2018 – 01/09/2020]

Bachelor of Science

Zhengzhou University <http://english.zzu.edu.cn/>

Address: 100 Kexue Blvd, Zhongyuan District, Zhengzhou, Henan, China, Zhengzhou, China | **Field(s) of study**: Electronic Information Engineering |

Final grade: 3.3(4.0 full) | **Number of credits**: 116.5

[01/10/2020 – 01/10/2023]

Bachelor of Science

Duisburg-Essen Uni <https://www.uni-due.de/de/index.php>

Address: Forsthausweg 2, 47057, Duisburg, Germany | **Field(s) of study**: Electrical and Electronic Engineering | **Final grade**: 2.0(1.0 full) | **Number of credits**: 180

[01/10/2023 – Current]

Master of Science

Technical University of Munich <https://www.tum.de/en/>

Address: Arcisstraße 21, 80333, München, Germany | **Field(s) of study**: Communication and Electronics Engineering | **Final grade**: 1,8(1.0 full) | **Number of credits**: 48

- **VHDL lab**
- **HW/SW design**
- **Seminar: An ultra-low power 32kHz oscillator** mainly about Pulsed driver techniques
- **System-on-Chip Technologies**

WORK EXPERIENCE

[08/03/2022 – 09/06/2022]

Test engineer

Dejet <http://www.dejet.de/>

Address: Harffstraße 47, 40591, Düsseldorf, Germany | **Name of unit or department**: Technical Testing Department

- **Testing communication functions and power consumption** for smartphones (OPPO, XIAOMI, VIVO, Realme), ensuring device performance and energy efficiency.
- **Developed and executed test plans** independently for both workplace and laboratory environments, adhering to quality and performance standards.
- **Collaborated with local customers** to conduct joint testing, providing valuable insights and ensuring client satisfaction.

[15/02/2023 – 31/07/2023]

Software developer

Address: Am Seestern 5, 40547, Düsseldorf, Germany | **Name of unit or department:** Consumer Cloud Service European Operations Center

- **Developed website using HTML, JavaScript, Bootstrap, and WordPress**, helping colleague to download the apk file massly and automatically.
- **Proficient in Python and SQLite**, with the ability to skip the website's robot recognition, process the website's button and text interactions, record all download results and store apk files.
- **Collaborated on projects using Git/Github and Docker**, working closely with colleagues to develop, manage, and track project versions efficiently.

PROJECTS

[15/05/2023 – 01/09/2023]

Condition Monitoring of Vehicles

The project is my bachelor thesis in Duisburg-Essen Uni, I connected the STMicroelectronics ASM330LHHX microcontroller(integrated with Gyroscope and accelerometer) and the Texas Instruments INA228 current/voltage sensor via the I²C interface. And the sensor data is used by the Machine Learning core(the decision tree) integrated in the ASM330LHHX to obtain the state of motion of the vehicle and the whole device's power consumption.

[01/04/2024 – 01/08/2024]

The implementation of IDEA on an FPGA board

In this project/lab, I used VHDL to implement and simulate the International Data Encryption Algorithm (IDEA) by software Xilinx ISE independently. Then I tested the implementations on an FPGA board(Spartan-3E Starter Kit). In the end, I did two optimizations, the first optimization uses the control unit and registers to achieve only one round for eight times encryption process. For the second time, The use of FPGA resources is further reduced by slitting a round into 4 partial steps and reusing Adder, multipliers and XOR Modules.

LANGUAGE SKILLS

Mother tongue(s): Chinese

Other language(s): English-IELTS 6.5 , German-Telc B2