

8.6 Register Maps

Table 15. Register Map

Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	DATA[27:0]																												ADDRESS[3:0]			
R15	0	0	0	0	0	0	1	0	0	0	0	1	1	1	1	1	1	1	1	VCO CAP MAN	VCO_CAPCODE[7:0]								1	1	1	1
R13	DLD_ERR_CNT[3:0]				DLD_PASS_CNT[9:0]										DLD_TOL [2:0]		1	0	0	0	0	0	1	0	0	0	0	0	1	1	0	1
R10	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	1	1	0	0	1	0	1	0
R9	0	0	0	0	0	0	1	1	1	1	0	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	1	1	1	0	0	1
R8	0	0	1	0	0	0	0	0	0	1	1	1	1	1	0	1	1	1	0	1	1	0	1	1	1	1	1	1	1	0	0	0
R7	0	FL_SELECT [4:0]					FL_PINMODE [2:0]			FL_INV	MUXOUT_SELECT [4:0]				MUX INV	MUXOUT_PINMODE [2:0]		LD_SELECT [4:0]				LD_INV	LD_PINMODE[2:0]			0	1	1	1			
R6	0	RD_DIAGNOSTICS[19:0]																				1	0	RDADDR[3:0]			uWI RE_ LOC K	0	1	1	0	
R5	0	0	0	0	0	0	0	OUT_LD EN	OSC_FREQ[2:0]		BUF EN DIS	0	0	0	VCO_SEL_ MODE [1:0]	OUTB_ MUX [1:0]		OUTA_ MUX [1:0]		0_DLY	MODE [1:0]		PWDN_MODE [2:0]		RES ET	0	1	0	1			
R4	PFD_DLY [2:0]			FL_FRC E	FL_TOC[11:0]											FL_CPG[4:0]			0	CPG_BLEED[5:0]					0	1	0	0				
R3	0	0	1	0	0	0	0	0	0	VCO_DIV[4:0]				OUTB_PWR[5:0]				OUTA_PWR[5:0]					OUT_B_ _PD	OUT_A_ _PD	0	0	1	1				
R2	0	0	OSC_2X	0	CPP	1	PLL_DEN[21:0]																				0	0	1	0		
R1	CPG[4:0]					VCO_SEL [1:0]		PLL_NUM[21:12]								FRAC_ORDER [2:0]			PLL_R[7:0]							0	0	0	1			
R0	ID	FRAC_DITHER [1:0]		NO_FCA L	PLL_N[11:0]											PLL_NUM[11:0]													0	0	0	0