Inquire

Voltage current status inquire

Inqiure frame:

Initial	Address	Function	Initial	Address	Check	Ending
code	code	code	address	length	code	code
D0	D1	D2	D3	D4	D5D6	D7
F7	01	03	04	03	Check	FD
					code	

eg: 0xF7, 0x02, 0x03, 0x04, 0x09, 0xE2, 0xAB, 0xFD

Inqiure reply frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5-D22	D11D12	D13
F7	01	03	04	03	data	Check	FD
						code	

 $\begin{array}{l} {\rm eg:} \ \ 0XF7, 0X02, 0X03, 0X04 \ , 0X09 \ , 0X41, 0X41 \ , 0X00, 0X00, 0X00, 0X00 \ , 0X00, 0X00 \ , 0X00, 0X00, 0X00 \ , 0X00, 0X00 \ , 0X00, 0X01 \ , 0X2C \ , 0X9D \ , 0X87, 0XFD \end{array}$

D5: CH1 Status: BIT0 CV 、BIT1 CC 、BIT2 SER、BIT3 PAR 、BIT5 OUTPUT、

D6: CH2 Status: BIT0 CV 、BIT1 CC 、BIT2 SER、BIT3 PAR 、BIT5 OUTPUT、

D7 D8 CH1 Voltage Data D9 D10 CH1 Current data D11 D12 CH2 Voltage Data D13 D14 CH2 Current data D15 D16 CH1 Voltage setting Data D17 D18 CH1 Current setting Data D19 D20 CH2 Voltage setting Data D21 D22 CH2 Current setting Data

Setting

Setting frame as below, Set reply frame as setting frame

CH1Voltage setting frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	02	0A	09	01	data	Check	FD
						code	

CH1 Current setting frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	01	0A	09	01	data	Check	FD
						code	

eg: 0xF7, 0x02, 0x0A, 0x0A, 0x01, 0x03, 0XE8, 0x8A, 0x56, 0Xfd

CH2 Voltage setting Frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	02	0A	0B	01	data	Check	FD
						code	

eg: 0xF7, 0x02, 0x0A, 0x0B, 0x01, 0x05, 0xDC, 0x01, 0x55, 0xFD

CH2Current Setting Frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	01	0A	0C	01	data	Check	FD
						code	

eg: 0xF7, 0x02, 0x0A, 0x0C, 0x01, 0x04, 0XB0, 0XC8, 0x55, 0XFD

output switch Setting frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	01	0A	1E	01	data	Check	FD
						code	

Ouput open Setting eg: 0xF7, 0x02, 0x0A, 0x1E, 0x01, 0x00, 0x01, 0X04, 0x92, 0XFD

Output turn off setting eg: 0xF7, 0x02, 0x0A, 0x1E, 0x01, 0x00, 0x00, 0XC4, 0x53, 0XFD

series-parallel setting frame

Initial	Address	Function	Initial	Address	data	Check	Ending
code	code	code	address	length		code	code
D0	D1	D2	D3	D4	D5D6	D7D8	D9
F7	01	0A	1F	01	data	Check code	FD

Series set eg: F7, 02, 0A, 1F, 01, 00, 01, F8,93, FD

Parallel connection Settings eg: F7, 02, 0A, 1F, 01, 00, 02, F9,D3, FD series-parallel cancel setting eg: F7, 02, 0A, 1F, 01, 00, 00, 38,52, FD