

# Sijie Lan

W341 Westgate Building  
University Park, PA 16802, USA  
(+1) 646-691-0456

sijielan.github.io  
sijielan@gmail.com

## RESEARCH AREAS

My research focuses on I/O stack optimization, including file systems (such as F2FS and Btrfs) and storage devices (ZNS and FDP). I also study operating systems and computer architecture to improve system efficiency and reliability. My goal is to develop reliable and efficient kernel-level storage systems that fully utilize underlying device architectures.

## EDUCATION

The Pennsylvania State University	Aug, 2021 – May, 2026
<i>Ph.D. student in Computer Science and Engineering (advisor: Prof. Mahmut Kandemir)</i>	<i>University Park, USA</i>
Xiamen University	Sept, 2018 – June, 2021
<i>M.Eng. in Computer Technology (advisor: Prof. Suzhen Wu)</i>	<i>Xiamen, China</i>
Zhejiang Sci-Tech University	Sept, 2013 – June, 2017
<i>B.Eng. in Computer Science and Technology</i>	<i>Hangzhou, China</i>

## RESEARCH EXPERIENCE

Optimization on Zoned Namespace SSDs. <i>Research Assistant</i>	Sept, 2023 – Present <i>Penn State, USA</i>
<ul style="list-style-type: none"><li>• <b>New I/O Interface Beyond Block Abstraction:</b> Improve I/O stack efficiency and CPU utilization by redesigning the storage interface beyond traditional block-based abstractions. [Under Review]</li><li>• <b>Mapping Strategies for Emerging ZNS Devices:</b> Design efficient logical-to-physical mapping mechanisms to improve space utilization and performance on zoned storage devices. [Under Review]</li><li>• <b>Garbage Collection Optimization:</b> Reduce request latency and minimize GC overhead through improved reclamation strategies.</li></ul>	
Flash Memory Reliability <i>Research Assistant</i>	Sept, 2019 – May, 2021 <i>Xiamen University, China</i>
<ul style="list-style-type: none"><li>• <b>BitFlip Scheme for NAND Flash:</b> Improve reliability and reduce read latency by mitigating bit-error probabilities. [MSST 2020]</li></ul>	

## PUBLICATIONS

[C1] Suzhen Wu, Sijie Lan, Jindong Zhou, Hong Jiang, Zhirong Shen. *BitFlip: A Bit-Flipping Scheme for Reducing Read Latency and Improving Reliability of Flash Memory*. MSST 2020.

[C2] Yingtian Zhang, Yan Kang, Ziyu Ying, Wanhang Lu, Sijie Lan, Huijuan Xu, Kiwan Maeng, Anand Sivasubramaniam, Mahmut T. Kandemir, and Chita R. Das. *Pirate: No Compromise Low-Bandwidth VR Streaming for Edge Devices*. ASPLOS 2025.

## *WORK EXPERIENCE*

**Software Engineer Intern** May, 2025 – Aug, 2025  
*Meta* Bellevue, WA

- Developed SSD-based cached system for objective store.
- Implemented data validation and consistency mechanisms.
- Analyzed system-level storage efficiency and data organization.

## *SPECIALIZED SKILLS*

**Research Areas:** File Systems, Storage Systems, Operating Systems, Flash Memory.

## Programming Languages: C++, C, Shell, Python

## Operating Systems: Linux, Windows

## *TEACHING EXPERIENCE*

<b>Teaching Assistant</b> <i>Penn State</i>	CMPSC 132 (Python and Data Structures) <i>Spring 2022</i>
<b>Teaching Assistant</b> <i>Penn State</i>	CMPSC 473 (Operating Systems) <i>Fall 2021</i>
<b>Teaching Assistant</b> <i>Xiamen University</i>	C Programming Language <i>Fall 2019</i>