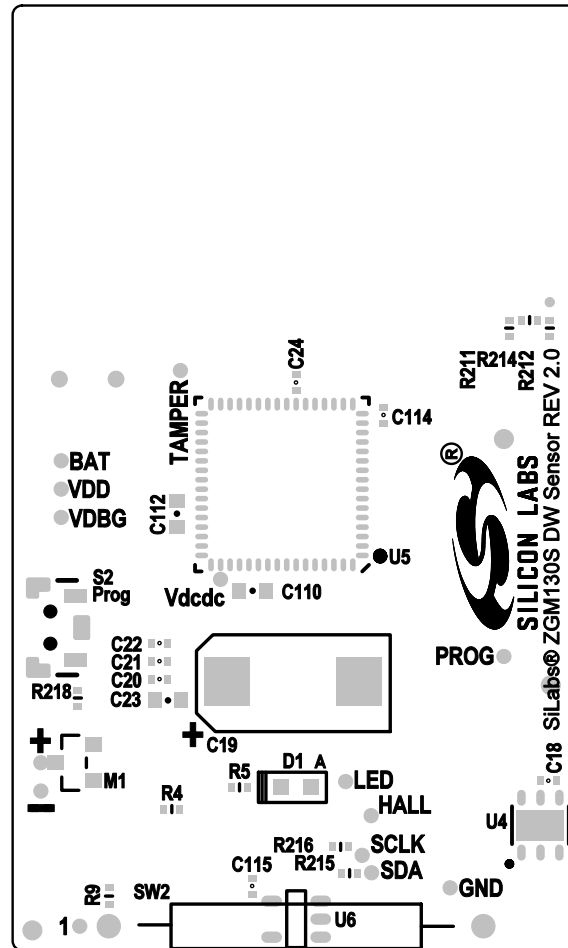
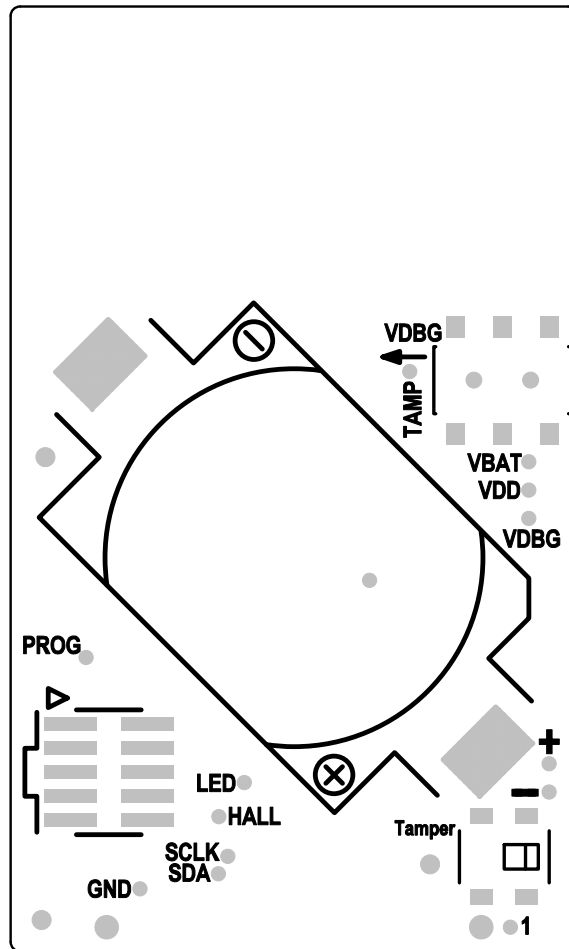
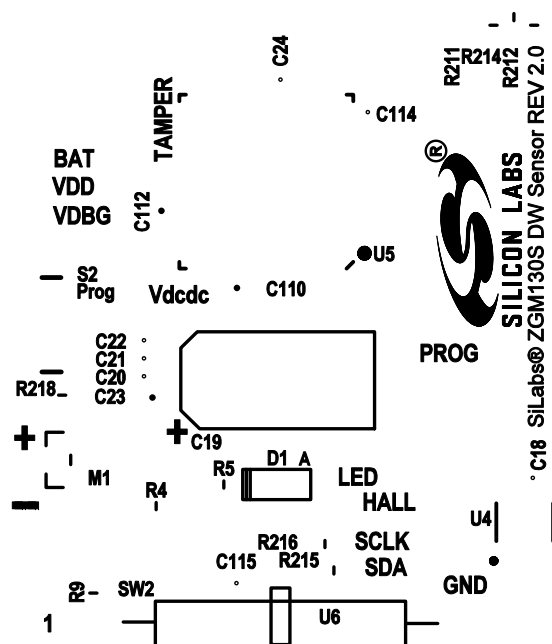


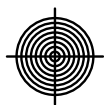
PRIMARY SILKSCREEN



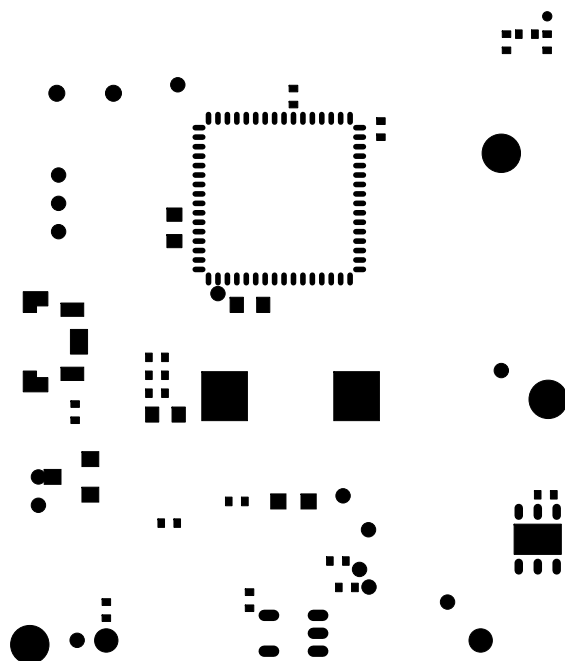
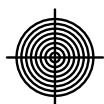
SECONDARY SILKSCREEN

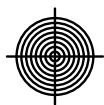




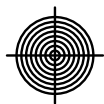
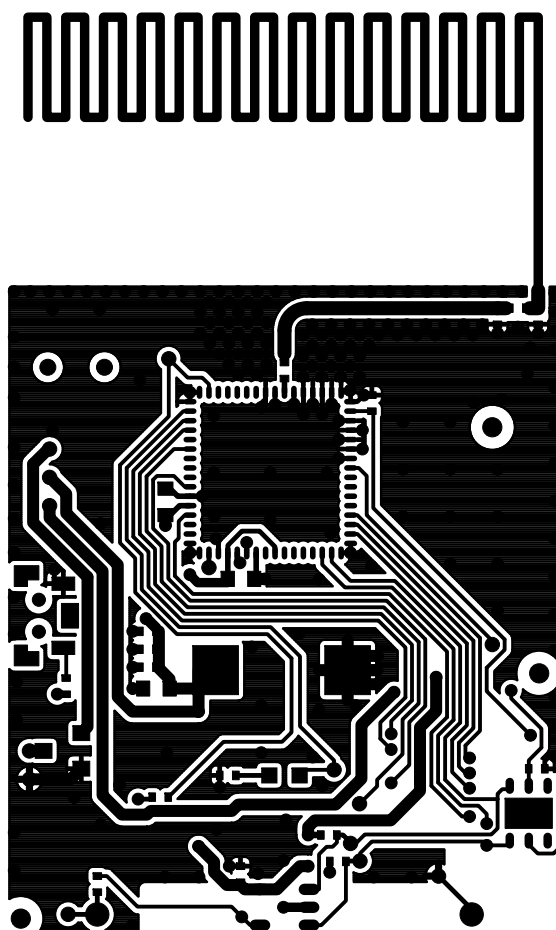


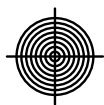
PRIMARY SOLDER MASK



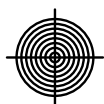
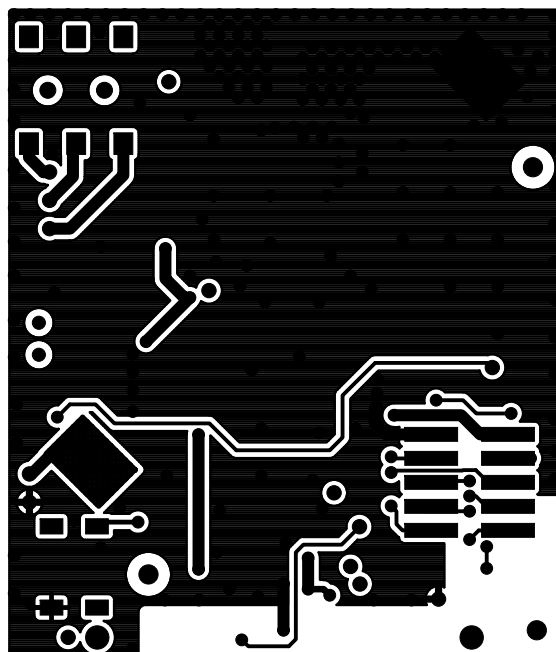


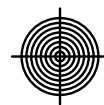
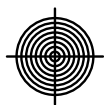
PRIMARY SIDE



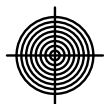
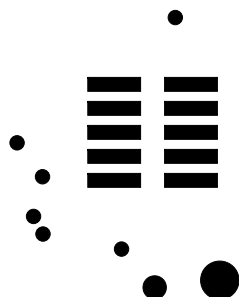
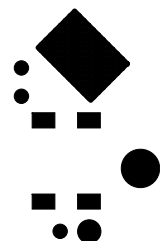
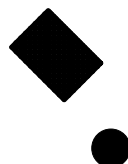
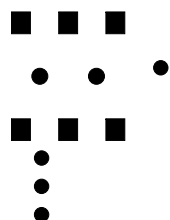


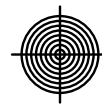
SECONDARY SIDE



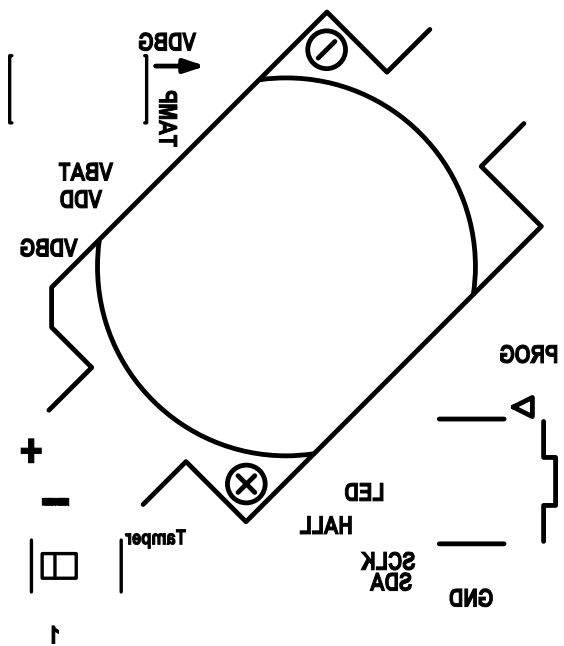


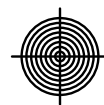
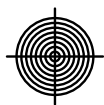
SECONDARY SOLDER MASK



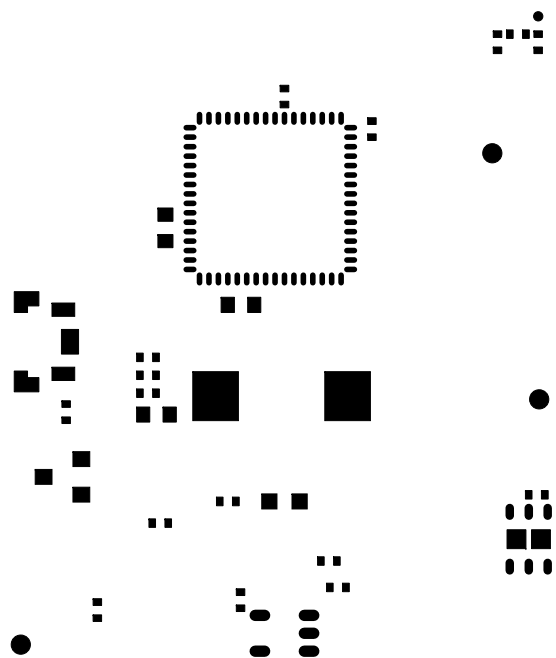
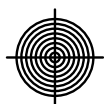


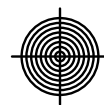
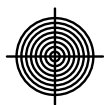
SECONDARY SILKSCREEN



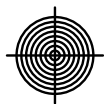
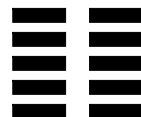
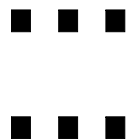


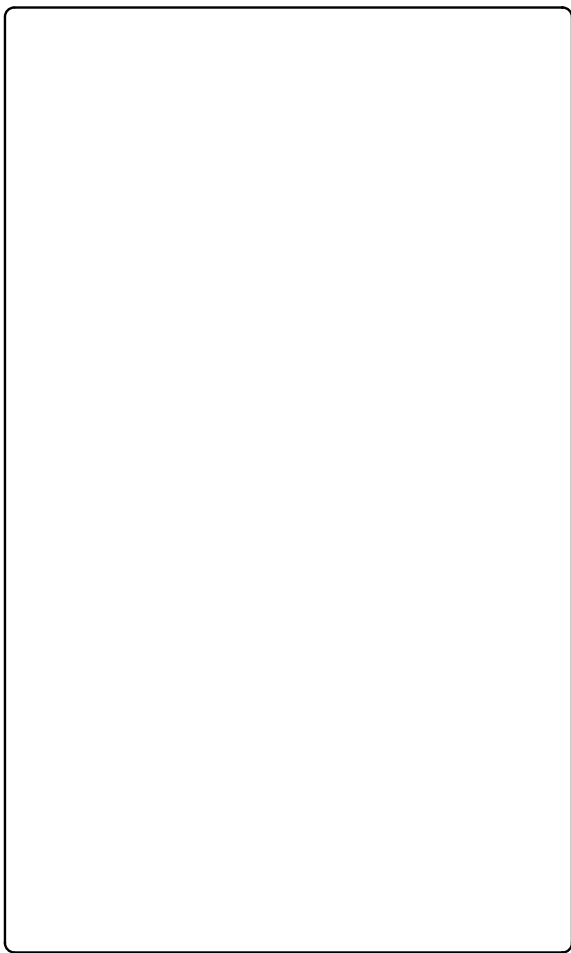
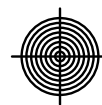
PRIMARY SOLDER PASTE

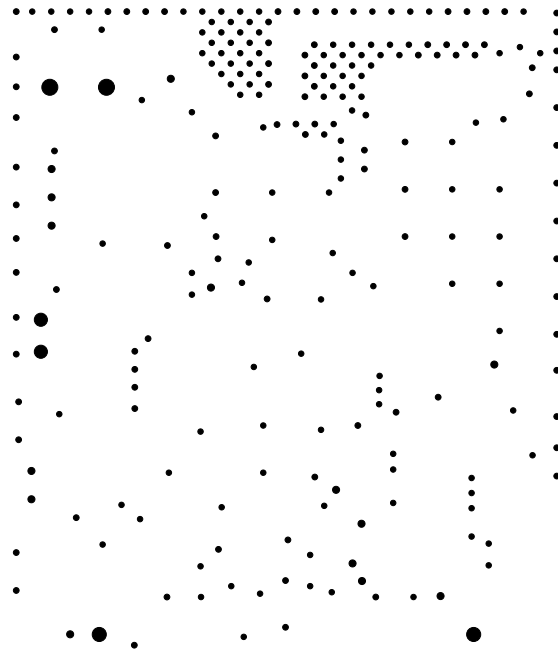


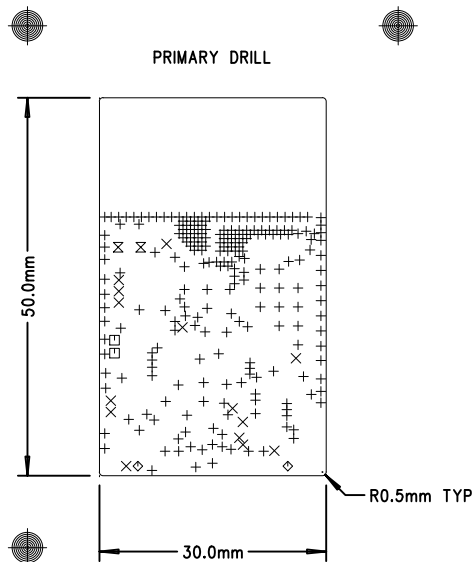


SECONDARY SOLDER PASTE









NOTES : UNLESS OTHERWISE SPECIFIED

1. MANUFACTURE IN ACCORDANCE WITH IPC-6012, TYPE 3, CLASS 2.
2. END PRODUCT FEATURES SHALL NOT VARY MORE THAN 20% FROM ARTWORK ORIGINALS.
3. MATERIAL SHALL BE COPPER CLAD FR-4, NEMA GRADE PER IPC-4101/26, COLOR NATURAL.
4. COPPER WEIGHT SHALL BE 1.0 OZ./SQ. FT. BEFORE PLATING.
5. ALL PLATED THROUGH HOLES SHALL HAVE A MINIMUM OF 0.001" COPPER.
6. DRILL HOLE TOLERANCE AFTER PLATING SHALL BE ± 0.003 ".
7. MINIMUM ANNULAR RING SHALL BE 0.001".
8. MINIMUM ANNULAR RING AT EMERGENT CONDUCTORS SHALL BE 0.003".
9. FINAL PCB THICKNESS SHALL BE 0.062" $\pm 10\%$.
10. WARP/TWIST SHALL NOT EXCEED 0.75%.
11. FINISH SHALL BE LPI, GREEN SMOBC, ENIG BOTH SIDES.
12. SILKSCREEN WITH NONCONDUCTIVE WHITE EPOXY INK.
13. REFERENCE ADDITIONAL FAB NOTES IN FILE README.TXT


LAYER STACKUP

FILE NAMES

PRIMARY SILKSCREEN	ZGM130S-PCB_PSS.PHO
PRIMARY SOLDERMASK	ZGM130S-PCB_PSM.PHO
PRIMARY SIDE	ZGM130S-PCB_PRI.PHO
SECONDARY SIDE	ZGM130S-PCB_SEC.PHO
SECONDARY SOLDERMASK	ZGM130S-PCB_SSM.PHO
SECONDARY SILKSCREEN	ZGM130S-PCB_SSS.PHO

SCALE: NONE

SIZE	QTY	SYM	PLT	TOOL	TOL
0.300	229	+	P	1	+0/-0.300
0.381	14	X	P	2	+/-0.075
0.700	2	□	N	3	+/-0.075
0.750	2	◇	P	4	+/-0.075
0.850	2	⊗	N	5	+/-0.075

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DIMENSIONS ARE IN INCHES AND APPLY AFTER FINISH						NAME:												
INTERPRET DRAWING PER MIL-D-1000						BRD4001A +ZGM130S												
TOLERANCES						Window Sensor Reference Design (PCB)												
HOLE TOLERANCES PER 78027			DESIGN			ZV			05APR2019			SIZE		PART NUMBER:		REV :		
DECIMALS		ANGLES		SURFACES		LAYOUT			CT			09APR2019			A		2.0	
.XX +/-		+/-		MICROINCHES		DO NOT SCALE DRAWING			SCALE			1:1			FABRICATION DRAWING			
.XXX +/-																		
PART TO BE FREE OF BURRS																		
BREAK EDGES		BEND RADIUS		BEND RELIEF														
MAX		MAX		MAX														