

#### VIA AND TRACE REQUIREMENTS:


- MIN VIA PAD SIZE 20mils
- MIN spacing between trace and pad is 5mils
- MIN spacing between VIA and pad is 5mils
- MIN width of trace is 4mils

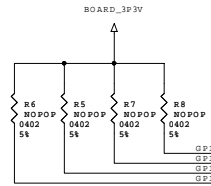
#### IMPEDANCE REQUIREMENTS:

- USB\_DP/M must be 90-ohm differential (+/-15%)
- USB\_SSTXP/N must be 90-ohms differential (+/-15%)
- USB\_SSRXP/N must be 90-ohms differential (+/-15%)
- PCIE\_TXP/N must be 100-ohms differential (+/-10%)
- PCIE\_RXP/N must be 100-ohms differential (+/-10%)
- PCIE\_REFCLKP/N must be 100-ohms differential (+/-10%)

#### LENGTH MATCHING REQUIREMENTS:

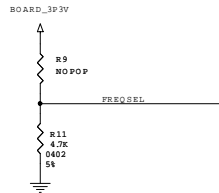
- USB\_DP/M within 25mils.
- USB\_SSTXP/N within 5mils
- USB\_SSRXP/N within 5mils
- PCIE\_TXP/N within 5mils
- PCIE\_RXP/N within 5mils
- PCIE\_REFCLKP/N within 25mils.

 <b>TEXAS INSTRUMENTS</b>	
TUSB7340_DEMO_REVB	
SIZE B	DWG NO:
SCALE: NONE	Tuesday, March 08, 2011 Sheet 1 of 4

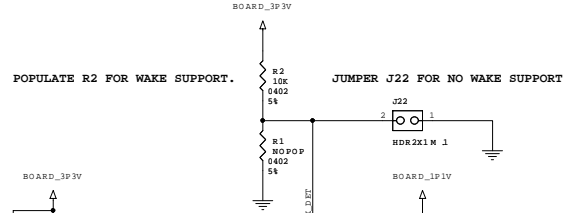
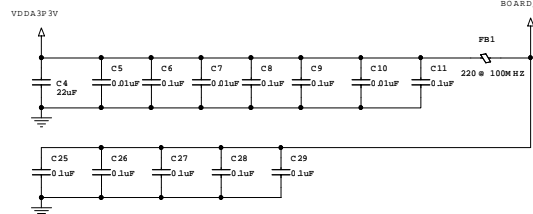
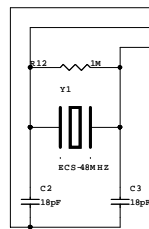


POPULATE PULLDOWN IF I2C EEPROM  
NOT USED AND DO NOT POPULATE  
PULLUP.

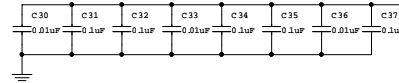
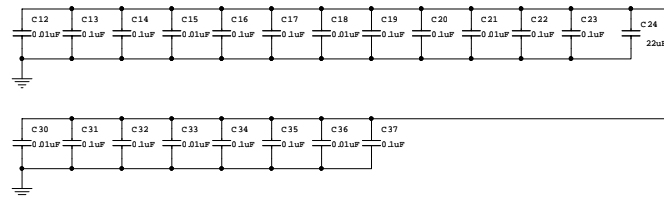
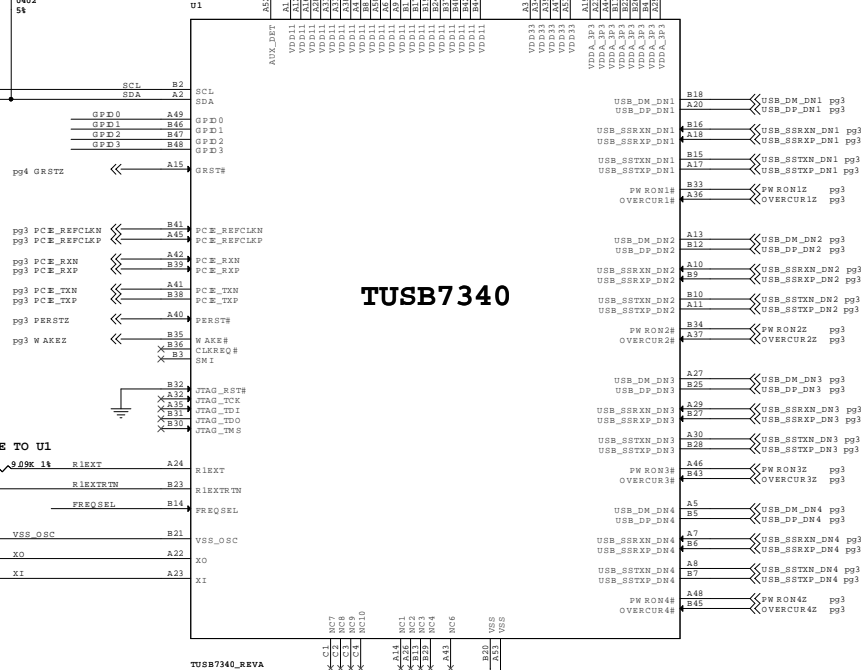
FREQSEL      CLOCK  
0              48MHz  
1              PLL\_FREQ\_SEL field in the  
                USB Control Register

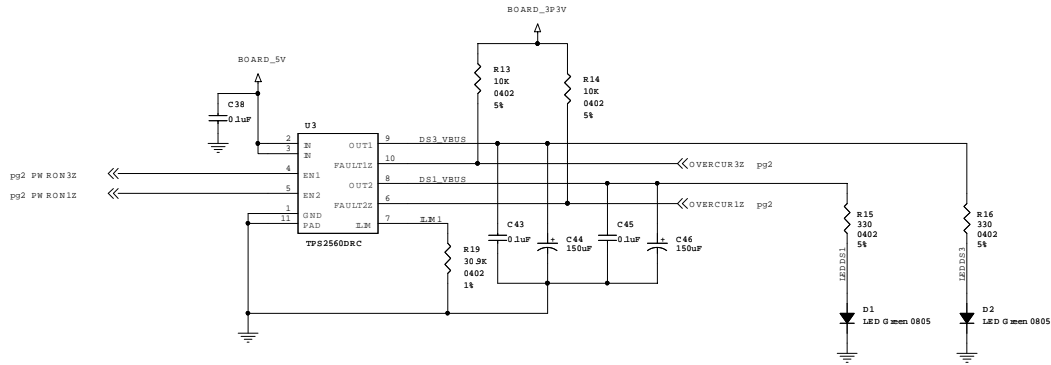


PLACE CLOSE TO U1  
R10 5.09k 1% R1EXT  
R1EXTTN  
FREQSEL

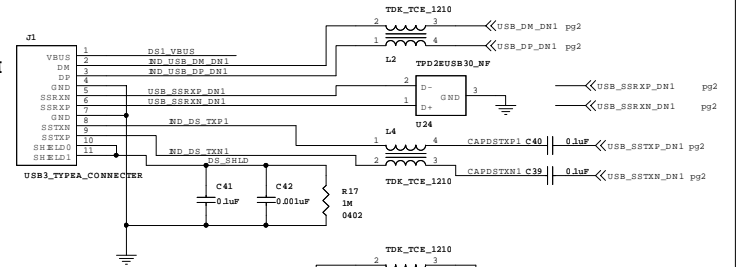


TUSB7340

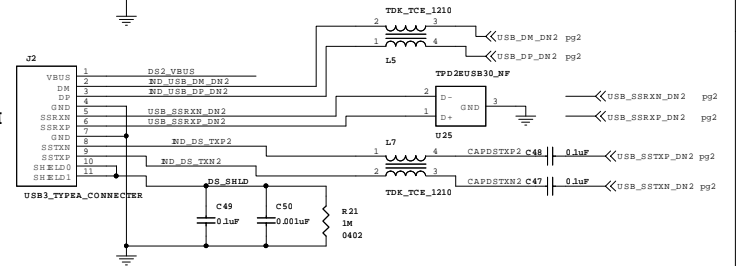




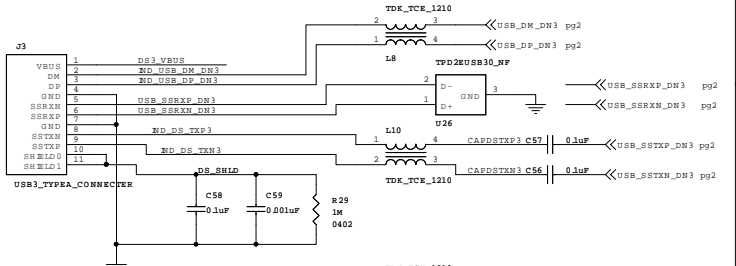
DOWNSTREAM  
PORT1



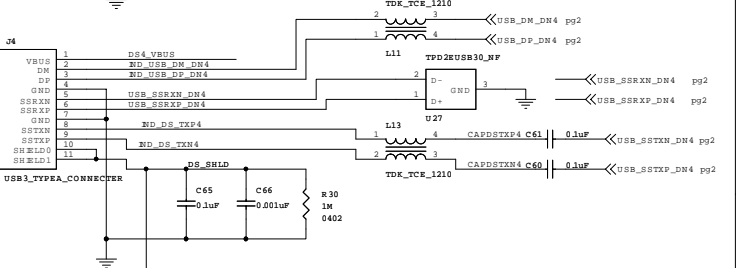
DOWNSTREAM  
PORT2



DOWNSTREAM  
PORT3



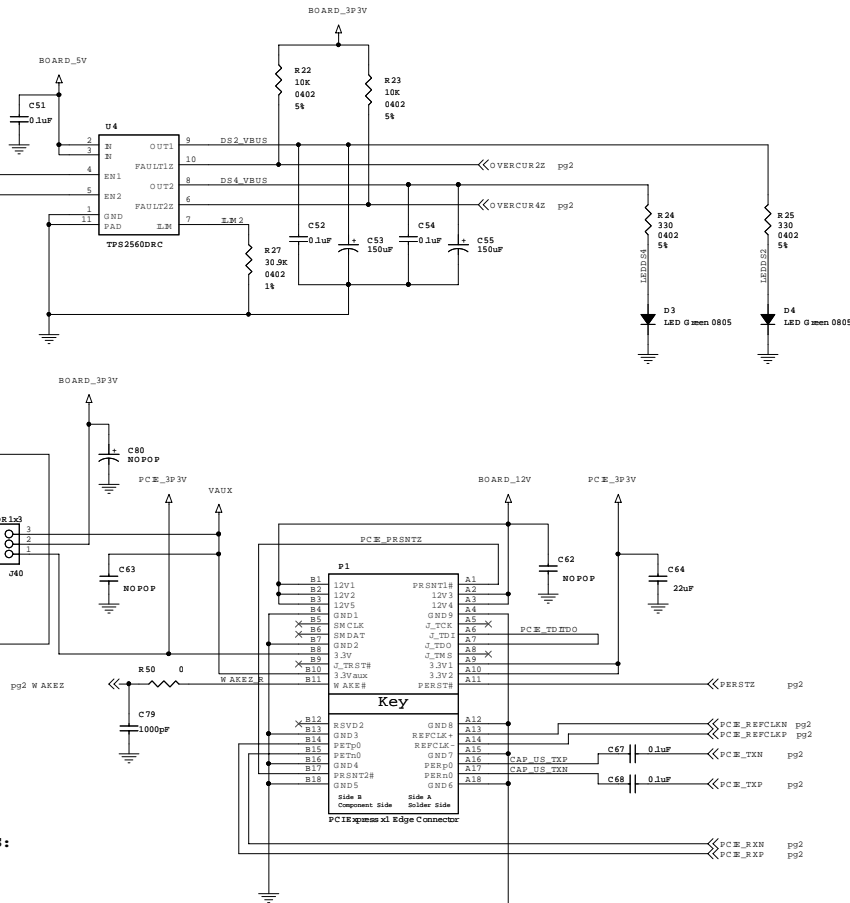
DOWNSTREAM  
PORT4



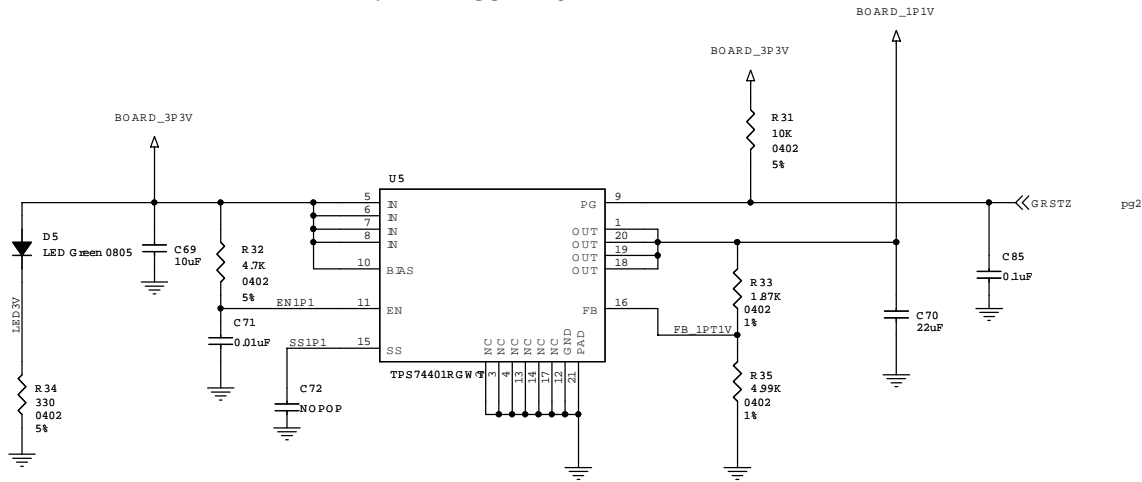
JUMPER BETWEEN 2 AND  
3 FOR WAKE TESTING.

JUMPER BETWEEN 1 AND  
2 FOR NORMAL  
OPERATION.

PCIE CEM SPEC MAX VALUES:  
PCIE\_3P3V: 3 AMPS.  
VAUX: 375mA



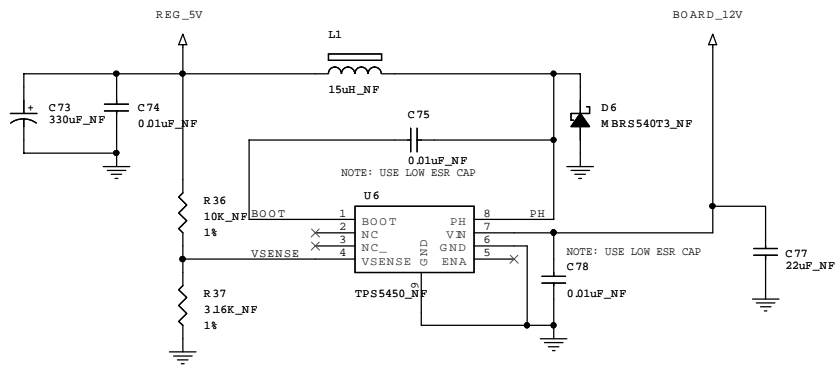
## 1.1V REGULATOR



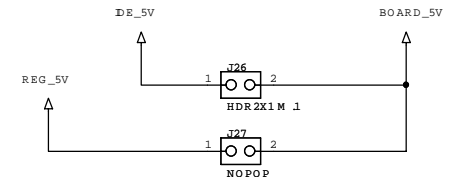
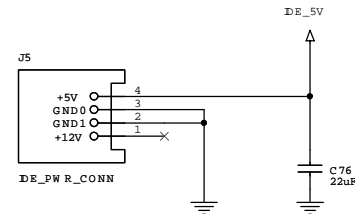
R33	R35	OUTPUT
1.13K	4.53K	1.0V
1.37K	4.42K	1.05V
1.87K	4.99K	1.1V (DEFAULT)
2.49K	4.99K	1.2V

## 5V VBUS OPTIONS

### OPTION 1: 5V REGULATOR



### OPTION 2: 5V FROM IDE CONNECTOR



NOTE: ONLY POPULATE ONE OPTION

POWER	
SIZE B	DWG NO: SCALE: NONE
Pdsay, December 01, 2010	
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