CS 4098 Project Report

An Experimental Compiler Design Platform

Interim project report

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Winter Semester 2014

Abstract

This project proposes a new educational platform for compiler design as an improvisation of the currently existing framework being used at the compiler construction laboratory in our institution. This involves building a compiler for a simple but a non-trivial programming language. We have designed the platform in such a manner that the students would build the compiler in a simple yet systematic method. The process has been broken down into smaller components and implemented stage by stage. Even though this has already been done in theory as well as practice, in our opinion, the correlation between them often goes unnoticed. The different stages have been integrated in such a way that their interlinking is given a lot of importance. This project is aimed at developing a self-sufficient experimental compiler design platform where students can build their own compiler with minimal expert supervision.

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Problem Definition

Compiler design involves a series of stages of development. Currently, an incremental approach is being used in building a compiler in the compiler construction laboratory in our institution. In this methodology, students are trained to develop a compiler for the Simple Integer Language (SIL). Students are expected to develop a compiler to translate the source code of SIL into executable code which is executed using the Simple Integer Machine (SIM). SIL is an experimental programming language which was designed for educational purposes [3] and SIM is a virtual machine with an elementary instruction set[4]. The language chosen for implementing the compiler is the C programming language. Compiler generation tools LEX and YACC have been used to aid the students to build the compiler.

In this currently used approach, we found that many students who enrolled for the compiler laboratory course (including ourselves) faced problems with correlating the compiler design theory with the implementation process. In our opinion, the students would be able to comprehend the process better with more educational resources and guidance.

Our project attempts to improvise the currently existing system by designing a new instructional framework consisting of documentation for the compiler construction process and an easy to follow roadmap to guide a student who is in the learning phase.

Introduction

This project aims to develop an online self-sufficient educational platform which can be used to tutor students in writing a compiler. The project will provide the students with a roadmap for the development process and guide them along the roadmap with supporting documentation. Using the step-by-step guidance offered by the roadmap, the students will be able to build the compiler under minimal expert supervision.

We have designed the platform assuming that a student before starting to follow the roadmap has a basic knowledge of Data structures, Computer organization and a working proficiency of the C programming language. Being instructional in nature, this project gives the learner an insight into the working of LEX, YACC and the usage of these tools to develop a compiler for SIL (Simple Integer language). The theory involved would be learned on a needto-do basis. This, in our opinion would further enhance the understanding of the theory behind the implementation process.

An *in-depth* explanation approach of the compiler design concepts and how these are interlinked with the back-end working of the compiler-generation tools has been used throughout the documentation. We believe that this would enable the students to have a better comprehension of the construction process and the convoluted nature of the different stages of construction.

The primary focus of the project is to make the compiler construction process an easier task than it currently is now at the laboratory and to provide a strong foundation of basic knowledge in compiler development.

Literature review

3.1 Compilation process review

A compiler translates a given source program in a specific programming language into executable code[2]. The executable code generated is dependent on the machine architecture on which the compiler is being built upon. A simple compiler (without any code optimizer) consist of five phases: Lexical Analysis, Syntax Analysis, Semantic Analysis, Intermediate code generation and Code generation (machine architecture specific)[1]. The first two phases are the initial focus of the project and are discussed below.

3.2 Lexical analysis using LEX

Lexical analysis is the process of breaking up a source program into tokens. A lexical token is a sequence of characters that can be treated as a unit in the grammar of a programming language [1]. A lexical analyzer scans a given input and produces an output of tokens.

LEX is a tool that translates a set of regular expression specifications into a C implementation of a corresponding finite state machine[1]. This C program when compiled, yields an executable lexical analyzer. Conceptually, LEX constructs a finite state machine to recognize all the regular expression patterns specified in the LEX program file. The lex.yy.c program stores information about the finite state machine in the form of a decision table (transition table). LEX makes it's decision table visible if we compile the LEX program with the -T flag. The finite state machine used by LEX is a deterministic finite state automaton (DFA). The lex.yy.c file simulates the DFA.

Also, LEX offers features to execute a single or compound C statement

when a pattern match is found in the input stream. Given its ability to scan and identify a given pattern, and the ability to execute a corresponding action, LEX can be used to generate a lexical analyzer.

3.3 Syntax analysis using YACC

Syntax analysis follows lexical analysis in the compilation process. The syntax of a programming language can be expressed using Context Free Grammars (CFG). Any sentential form of the programming language's grammar is considered a syntactically correct program. The process of checking whether a program can be derived from the programming language's grammar is referred to as parsing. YACC (Yet Another Compiler Compiler) was developed in 1970 by Stephen C. Johnson at AT& T Corporation. YACC is a tool that translates the given CFG specification in a YACC program to a corresponding Push Down Automaton (PDA) implementation in C language. The generated C program when compiled, yields an executable parser. The source program is fed to the parser to check if it is syntactically correct.

3.4 Semantic analysis using YACC

In addition to syntax analysis, YACC also provides features to support semantic analysis of the source program. Semantic analysis is achieved using C code. C code can be extensively embedded into a YACC program. YACC provides support for add an action to be executed with every grammar rule. These actions are written in C. To support the C code in the actions section, YACC provides an auxiliary functions section (also written in C).

Design

This chapter contains the proposed design which has been followed up till the current state of the project and will be followed (or improvised upon) till the completion of the project.

4.1 Documentation

This project will consist of vast amount of documentation on the usage of LEX and YACC to generate a compiler. Initially, the documentation phase will concentrate more on the mastery of the tools and gradually introduce compiler design concepts and how these can be implemented using these tools.

The documentation will follow a very simple explanation approach using plenty of examples and input/output samples. The idea is to reduce the amount of time a learner spends in the beginning stages and instead invest this time in later on stages of compiler construction which are comparatively more complex. This way, the learner will be able to spend more time in implementing challenging compiler patches instead of spending a lot of time in the learning phase.

4.2 Testing

The document is to be embedded with plenty of code in examples and exercises. Before being used in the documentation, each and every code snippet is to be implemented and tested in the laboratory.

4.3 Roadmap

The Road map is the key to the achieving the project's objective. Along with the documentation and given code snippets, a learner would be asked to follow the roadmap which is yet to be designed.

4.4 Version Control

Since this project contains various components and evolves through many stages, it needs to be maintained using a version control system. The advantage of using one would be the ease of being able to roll back to any version at any point of time during the development phase of the project. We have chosen Git for this purpose.

4.5 Online platform

To enhance the availability of the project to students, this project will be hosted online at the public domain **silcnitc.github.io**. The website is being developed with HTML5, CSS3 and JavaScript. Github is a remote server for Git. Under licensed conditions, the project will be released on an open source basis on Github.

4.6 Assembling the framework

Students will use the roadmap to build a compiler for SIL. Towards the code generation phase, they would be instructed to generate code for the SIM architecture [4]. Once all the individual components have been completely developed, they will be tested and/or proof read several times before they will be integrated with each other appropriately on the website.

Current status

With the current work progress, the platform can be potentially deployed once the proposed stages of the project in this section have been completed. The exact details of the stages are as follows:

5.1 LEX Documentation

A LEX document for the lexical analysis phase has been designed, compiled and reviewed.

5.2 YACC Documentation

A YACC document for the parsing phase has been designed, compiled and reviewed.

5.3 LEX-YACC Documentation

A LEX-YACC document for using YACC effectively with LEX has been designed, compiled and reviewed.

5.4 Attributes Documentation

A document on using the attribute stack of YACC is being designed currently.

5.5 Extensions to SIL

Language extensions to SIL are currently being designed and are yet to be tested. These include user-defined types, dynamic memory allocation and pointers.

5.6 Online Platform

The development of the mainframe of the website has been completed. All the completed documents are available online. Other components of the website are currently under construction.

References

- [1] Modern Compiler Implementation in C, by Andrew W. Appel
- [2] Compilers: Principles, Techniques and Tools, by Alfred V. Aho, Monica S. Lam, Ravi Sethi, Jeffrey D. Ullman
- [3] Simple Integer Language description, Dr. Murali Krishnan K. http://athena.nitc.ac.in/~kmurali/Compiler/sil.html
- [4] Simple Integer Machine architecture, Dr. Murali Krishnan K. http://athena.nitc.ac.in/~kmurali/Compiler/sim.html