



bakhshipoor / STM32H7-W25Q128-External-Loader

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bakhshipoor Update README.md

d2833ad · 2 years ago

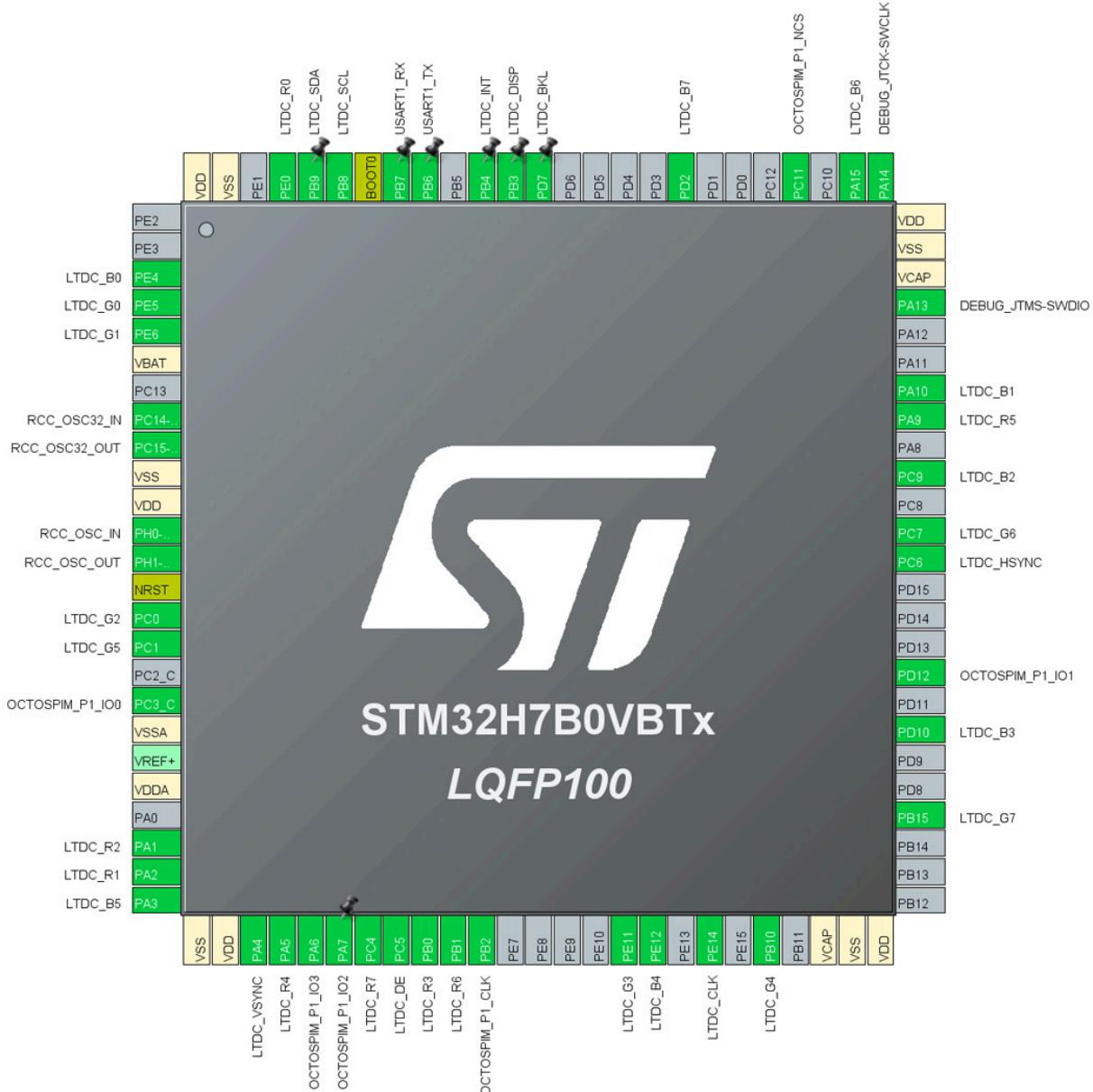


31 lines (25 loc) · 1.13 KB

STM32H7-W25Q128-External-Loader

W25Q128 External Loader for STM32H7b0VBT6

Pin Out:



OCTOSPI Configuration:

OCTOSPI1 Mode and Configuration

Mode

Mode	Quad SPI
Clock	Port1 CLK
HyperBus(TM) 1.8V Inverted Clock	Disable
<input checked="" type="checkbox"/> Chip Select Port1 NCS	
Data Strobe	Disable
Data [3:0]	Port1 IO[3:0]
Data [7:4]	Disable

Configuration

NVIC Settings GPIO Settings MDMA Settings
 Parameter Settings User Constants

Configure the below parameters :

<input type="text" value="Search (Ctrl+F)"/>	<input type="button" value=""/>	<input type="button" value=""/>	<input type="button" value="i"/>																														
<p>✓ Generic</p> <table border="0" style="width: 100%; border-collapse: collapse;"><tr><td style="width: 30%;">Fifo Threshold</td><td style="width: 70%;">1</td></tr><tr><td>Dual Quad mode</td><td>Disable</td></tr><tr><td>Memory Type</td><td>Micron</td></tr><tr><td>Device Size</td><td>24</td></tr><tr><td>Chip Select High Time</td><td>8</td></tr><tr><td>Free Running Clock</td><td>Disable</td></tr><tr><td>Clock Mode</td><td>Low</td></tr><tr><td>Wrap Size</td><td>Not Supported</td></tr><tr><td>Clock Prescaler</td><td>2</td></tr><tr><td>Sample Shifting</td><td>Half Cycle</td></tr><tr><td>Delay Hold Quarter Cycle</td><td>Disable</td></tr><tr><td>Chip Select Boundary</td><td>0</td></tr><tr><td>Delay Block</td><td>Disable</td></tr><tr><td>Maximum Transfer</td><td>0</td></tr><tr><td>Refresh Rate</td><td>0</td></tr></table>				Fifo Threshold	1	Dual Quad mode	Disable	Memory Type	Micron	Device Size	24	Chip Select High Time	8	Free Running Clock	Disable	Clock Mode	Low	Wrap Size	Not Supported	Clock Prescaler	2	Sample Shifting	Half Cycle	Delay Hold Quarter Cycle	Disable	Chip Select Boundary	0	Delay Block	Disable	Maximum Transfer	0	Refresh Rate	0
Fifo Threshold	1																																
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Device Size	24																																
Chip Select High Time	8																																
Free Running Clock	Disable																																
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Chip Select Boundary	0																																
Delay Block	Disable																																
Maximum Transfer	0																																
Refresh Rate	0																																

OCTOSPI1 Mode and Configuration

Mode

Mode Quad SPI
Clock Port1 CLK
HyperBus(TM) 1.8V Inverted Clock Disable
 Chip Select Port1 NCS
Data Strobe Disable
Data [3:0] Port1 IO[3:0]
Data [7:4] Disable

Configuration

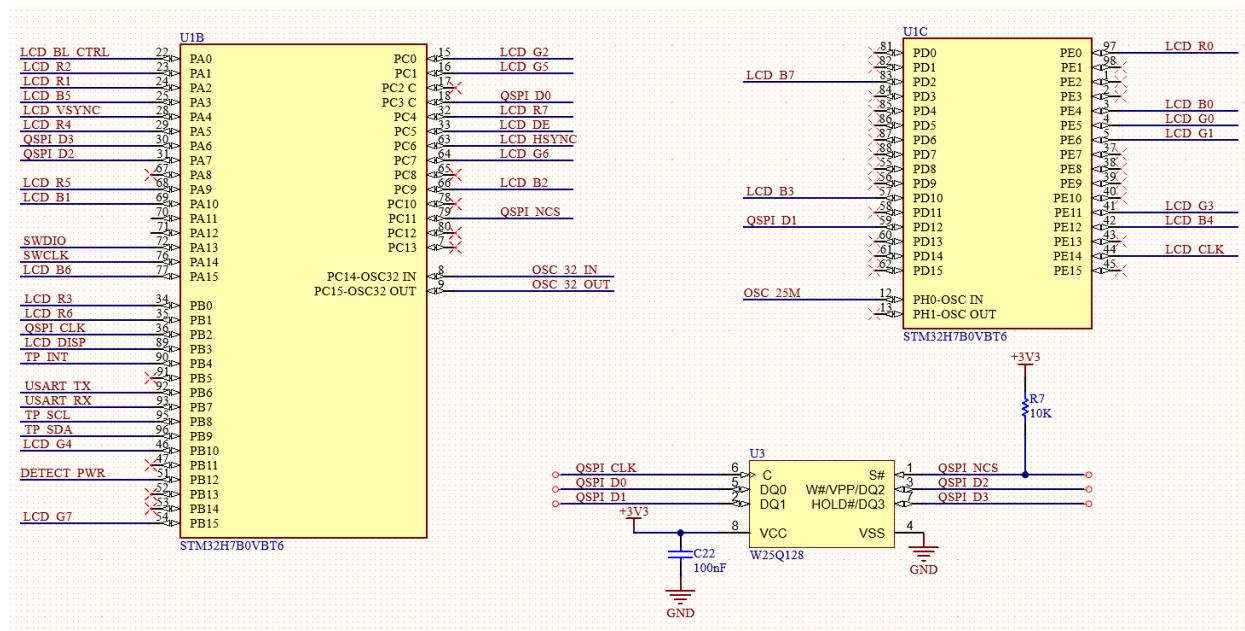
Reset Configuration

NVIC Settings GPIO Settings MDMA Settings
 Parameter Settings User Constants

Search Signals
Search (Ctrl+F)

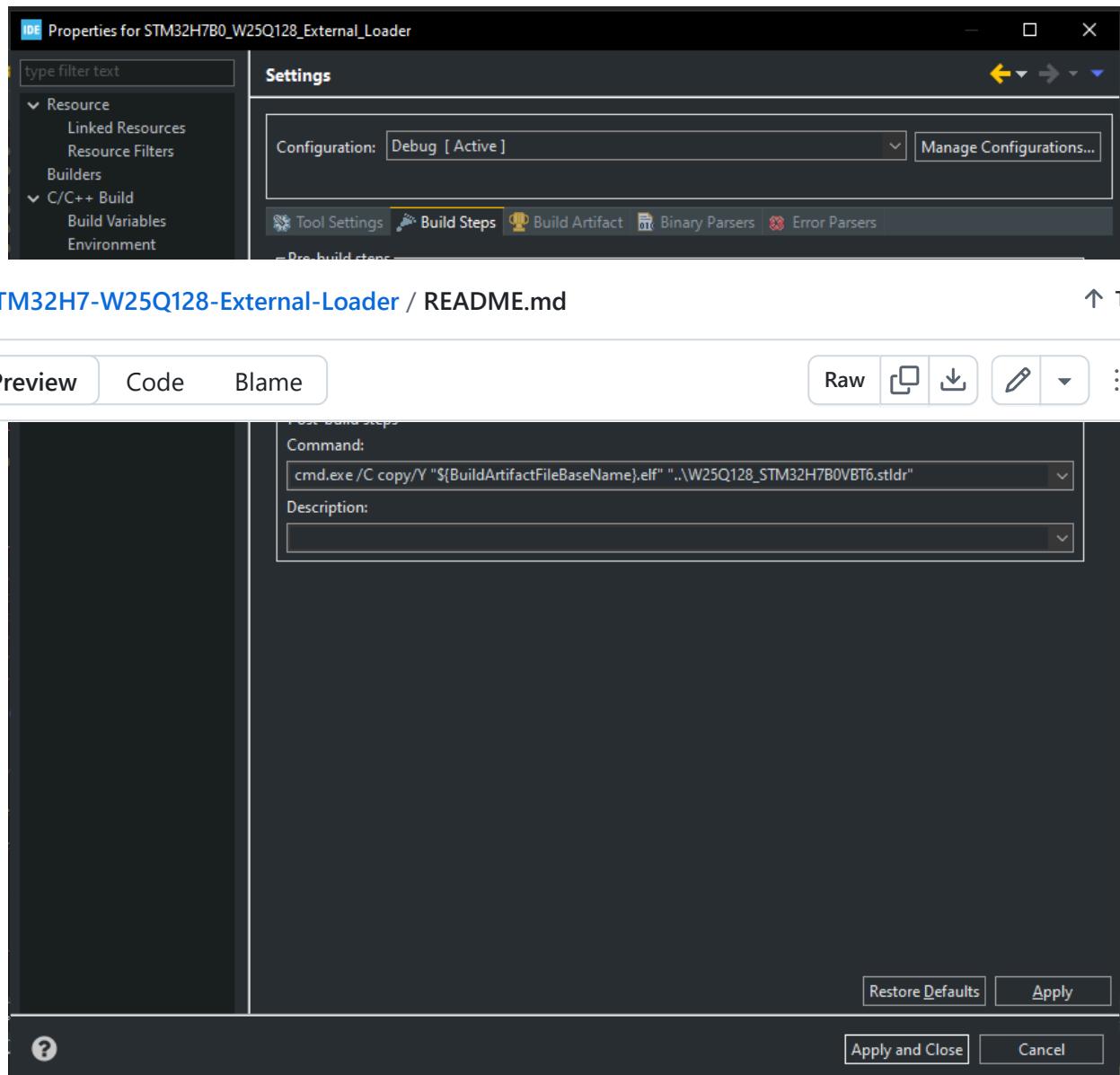
Pin Name	Signal on Pin	GPIO output	GPIO mode	GPIO Pull-up	Maximum
PA6	OCTOSPIM_P1_IO3	n/a	Alternate ...	No pull-up ...	Very High
PA7	OCTOSPIM_P1_IO2	n/a	Alternate ...	No pull-up ...	Very High
PB2	OCTOSPIM_P1_CLK	n/a	Alternate ...	No pull-up ...	Very High
PC3_C	OCTOSPIM_P1_IO0	n/a	Alternate ...	No pull-up ...	Very High
PC11	OCTOSPIM_P1_NCS	n/a	Alternate ...	No pull-up ...	Very High
PD12	OCTOSPIM_P1_IO1	n/a	Alternate ...	No pull-up ...	Very High

Schematic:



Project Properties

```
cmd.exe /C copy/Y "${BuildArtifactFileName}.elf"
..\W25Q128_STM32H7B0VBT6.stldr"
```



Change Linker Script file. Click Browse and choose "linker.ld"

Uncheck "Discard Unused Sections (-WL,--gc-sections)"

