



bakhshipoor / STM32H7-W25Q128-External-Loader



<> Code

Issues

Pull requests

Actions

Projects

Security

Insights

STM32H7-W25Q128-External-Loader / README.md



bakhshipoor Update README.md

d2833ad · 2 years ago

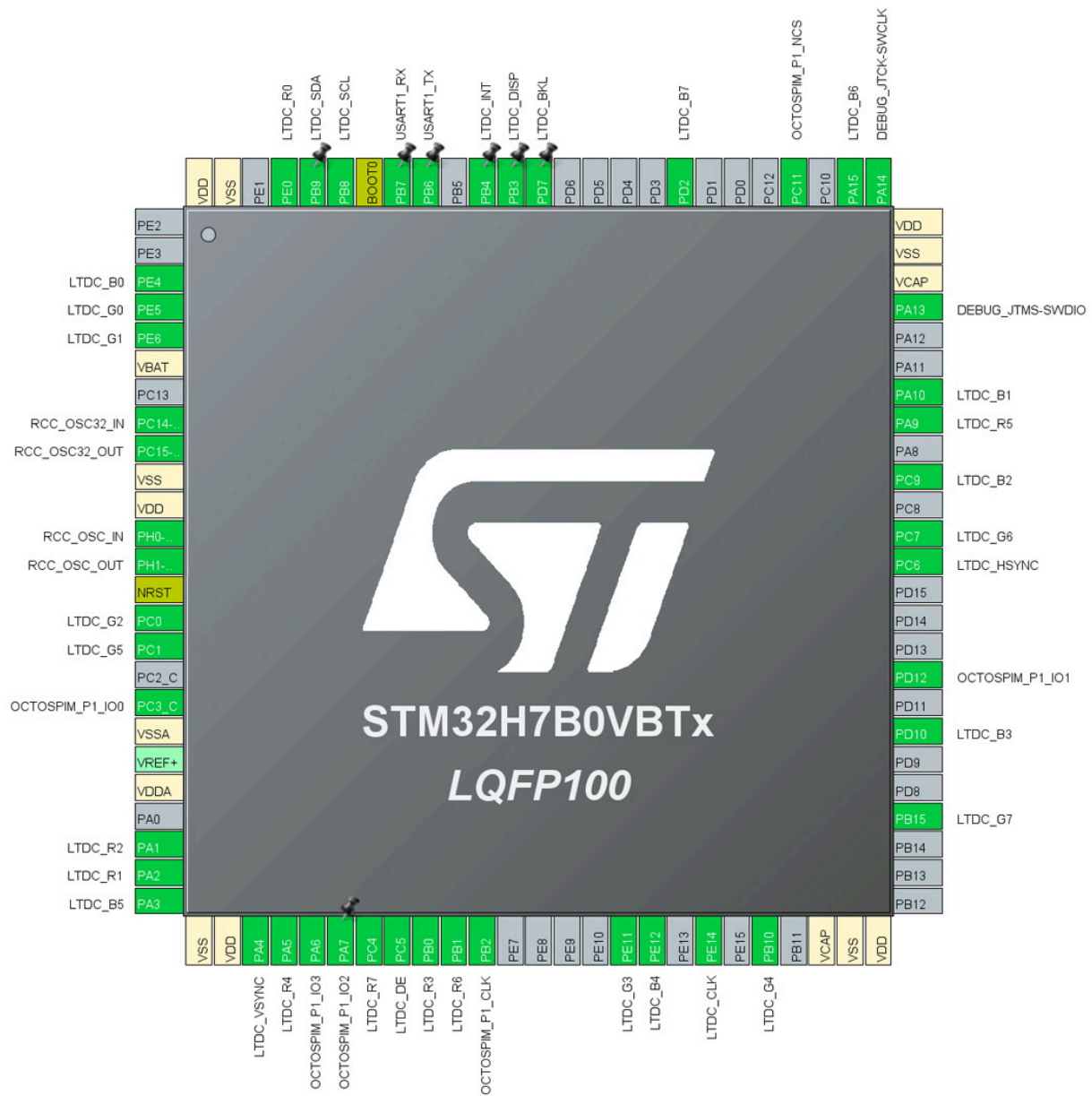


31 lines (25 loc) · 1.13 KB

STM32H7-W25Q128-External-Loader

W25Q128 External Loader for STM32H7b0VBT6

Pin Out:



OCTOSPI Configuration:

OCTOSPI1 Mode and Configuration

Mode

Mode Quad SPI

Clock Port1 CLK

HyperBus(TM) 1.8V Inverted Clock Disable

☒ Chip Select Port1 NCS

Data Strobe Disable

Data [3:0] Port1 IO[3:0]

Data [7:4] Disable

Configuration

Reset Configuration

☒ NVIC Settings

☒ GPIO Settings

☒ MDMA Settings

☒ Parameter Settings

☒ User Constants

Configure the below parameters :

Generic

Fifo Threshold

1

Dual Quad mode

Disable

Memory Type

Micron

Device Size

24

Chip Select High Time

8

Free Running Clock

Disable

Clock Mode

Low

Wrap Size

Not Supported

Clock Prescaler

2

Sample Shifting

Half Cycle

Delay Hold Quarter Cycle

Disable

Chip Select Boundary

0

Delay Block

Disable

Maximum Transfer

0

Refresh Rate

0

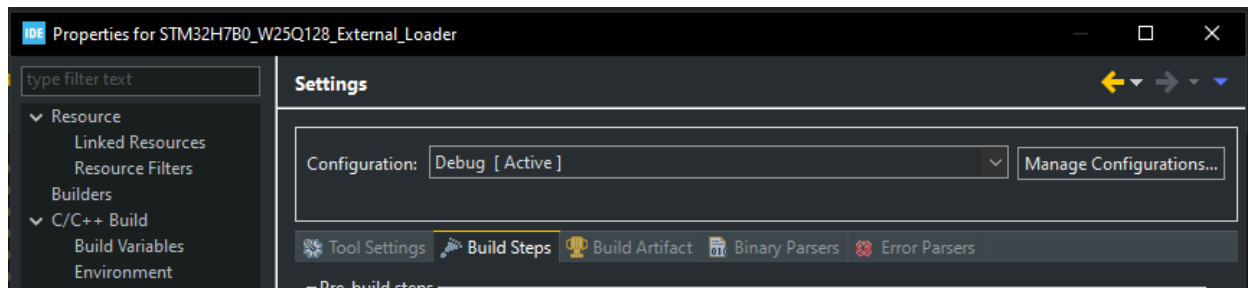
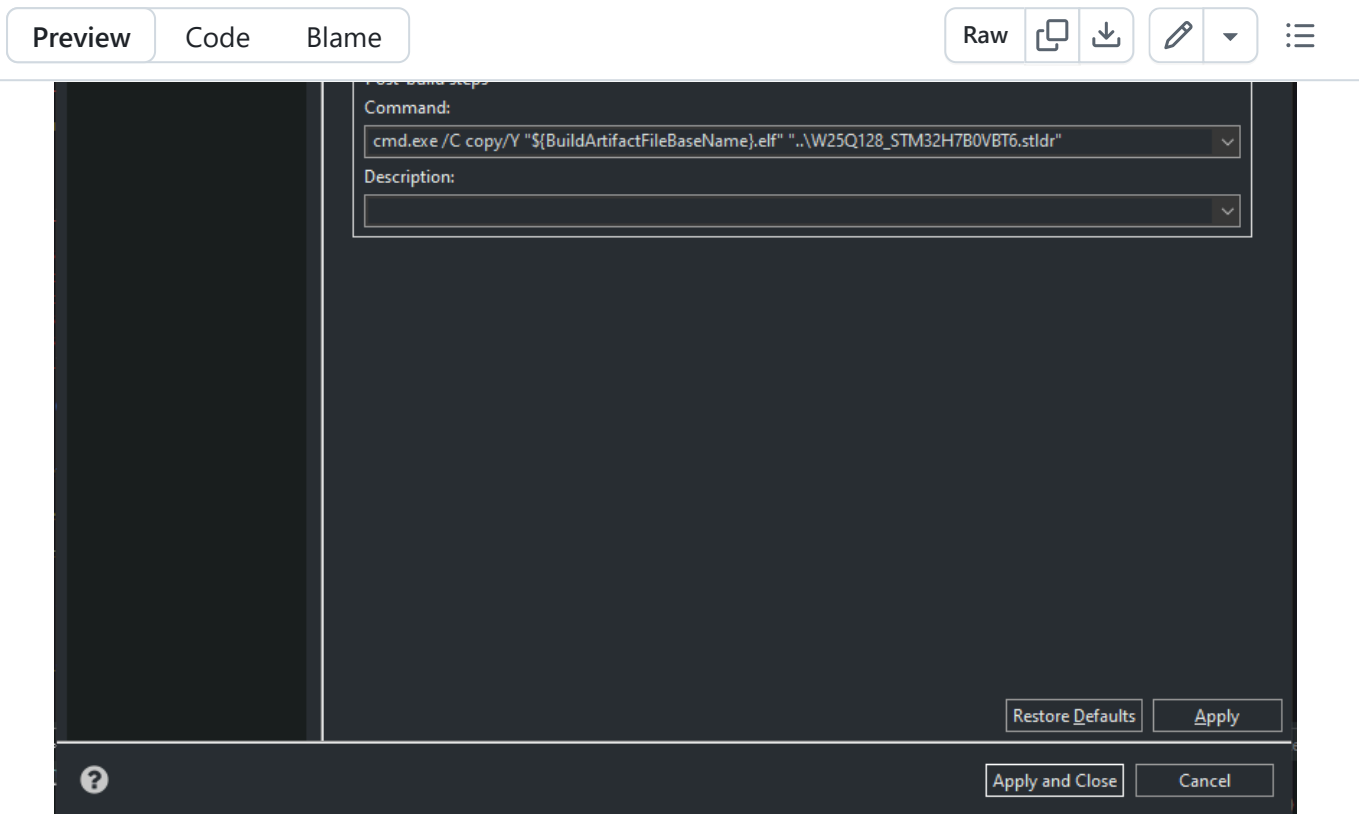
Mode

Configuration

Search (Ctrl+F)



5/7

[STM32H7-W25Q128-External-Loader / README.md](#)[↑ Top](#)

Change Linker Script file. Click Browse and choose "linker.ld"

Uncheck "Discard Unused Sections (-WL,--gc-sections)"

