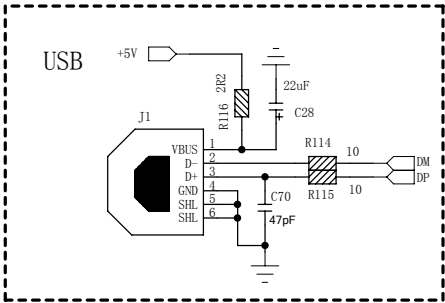
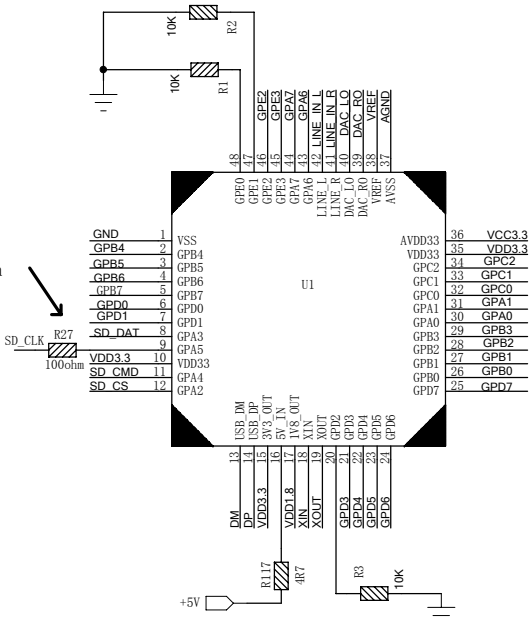


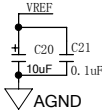
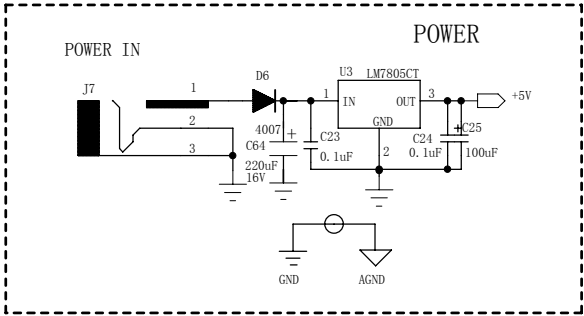
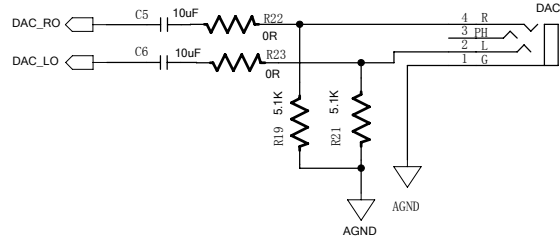
Mountain View Silicon



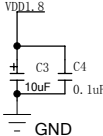
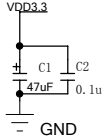
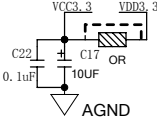
R27 optional if routing is short enough



R117 NO Need if IC is powered by +3.7V Li-BAT

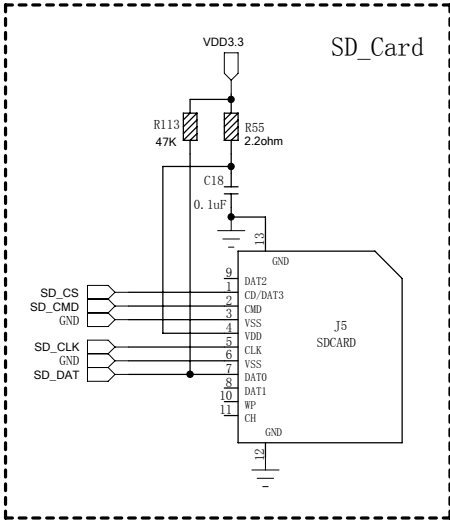
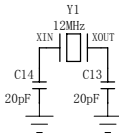


C20 close to pin



Slave Mode Setting:		
GPE0	GPD2	GPE1
0	0	0

RXD TXD ↔ GPA0/1 : Slave Mode UART



DRAWN:	DATED:	COMPANY: Au6850c Slave Mode_Design		
CHECKED:	DATED:	TITLE:		
APPROVED:	DATED:	PROJECT NO:	DRAWING NO:	
RELEASED:	DATED:	SIZE: A3	REV: V0.4	SHEET: 1 of 9