

**BIGTREE TECH**



**MANITA**

**M8P**

SCH.

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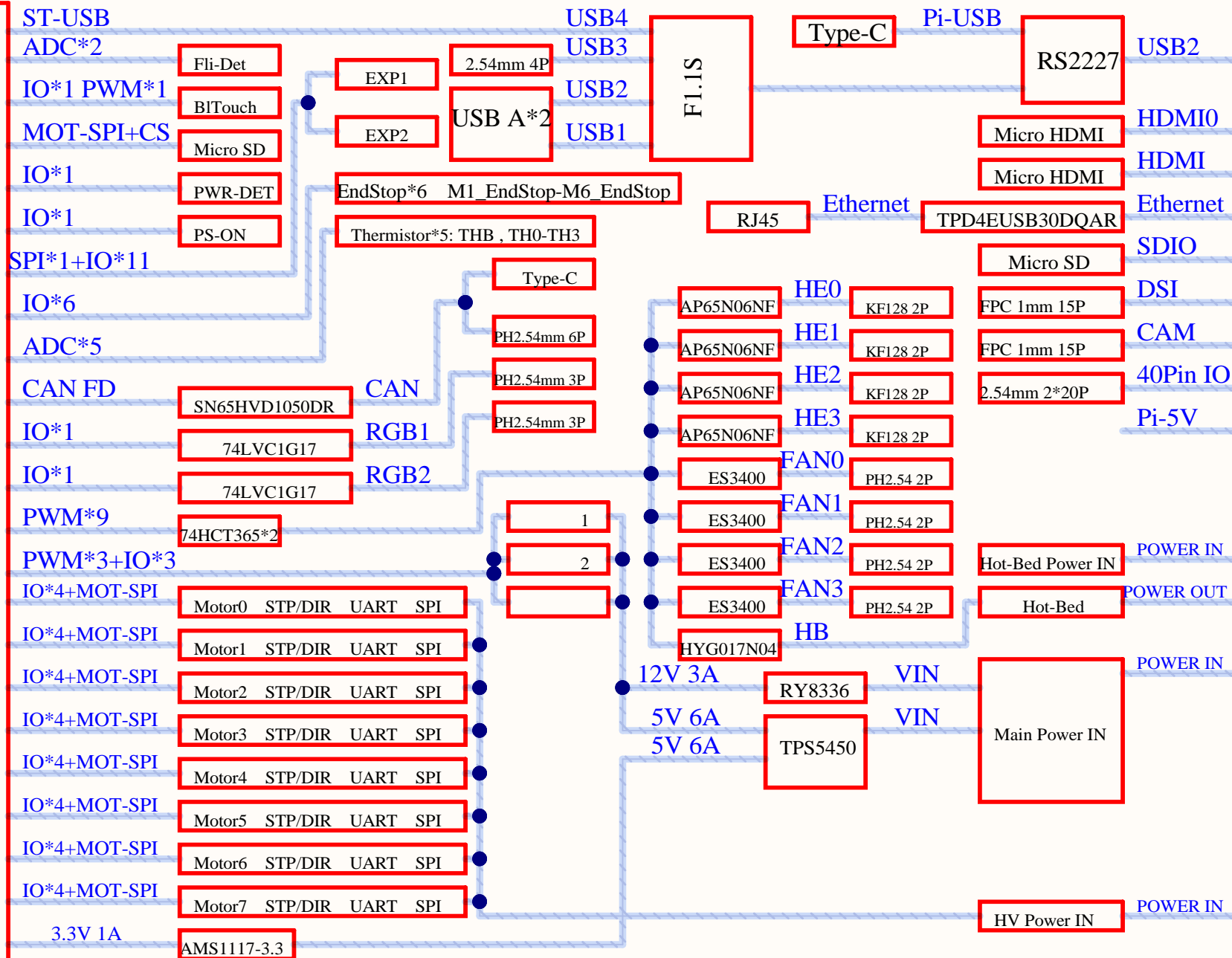
5.

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7.

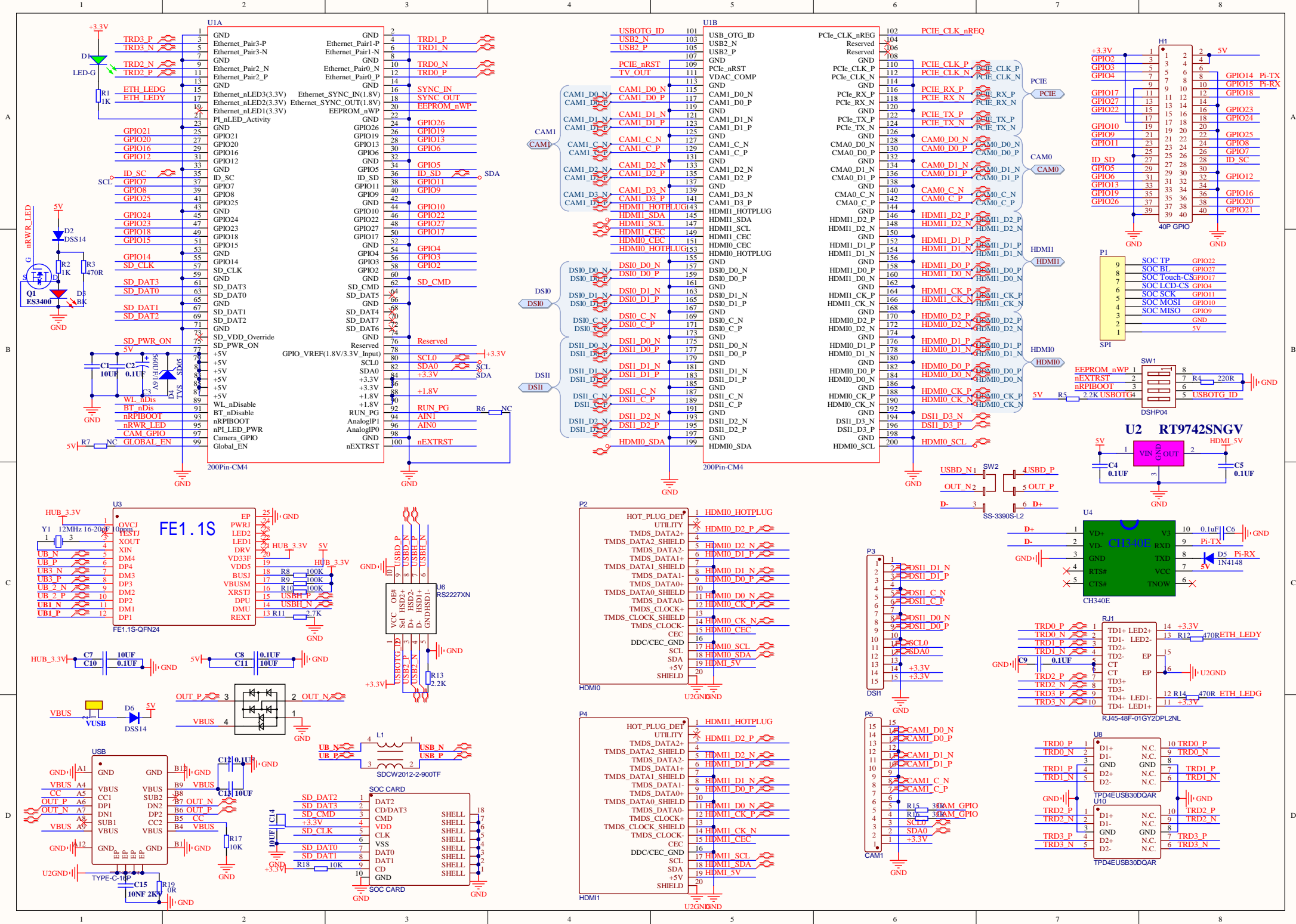
Version.

STM32H723ZET6



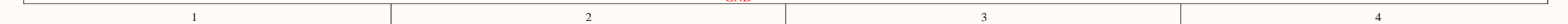
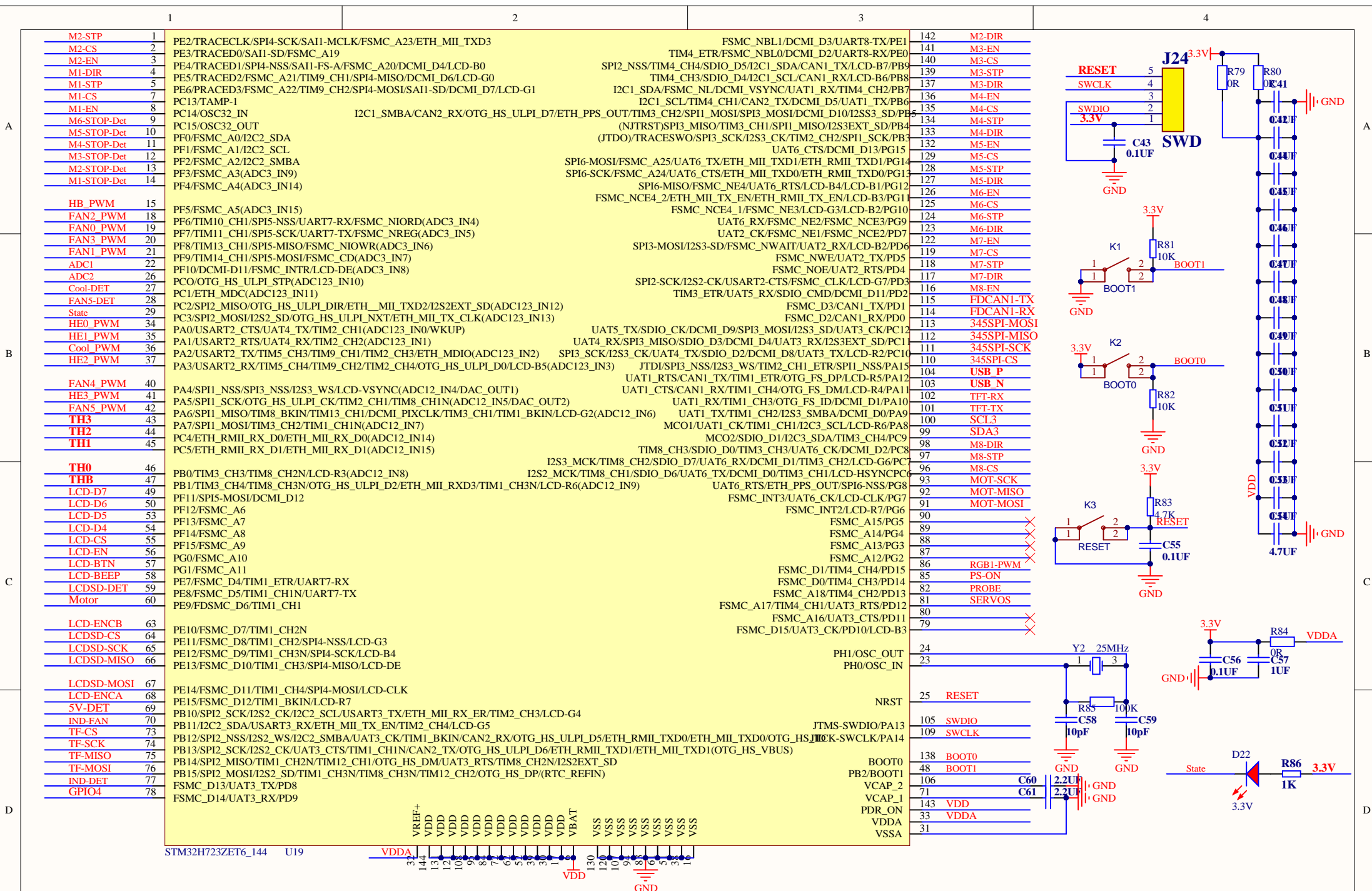
CM4

8P HB9500

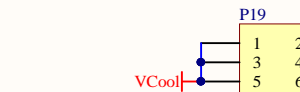
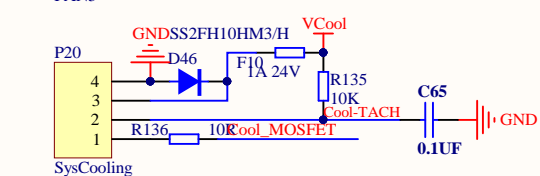
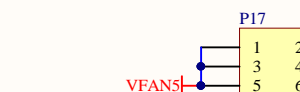
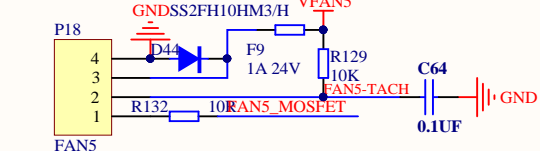
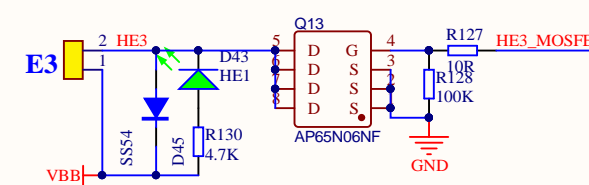
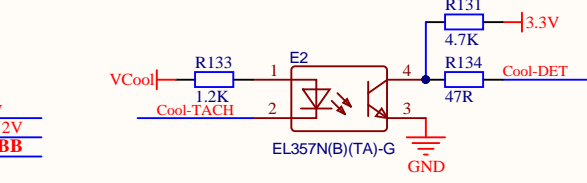
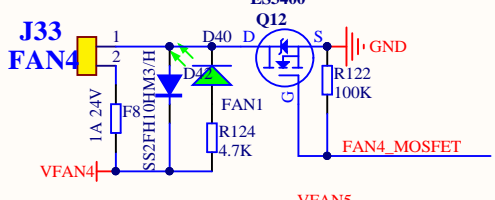
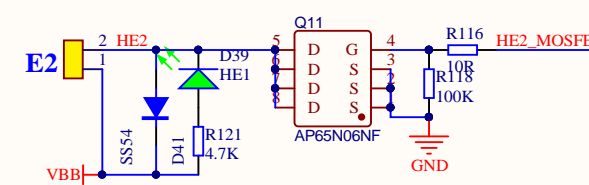
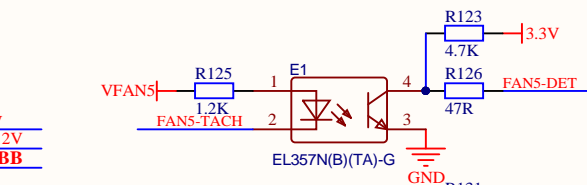
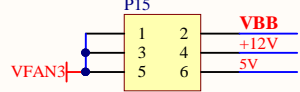
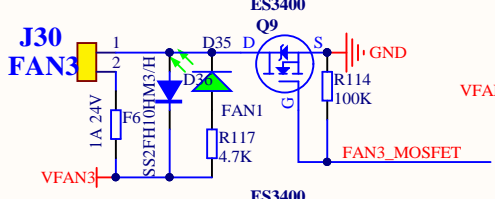
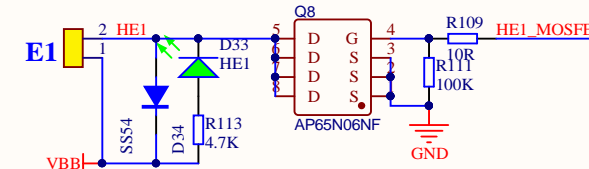
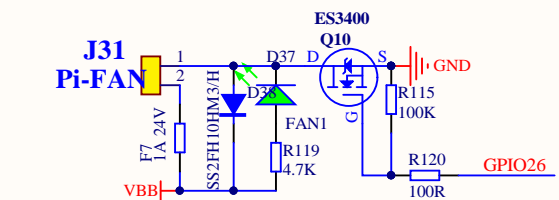
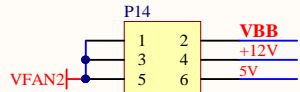
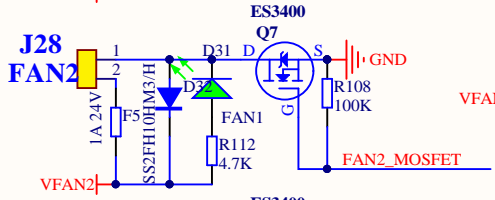
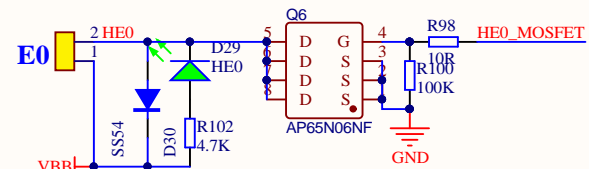
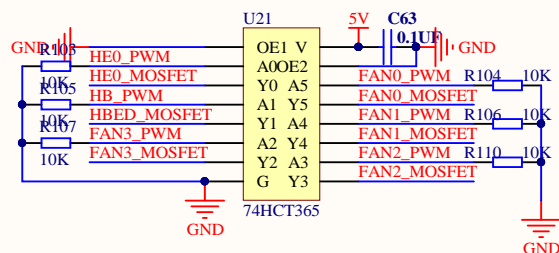
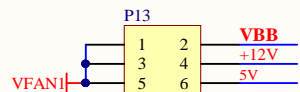
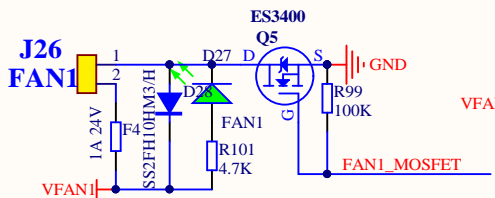
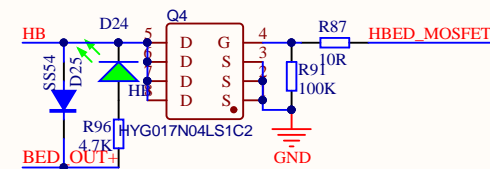
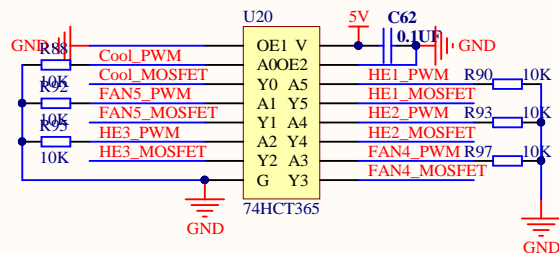
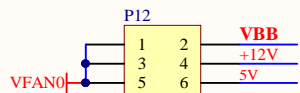
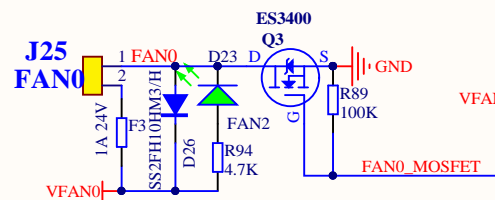


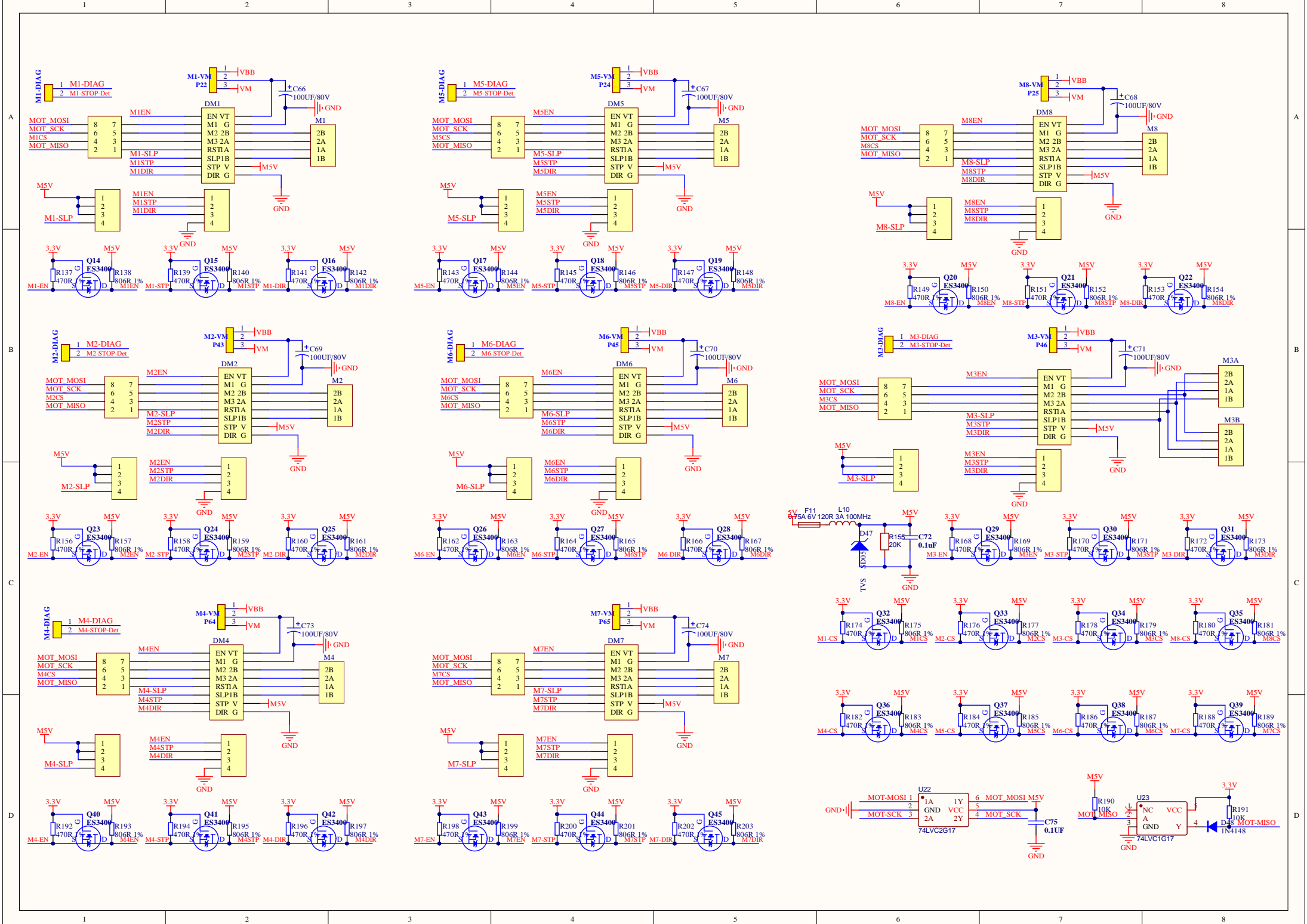




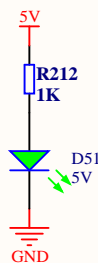
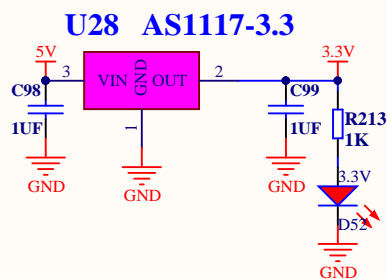
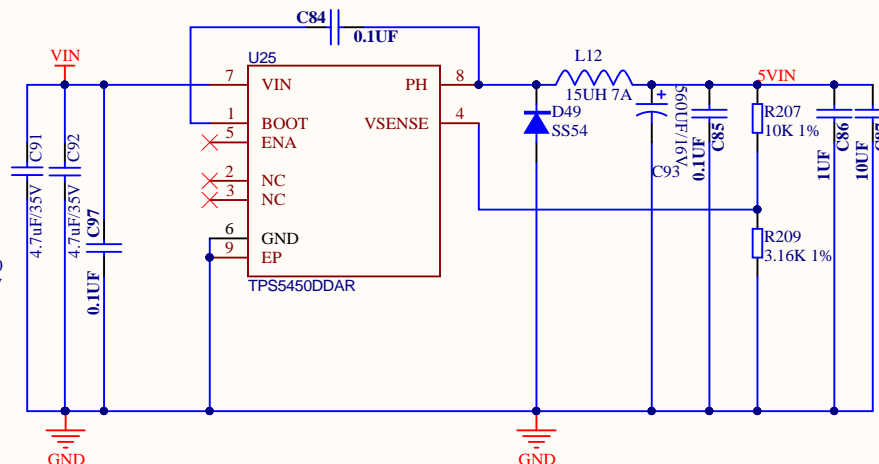
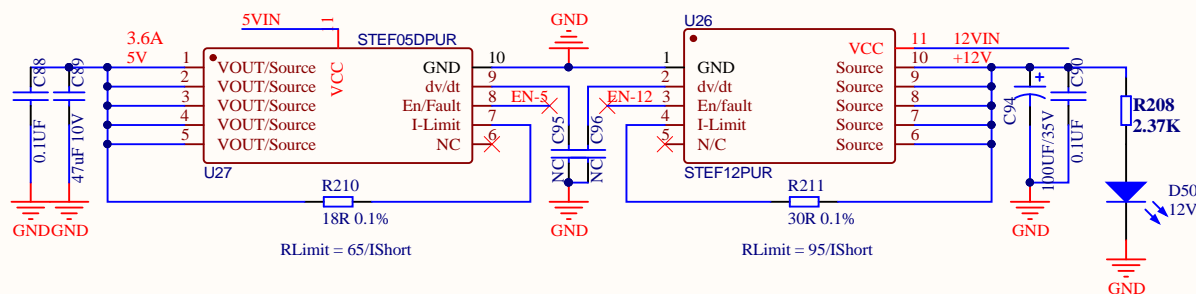
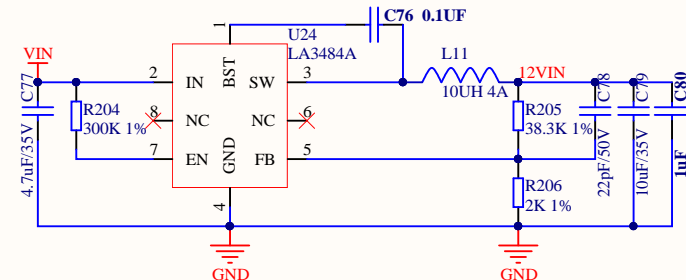
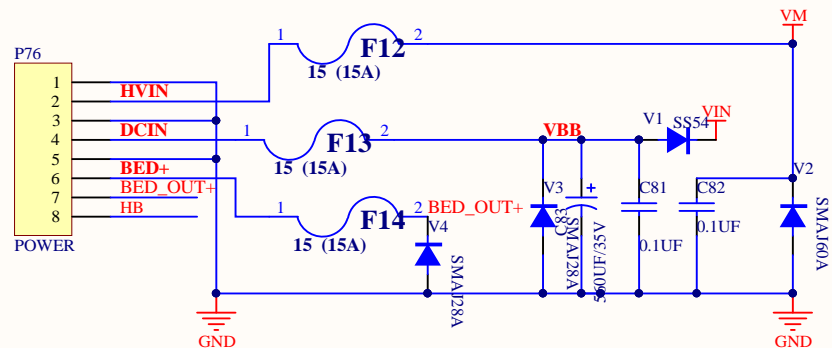












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A	<h1>Revision History</h1>																																		
B	<table><tr><th>Version</th><th>Date</th><th>Author</th><th>Change Note</th></tr><tr><td>V2.0</td><td>2023.06.13</td><td>Byron JK</td><td></td></tr><tr><td>V2.0.1</td><td>2024.04.13</td><td>Byron JK</td><td>Change the resistance value between the USB port casing and GND to 0R.</td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td></tr></table>			Version	Date	Author	Change Note	V2.0	2023.06.13	Byron JK		V2.0.1	2024.04.13	Byron JK	Change the resistance value between the USB port casing and GND to 0R.																				
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