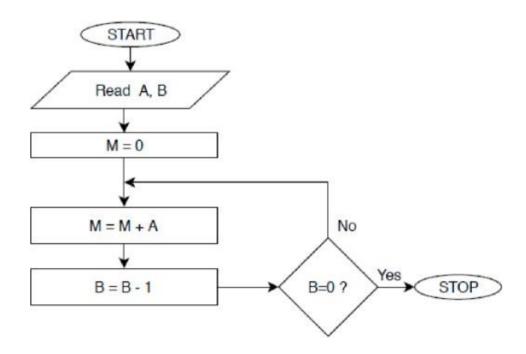## Control and Datapath

1. Following figure shows the flow chart for multiplication using repeated addition. Separate the data flow and control flow in this flow chart and write the Verilog modules for both data path and control path.