IITBB Digital System Design and with Verilog

Date: June 10, 2025

Mealy and Moore Machines

1. Simulate the codes of 101 sequence detectors (both Mealy and Moore machines) for non-overlapping cases.
2. Write the Verilog code for 101 sequence detectors (both Mealy and Moore machines) for overlapping case.
3. Write the Verilog code for 1010 sequence detectors (both Mealy and Moore machines) for non-overlapping/overlapping case.

End of Assignment