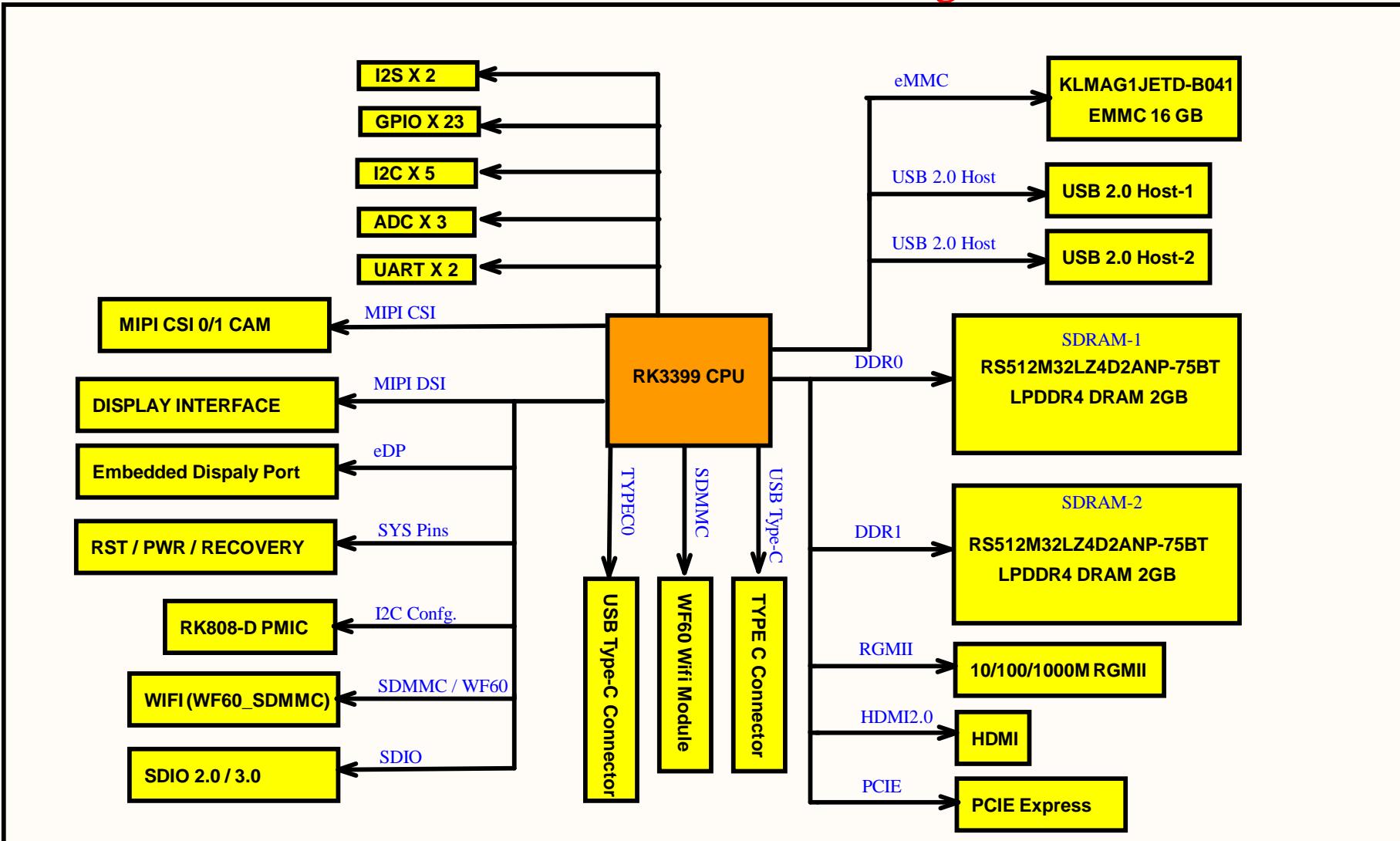


SOM Interface Block Diagram



TITLE: **RK3399 SOM Board Design V1.0**

DOCUMENT NAME: **[0]- Block Diagram.SchDoc**

PROJECT CODE: **A_V1.0**

Sheet **1** of **21**

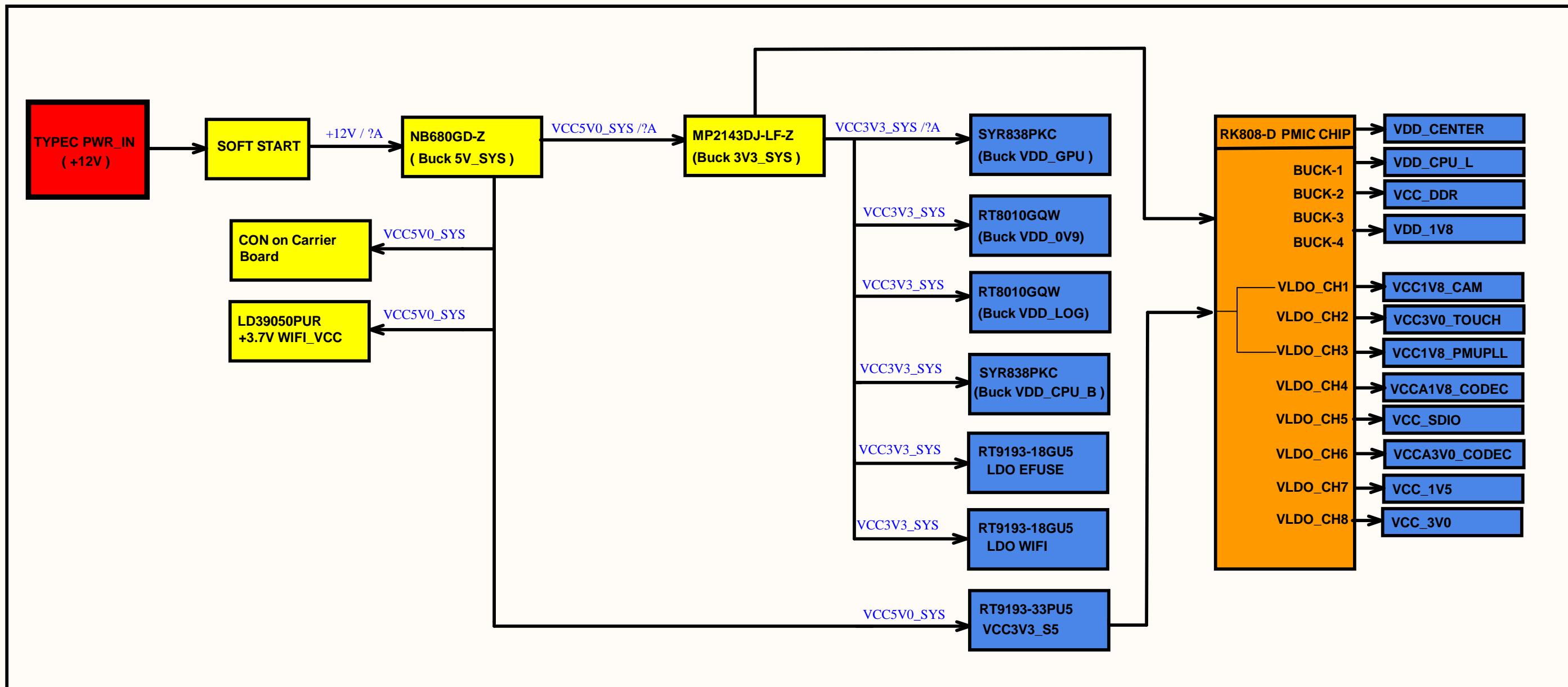
SCALE 1:1 TEMPLATE: **A4** AUTHOR: **AVIRAL MISHRA**

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ESTEEM PCB

Power Tree Block Diagram

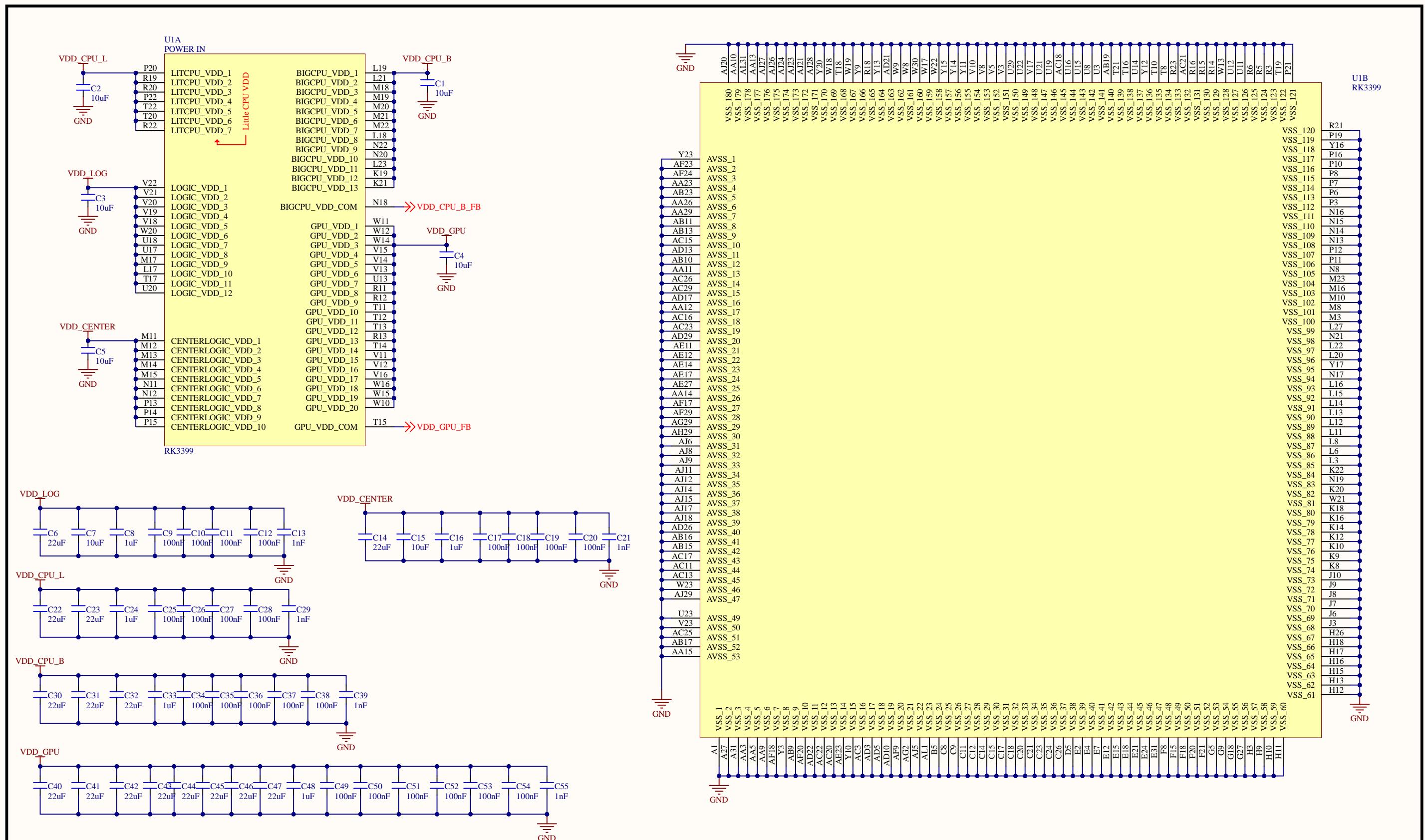


TITLE: RK3399 SOM Board Design V1.0		
DOCUMENT NAME: [0]-Power Blocks Diagram.SchDoc		
PROJECT CODE:	A_V1.0	Sheet 2 Of 21
SCALE 1:1	TEMPLATE: A3	AUTHOR: ABC
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ESTEEM PCB

Power Page of CPU



TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [1]-Power Pins Page of RK3399.SchDoc

PROJECT CODE: A_V1.0

Sheet 3 Of 21

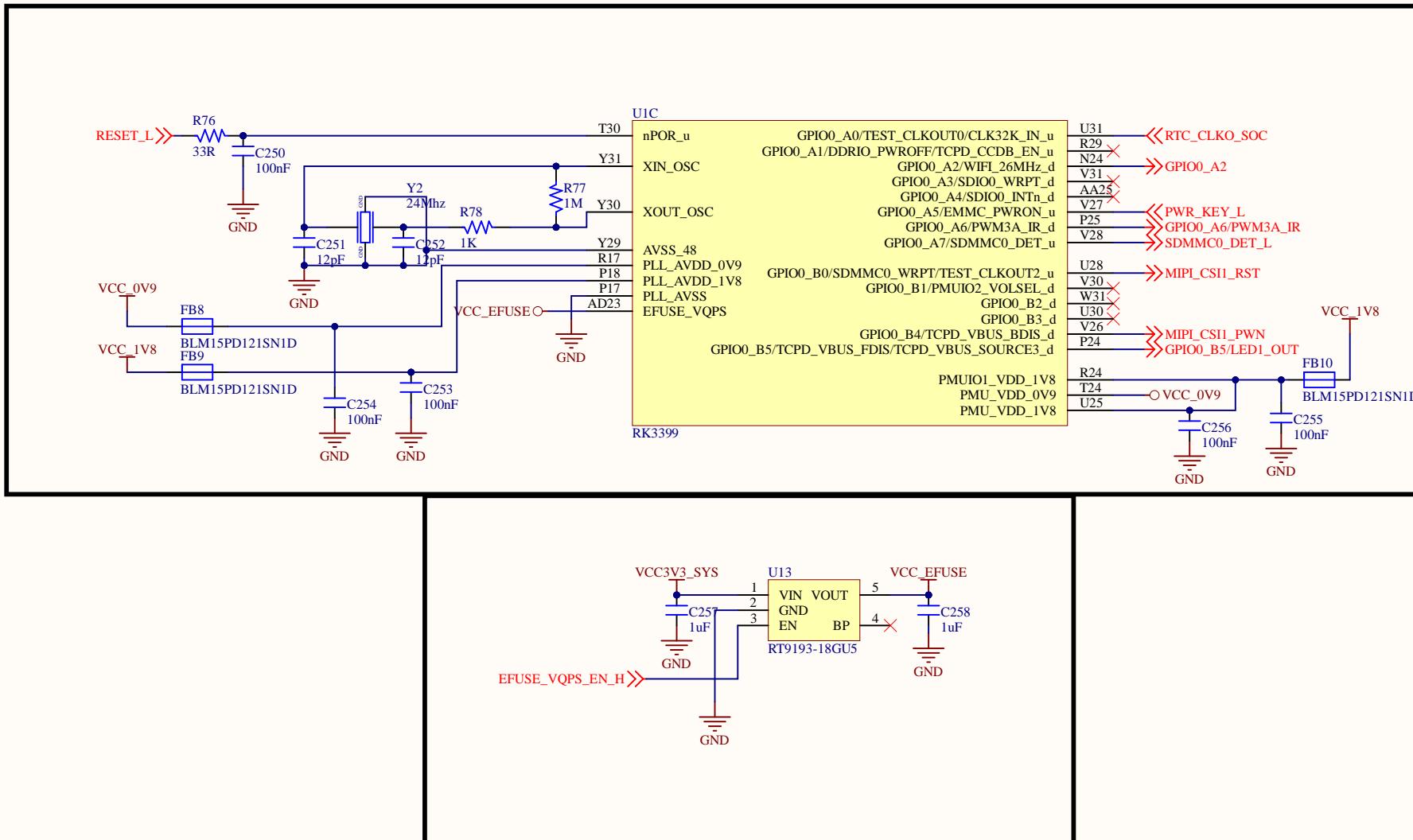
SCALE 1:1 TEMPLATE: A3 AUTHOR: ABC

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ESTEEM PCB

PMU Controller of CPU



TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [2]-PMUIO_BLOCK.SchDoc

PROJECT CODE: A_V1.0

Sheet 4 of 21

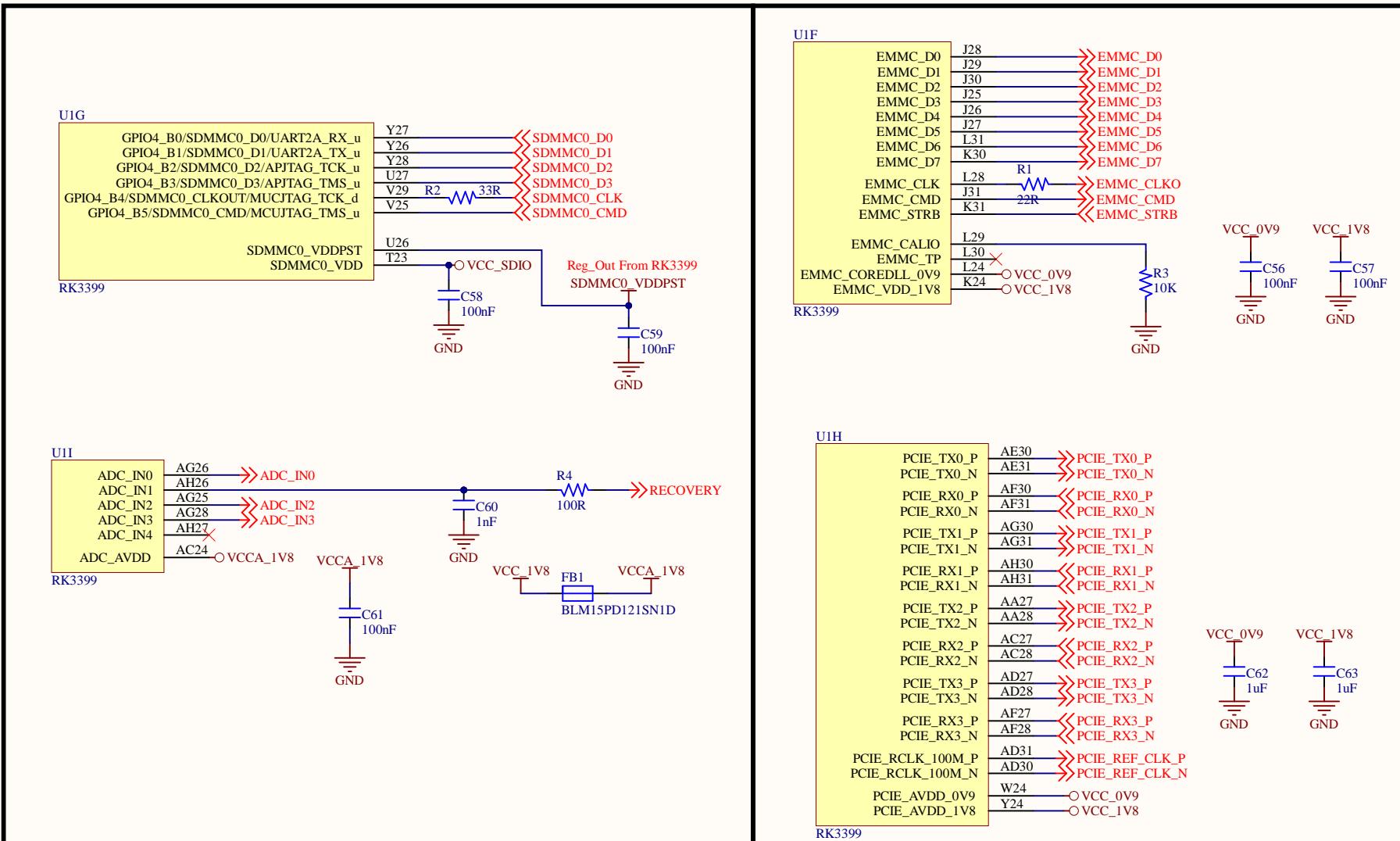
SCALE 1:1 TEMPLATE: A4 AUTHOR: AVIRAL MISHRA

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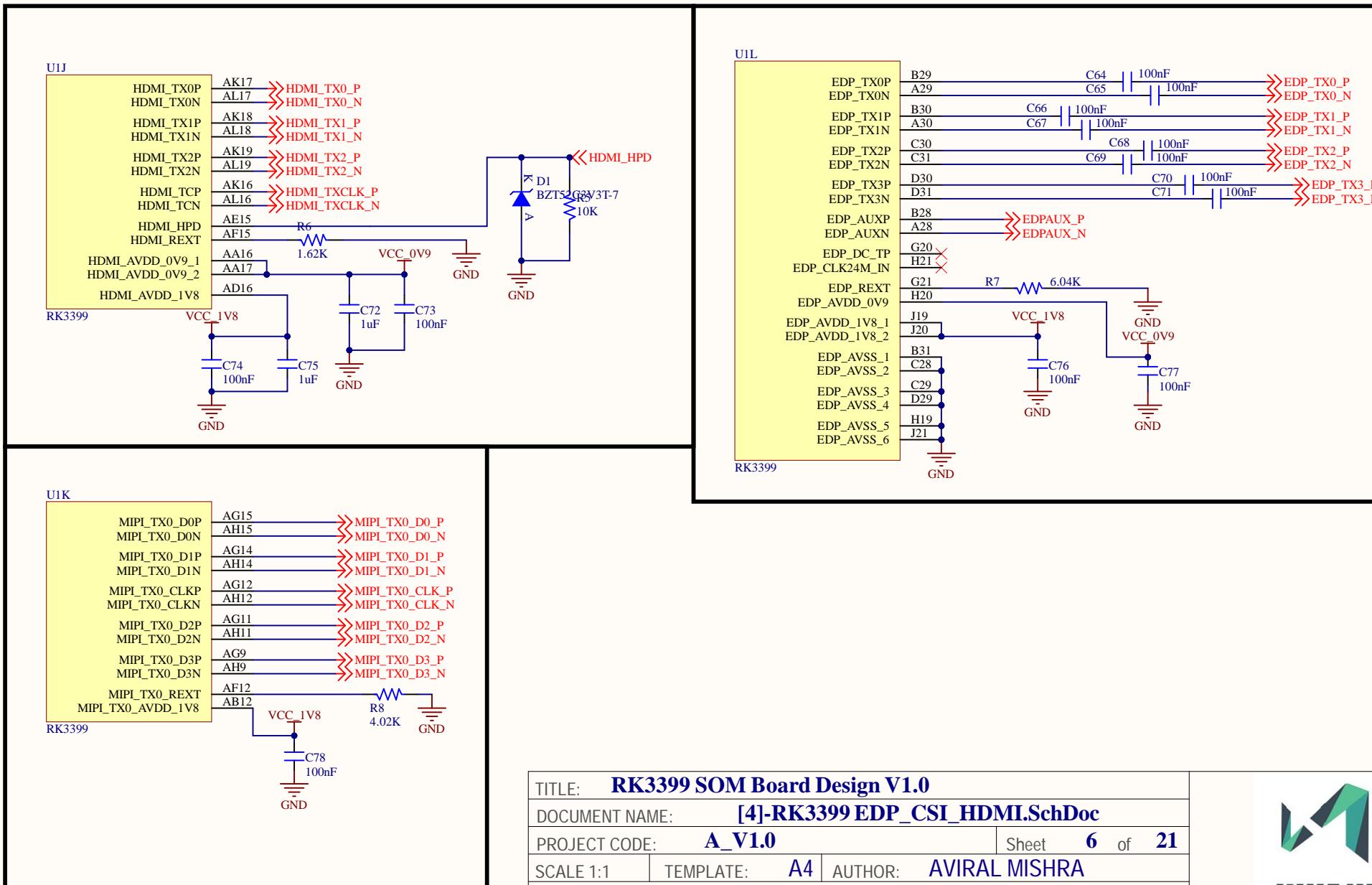
EMMC, SDMMC, PCIE and ADC of CPU



TITLE: RK3399 SOM Board Design V1.0	
DOCUMENT NAME: [3]-RK3399_EMMC_ADC_PCIE.SchDoc	
PROJECT CODE:	A_V1.0
SCALE 1:1	TEMPLATE: A4
AUTHOR: AVIRAL MISHRA	
Sheet 5 of 21	
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Display, HDMI and EDP of CPU



TITLE: **RK3399 SOM Board Design V1.0**

DOCUMENT NAME: **[4]-RK3399 EDP_CSI_HDMI.SchDoc**

PROJECT CODE: **A_V1.0** Sheet **6** of **21**

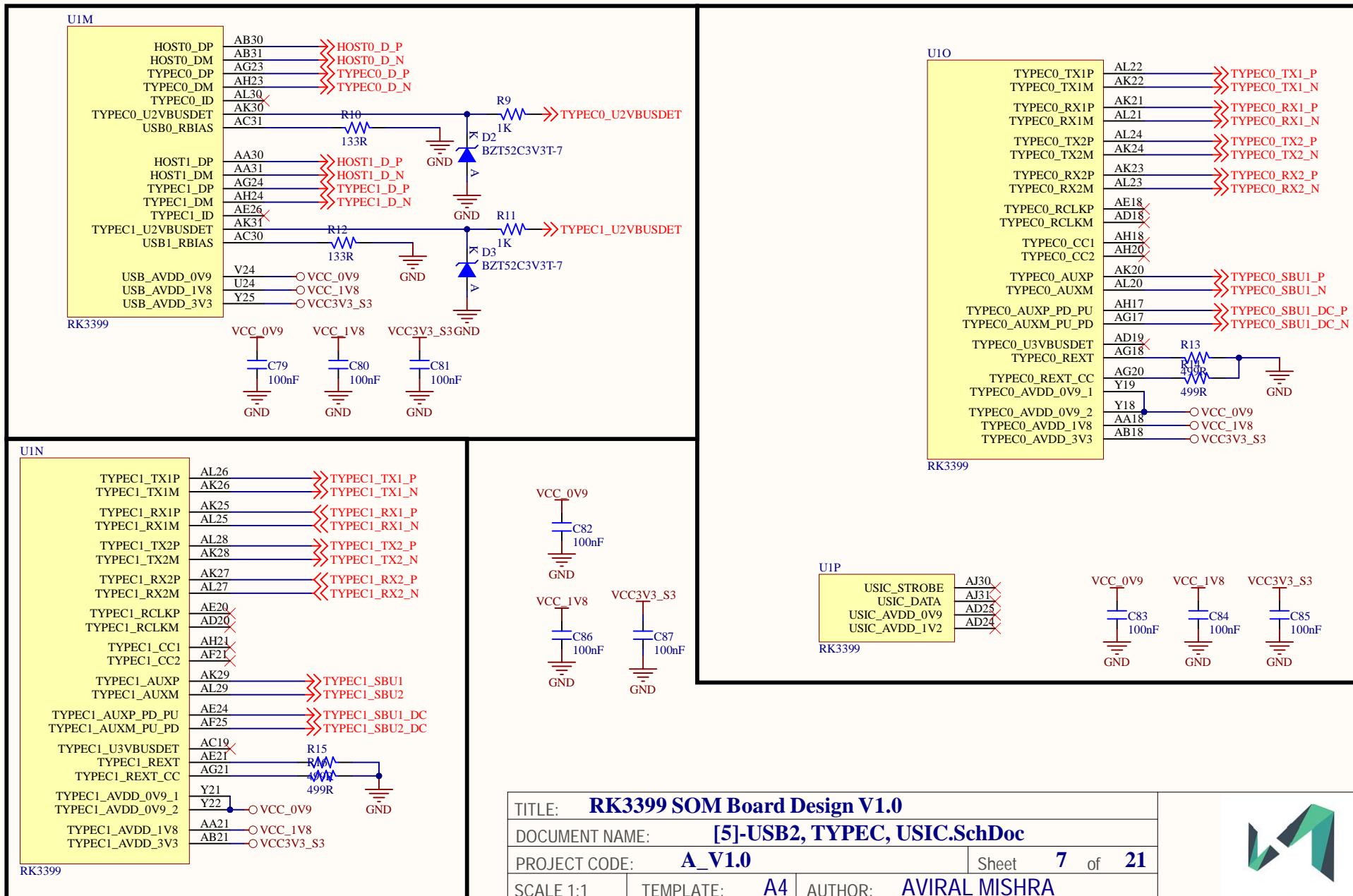
SCALE 1:1 TEMPLATE: **A4** AUTHOR: **AVIRAL MISHRA**

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ESTEEM PCB

USBs and USIC of CPU



TITLE: **RK3399 SOM Board Design V1.0**

DOCUMENT NAME: **[5]-USB2, TYPEC, USIC.SchDoc**

PROJECT CODE: **A_V1.0**

Sheet **7** of **21**

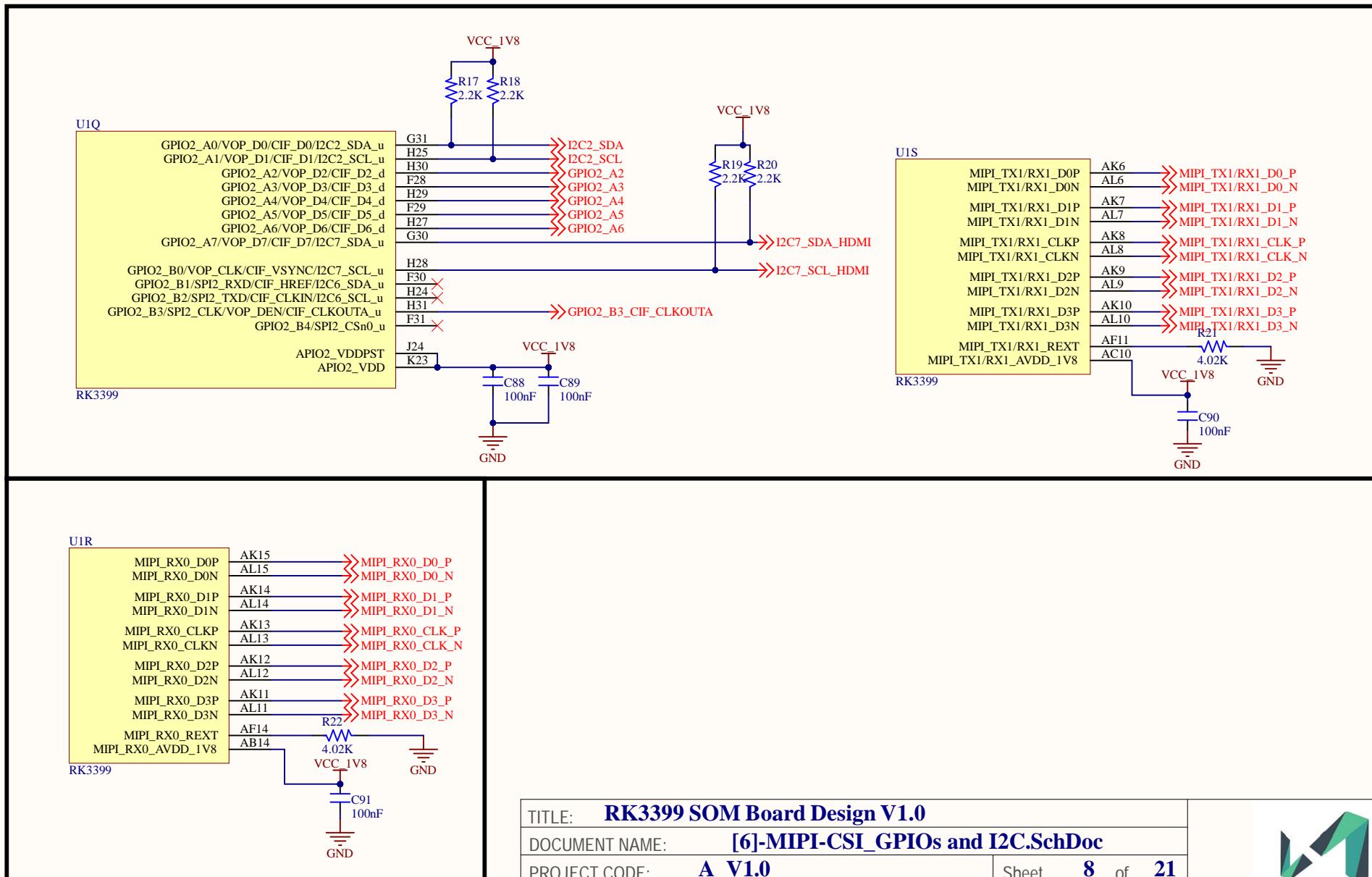
SCALE 1:1 TEMPLATE: **A4** AUTHOR: **AVIRAL MISHRA**

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ESTEEM PCB

DVP MIPI CSI FOR CPU



TITLE: **RK3399 SOM Board Design V1.0**

DOCUMENT NAME: **[6]-MIPI-CSI_GPIOS and I2C.SchDoc**

PROJECT CODE: **A_V1.0** Sheet **8** of **21**

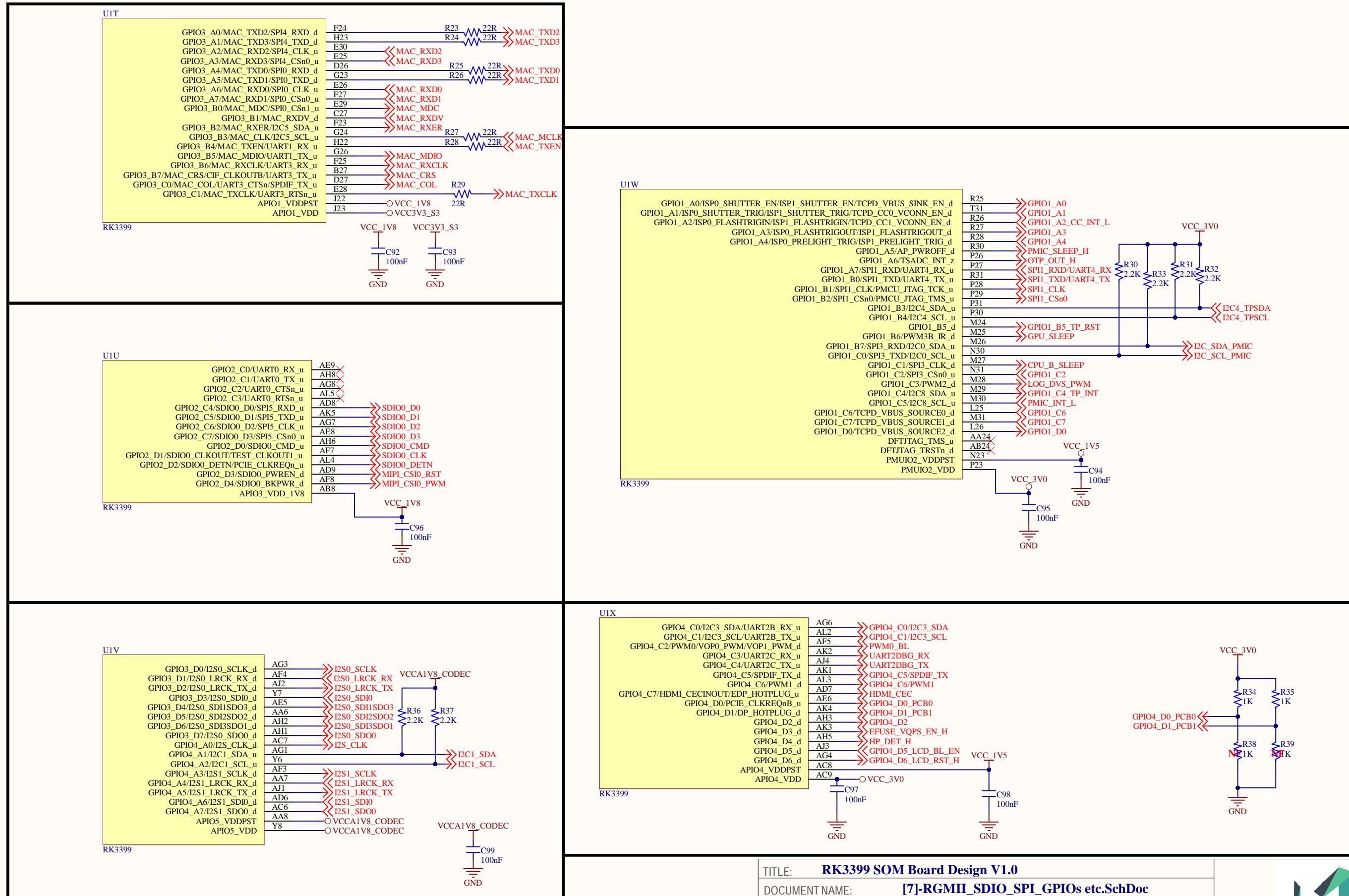
SCALE 1:1 TEMPLATE: **A4** AUTHOR: **AVIRAL MISHRA**

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ESTEEM PCB

RGMII/SDIO AND SPI Part of CPU



PK3200 SOM-B Rev. V1.0

DOCUMENT NAME [7] RGMII SDIO SPI CPIOs etc SubDoc

DOCUMENT NAME: **[]-RGMH**

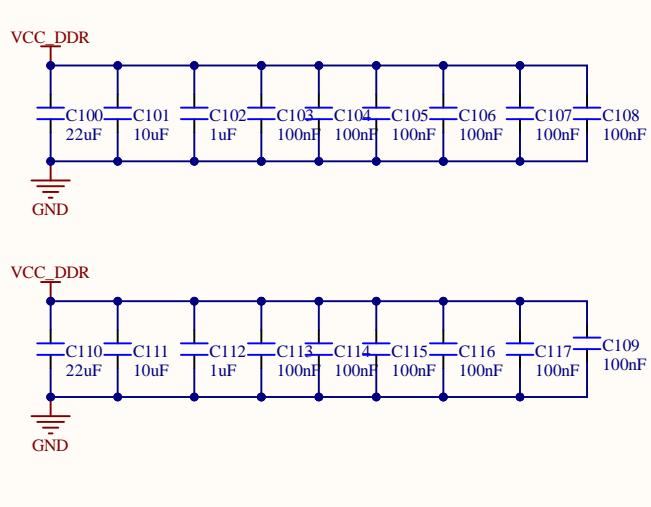
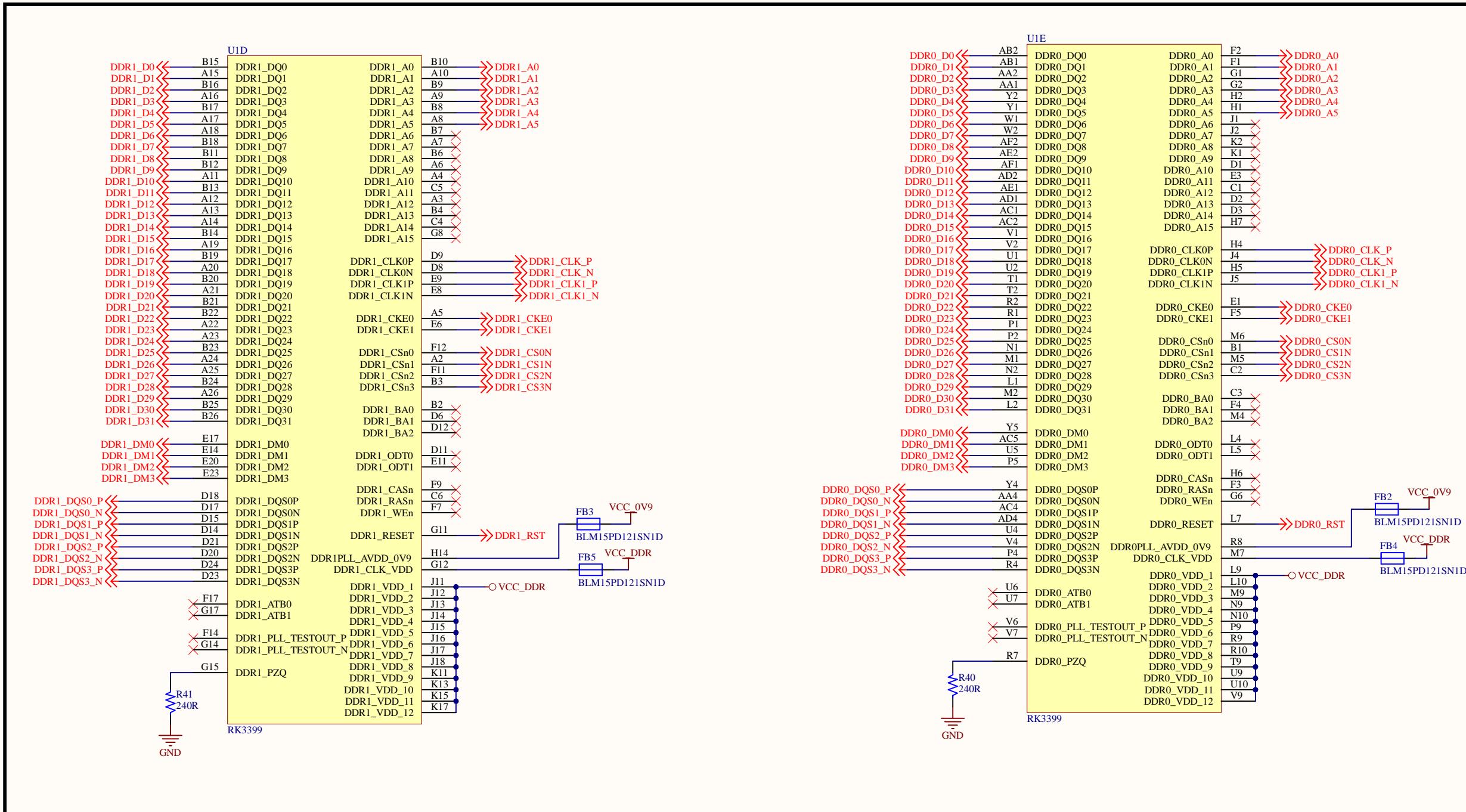
Sheet 9 of 21

SCALE 1:1 TEMPLATE: A3 AUTHOR: ABC
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ESTEEM RGB

LPDDR3 of CPU



TITLE: RK3399 SOM Board Design V1.0		
DOCUMENT NAME: [8]-RK3399 SDRAM Page.SchDoc		
PROJECT CODE:	A_V1.0	Sheet 10 Of 21
SCALE 1:1	TEMPLATE: A3	AUTHOR: ABC

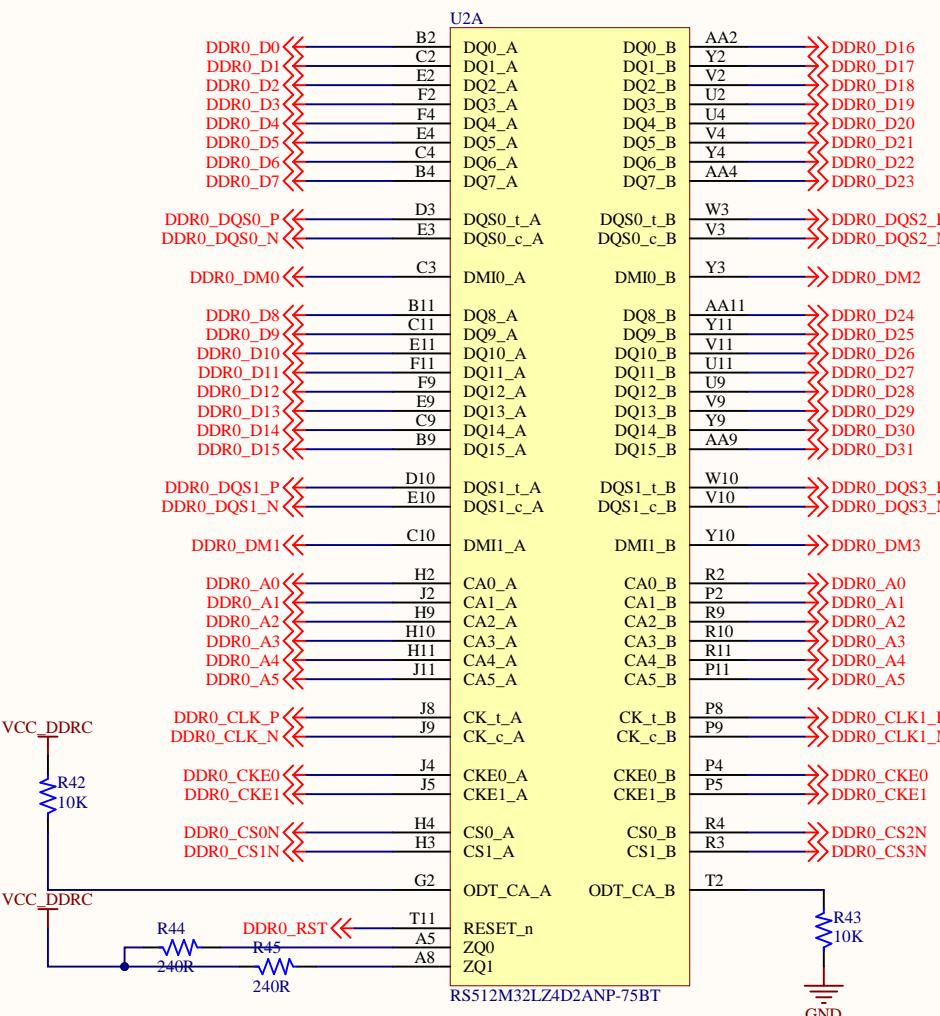
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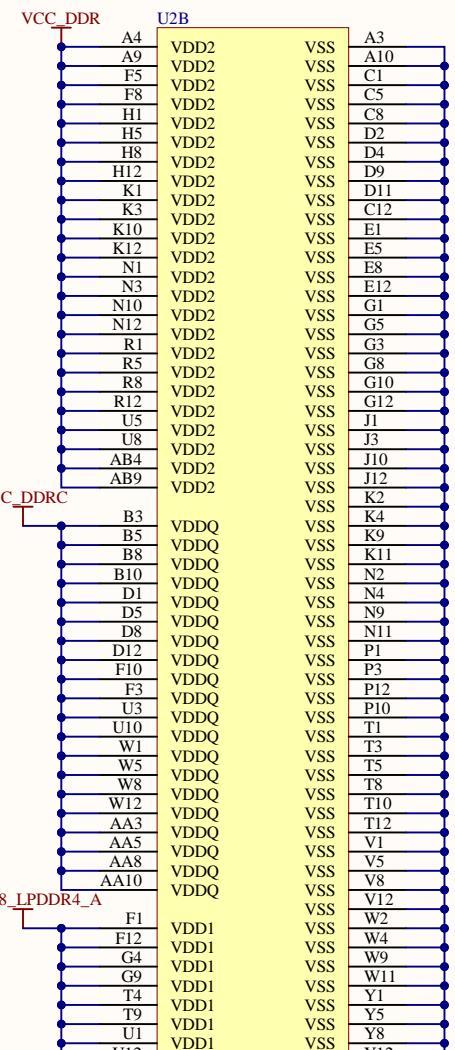
ESTEEM PCB

RS512M32LZ4D2ANP-75BT DRAM

A

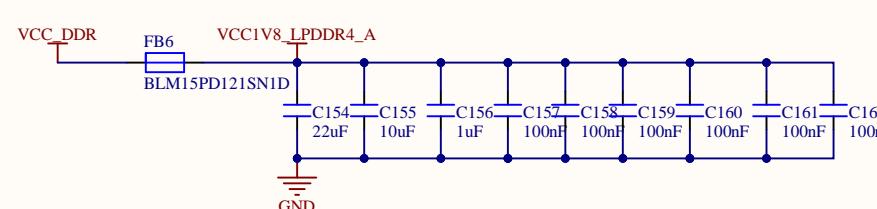
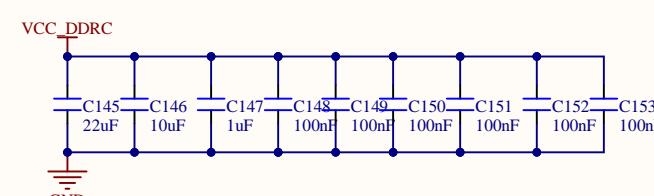
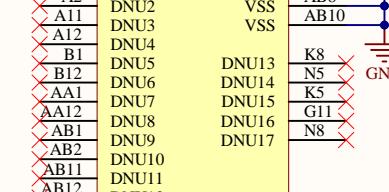
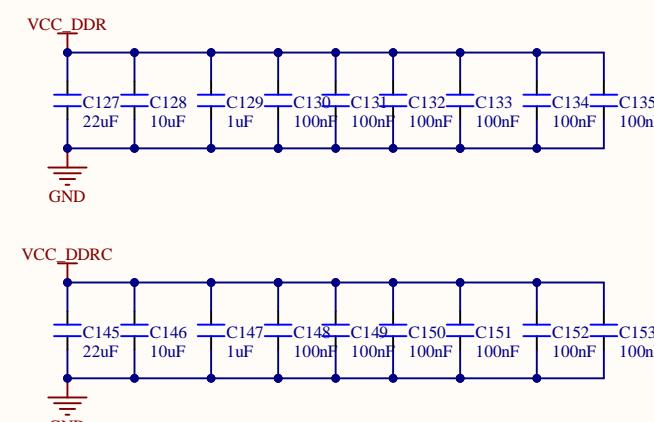
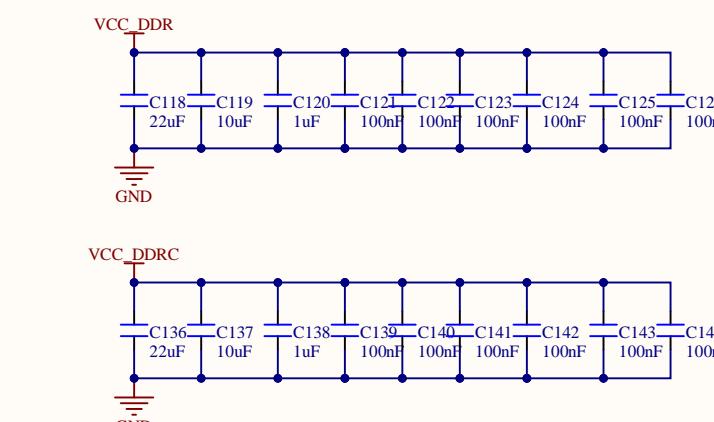


VCC1 -> VCC1V8_LPDDR4 (Supply Pins)
 VCC2 -> VCC_DDR (CA Power)
 VCCQ -> VCC_DDRC (I/O Buffer Power)



RS512M32LZ4D2ANP-75BT

B



TITLE: RK3399 SOM Board Design V1.0		
DOCUMENT NAME: [9]-LPDDR4 SDRAM-1.SchDoc		
PROJECT CODE: A_V1.0		Sheet 11 Of 21
SCALE 1:1	TEMPLATE: A3	AUTHOR: ABC
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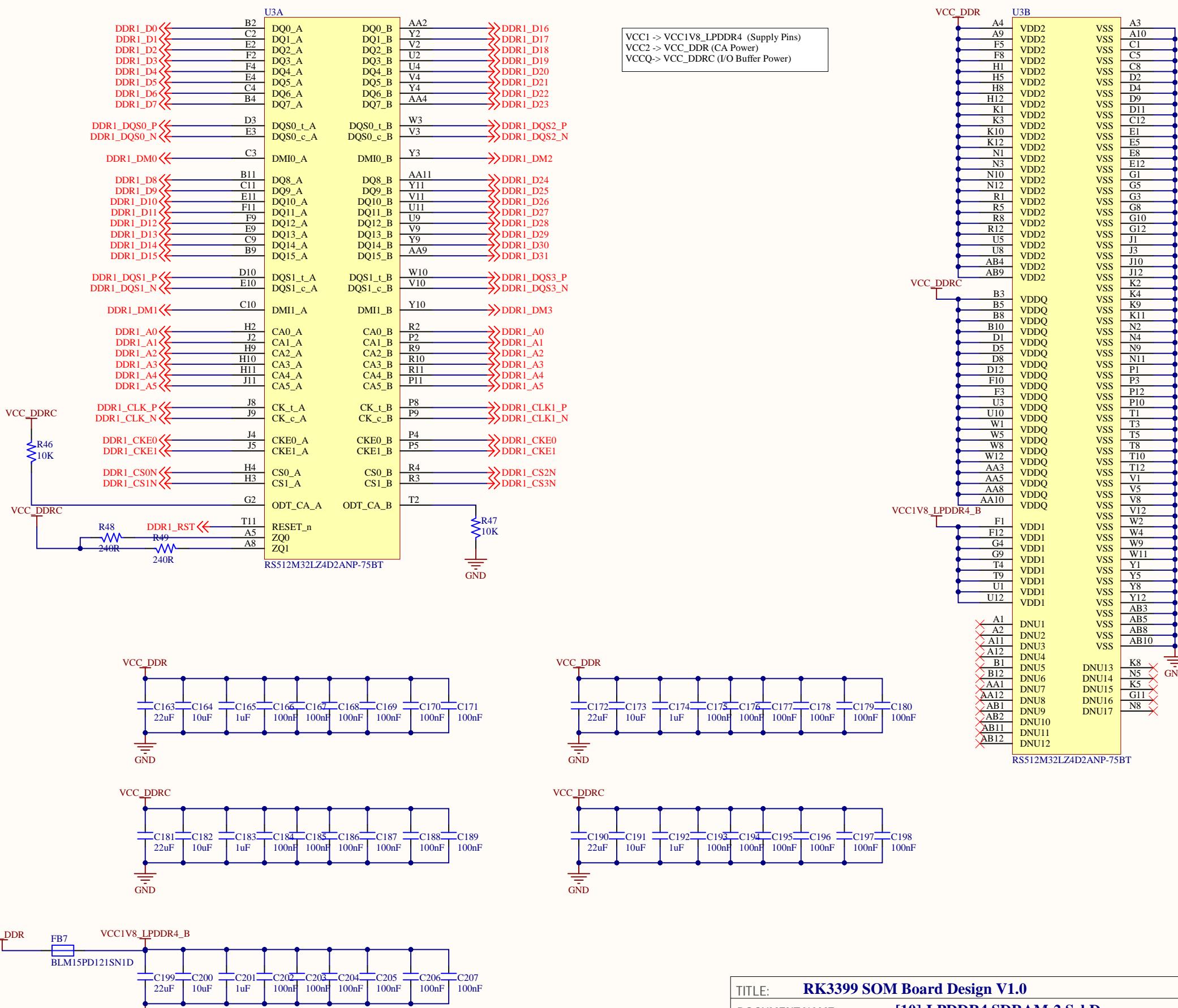
ESTEEM PCB

C

D

8

RS512M32LZ4D2ANP-75BT DRAM



TITLE BK23200 SOM Board Design V1.0

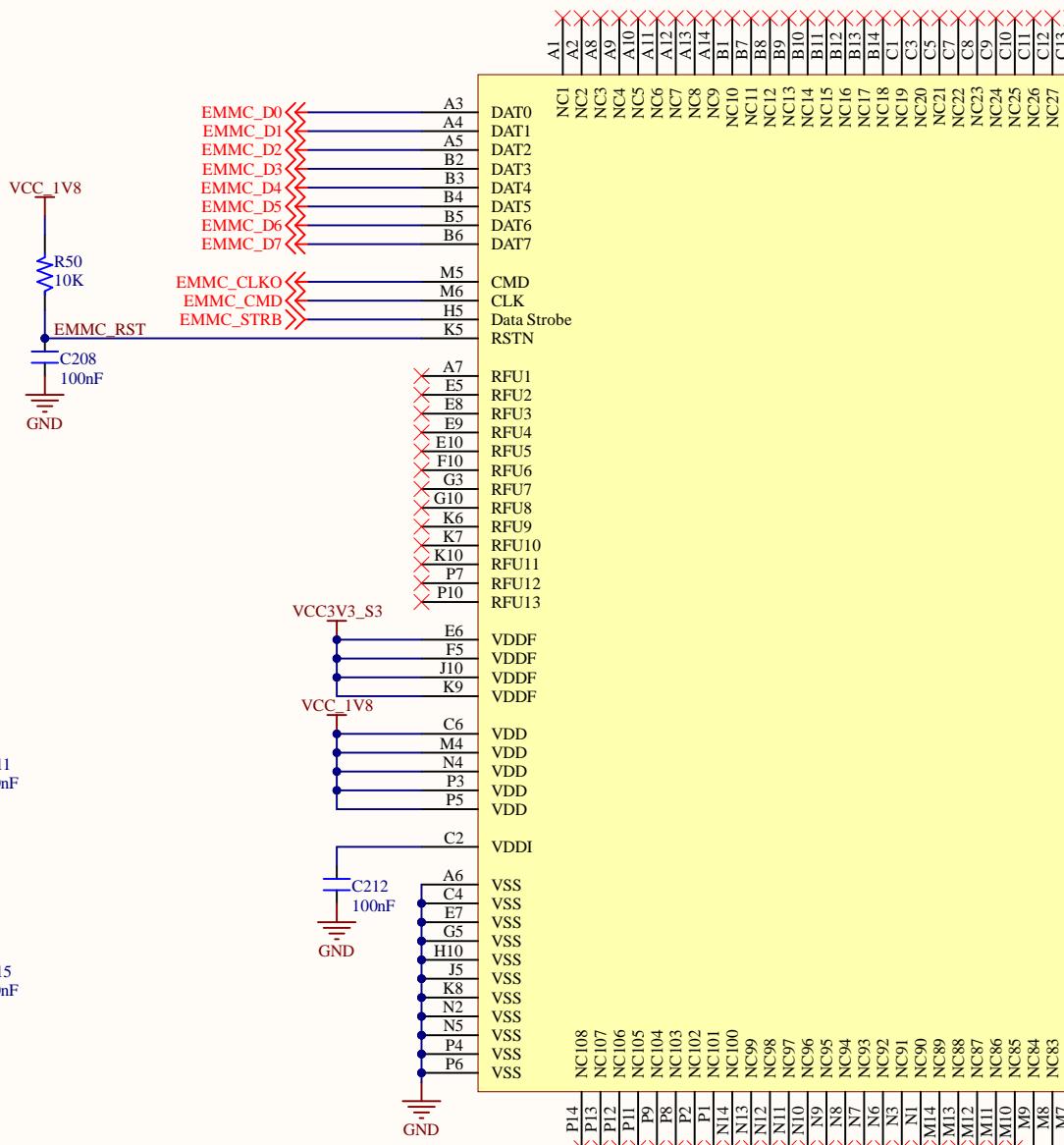
DOCUMENT NAME: [10]-LPDDR4 SDRAM-2 SchDec

PROJECT CODE: A V1.9

PROJECT CODE: —
SCALE 1:1 TEMPLATE: A3 AUTHOR: AB

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TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [11]-EMMC Chip.SchDoc

PROJECT CODE: A_V1.0

Sheet 13 of 2

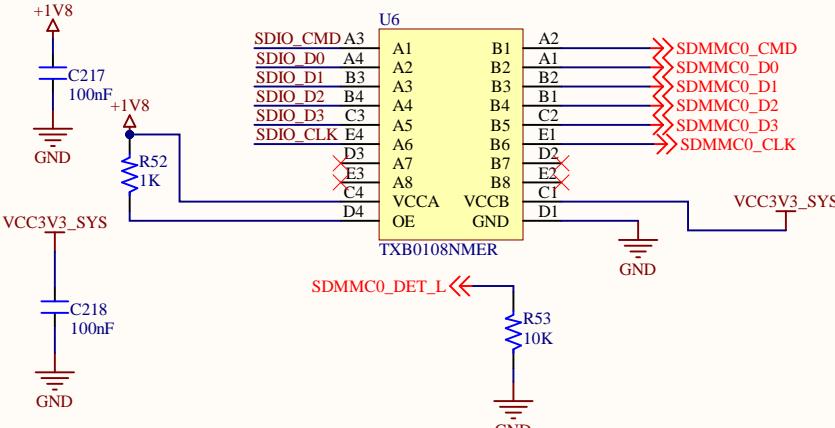
SCALE 1:1 TEMPLATE: A4 AUTHOR: AVIRAL MISHRA



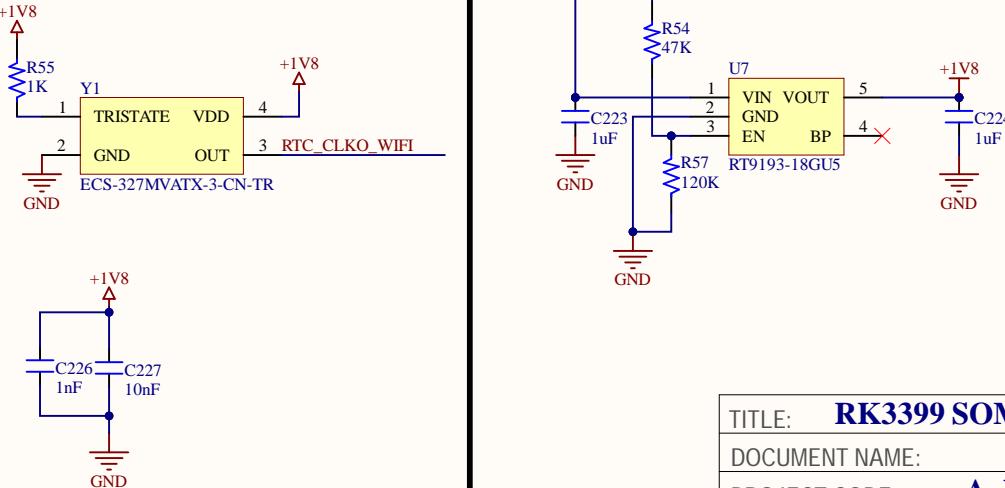
ESTEEM PCB

WF60 WIFI Module

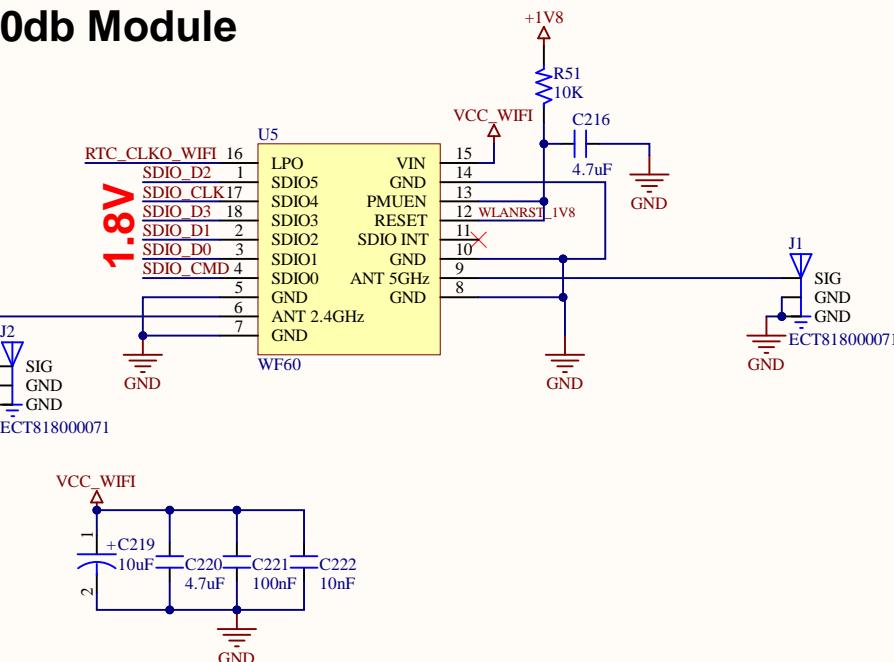
Level Shifter



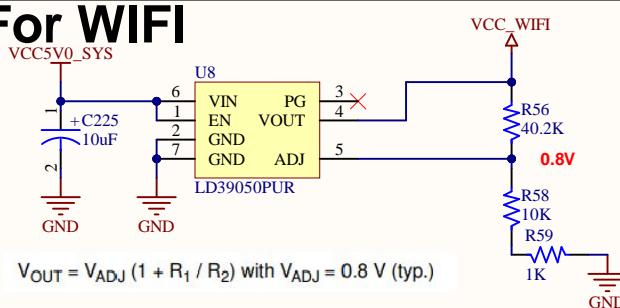
32.76 Khz Clock



WF60db Module



+3.7V For WIFI



TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [12]-WIFI Module WF60.SchDoc

PROJECT CODE: A_V1.0

Sheet 14 of 21

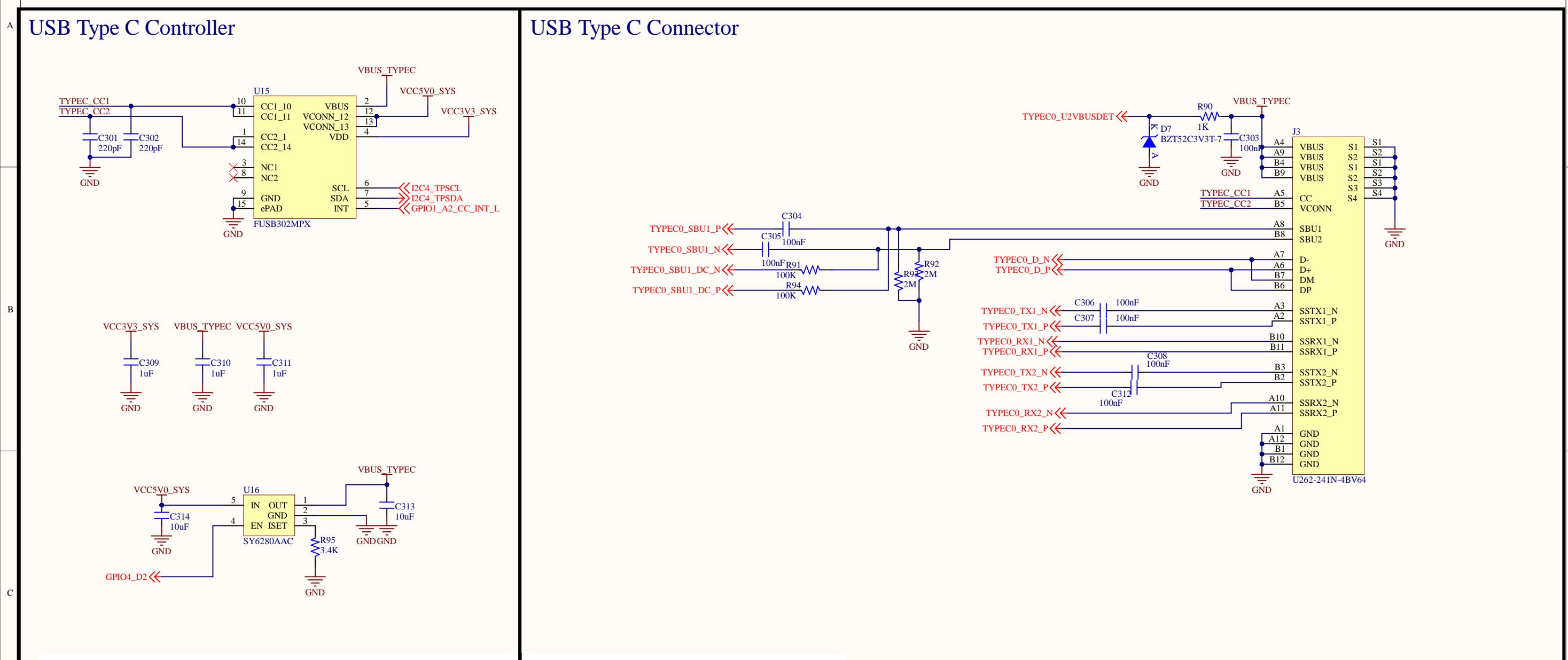
SCALE 1:1 TEMPLATE: A4 AUTHOR: AVIRAL MISHRA

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ESTEEM PCB

USB Type C Connector



A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12

[GND] [TX1+] [TX1-] [VBUS] [CC1] [D+] [D-] [SBU1] [VBUS] [RX2-] [RX2+] [GND]

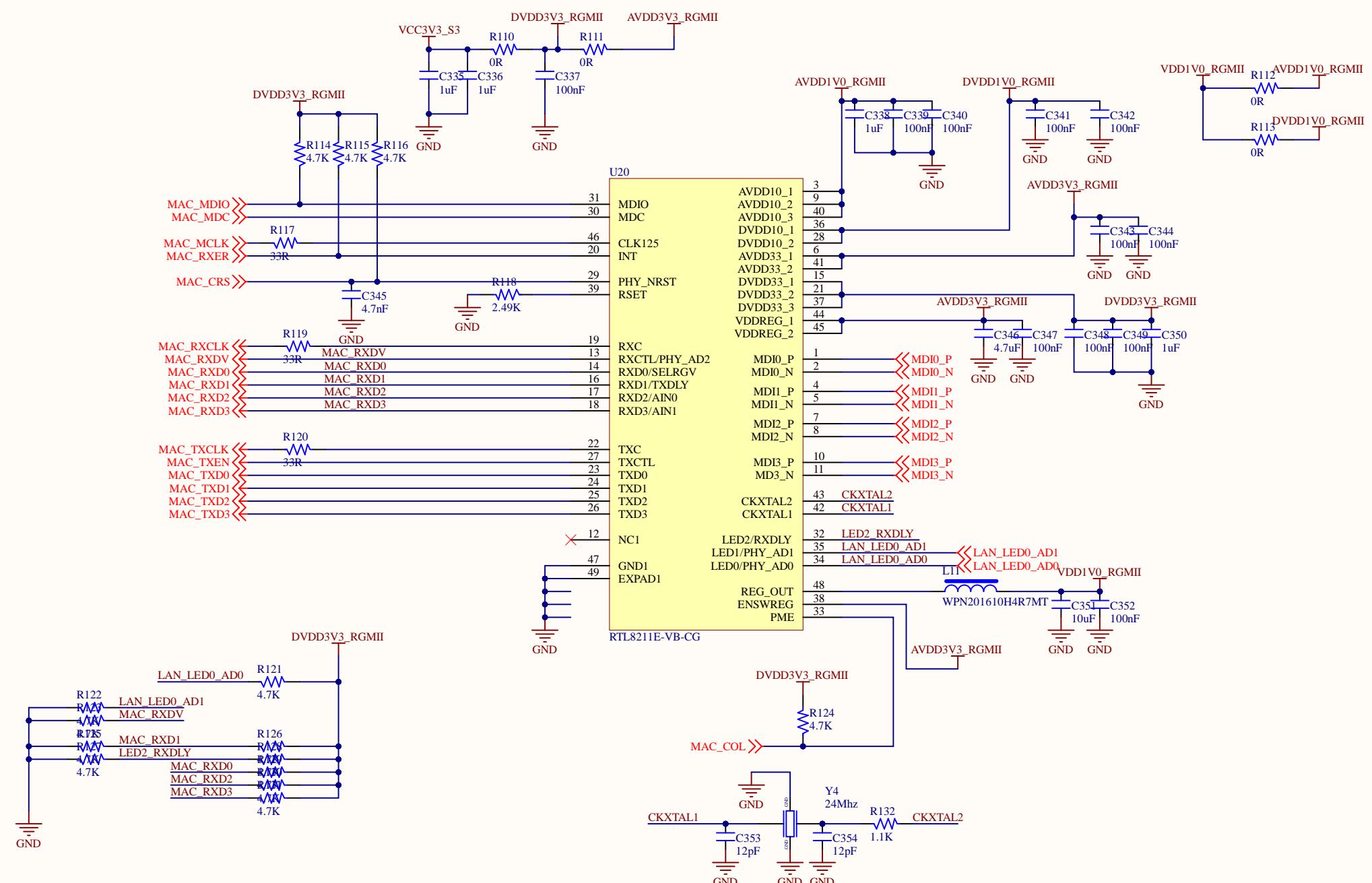


B12 B11 B10 B9 B8 B7 B6 B5 B4 B3 B2 B1

[GND] [RX1+] [RX1-] [VBUS] [SBU2] [D-] [D+] [CC2] [VBUS] [TX2-] [TX2+] [GND]

TITLE: RK3399 SOM Board Design V1.0											
DOCUMENT NAME: [13]-TYPEC0_PROG.SchDoc											
PROJECT CODE: A_V1.0								Sheet 15 Of 21			
SCALE 1:1						TEMPLATE: A3	AUTHOR: ABC				
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TITLE: RK3399 SOM Board Design V1.0		
DOCUMENT NAME: [14]-RGMII_PHY.SchDoc		
PROJECT CODE: A_V1.0		Sheet 16 Of 21
SCALE 1:1	TEMPLATE: A3	AUTHOR: ABC
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ESTEEM PCB

A

A

B

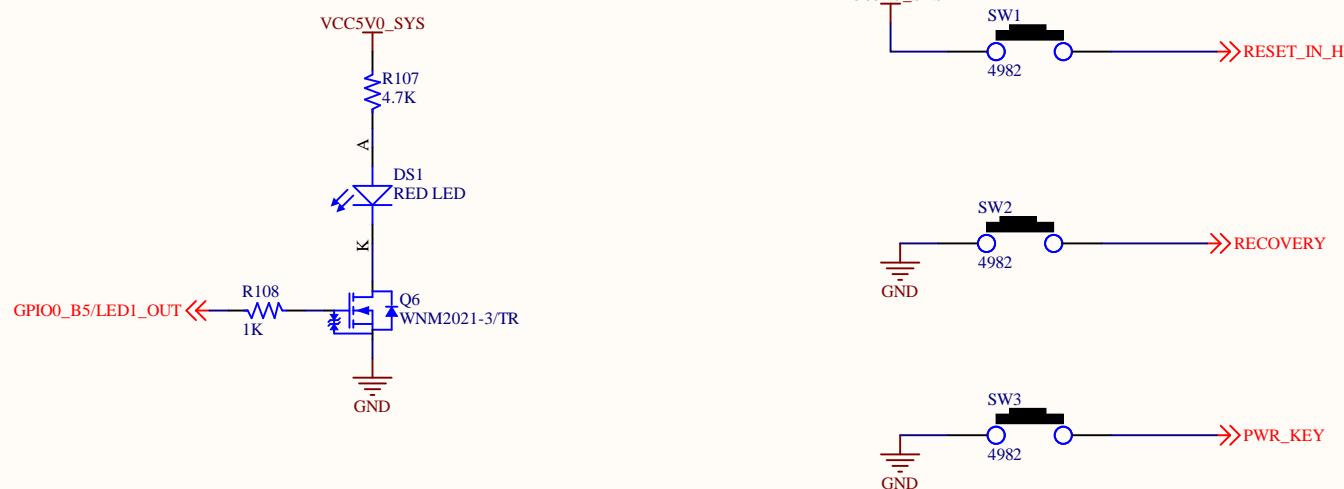
B

C

C

D

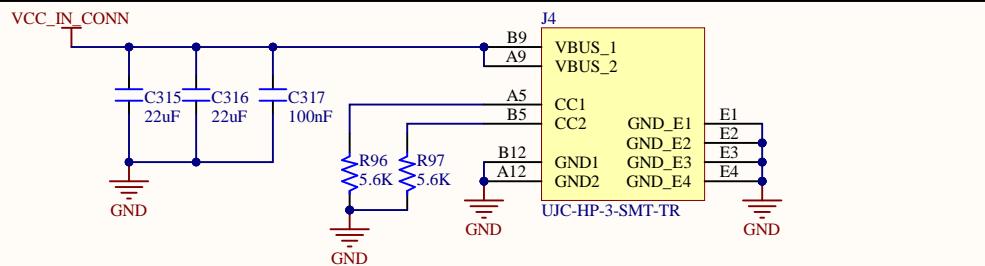
D



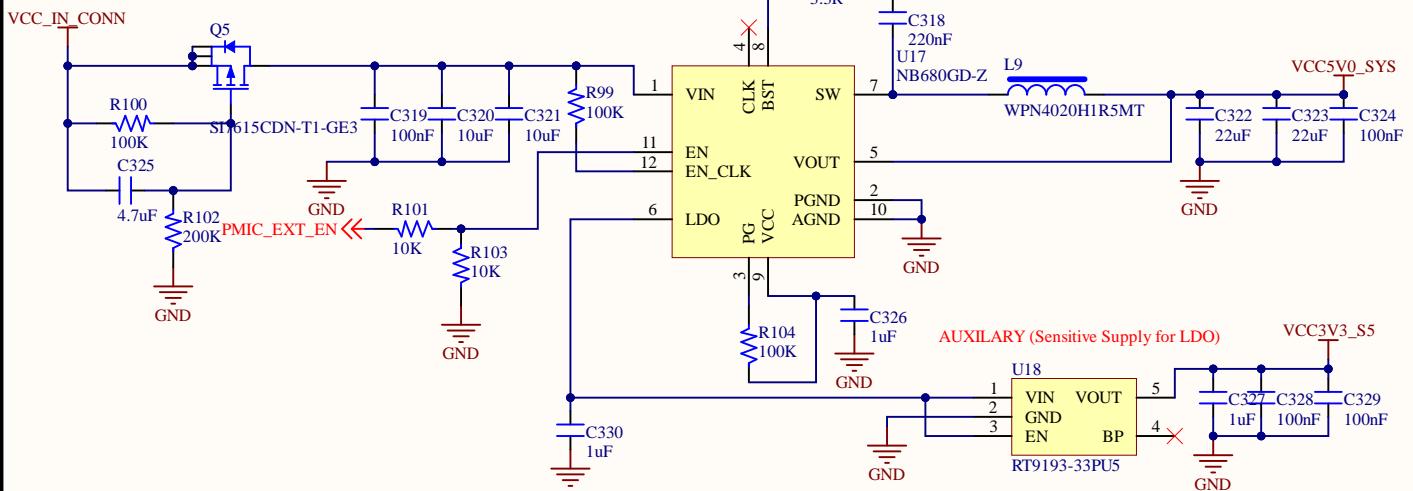
TITLE: RK3399 SOM Board Design V1.0		
DOCUMENT NAME: [15]-Push_Button_&_SYS_LED.SchDoc		
PROJECT CODE:	A_V1.0	Sheet 17 of 21
SCALE 1:1	TEMPLATE: A4	AUTHOR: AVIRAL MISHRA
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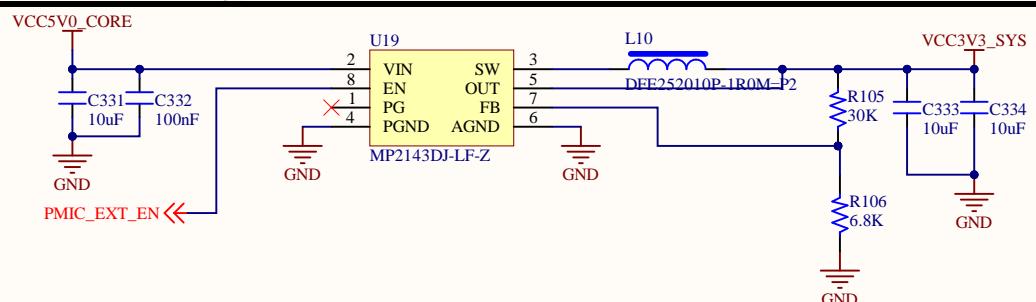
I/P +12V Regulators



+5V Regulators

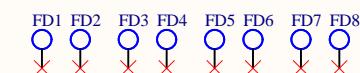


+3V3 Regulators



Regulators for CPU

Fiducials



TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [16]-SP1_TYPEC_PWR_IN.SchDoc

PROJECT CODE: A_V1.0

Sheet 18 of 21

SCALE 1:1 TEMPLATE: A4 AUTHOR: AVIRAL MISHRA

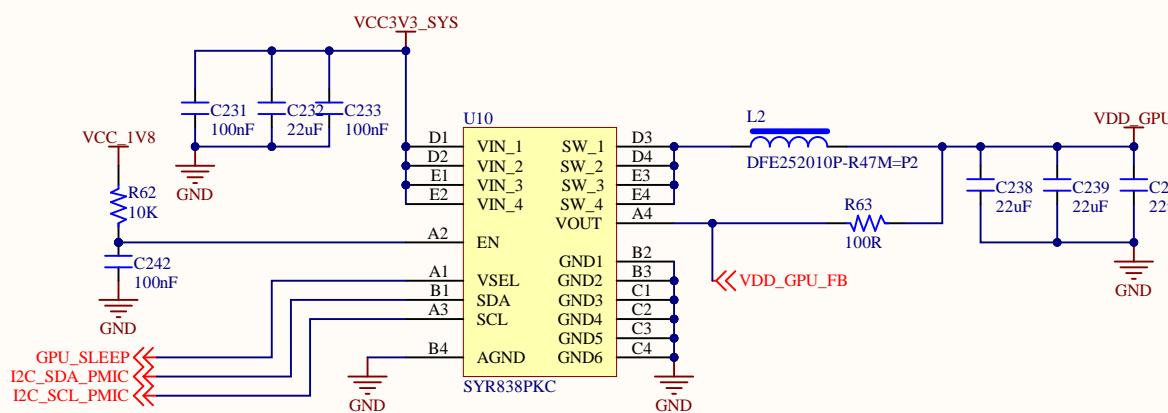
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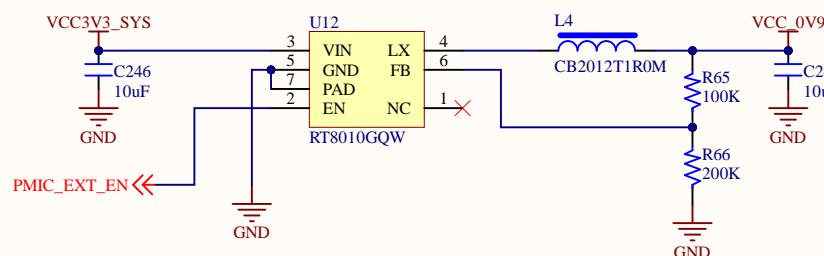
ESTEEM PCB

POWER REGULATORS FOR CPU & GPU

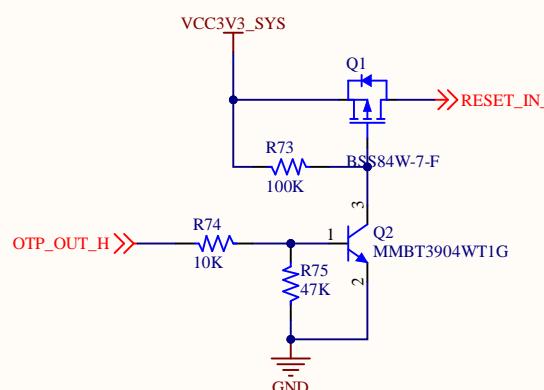
Regulators for GPU



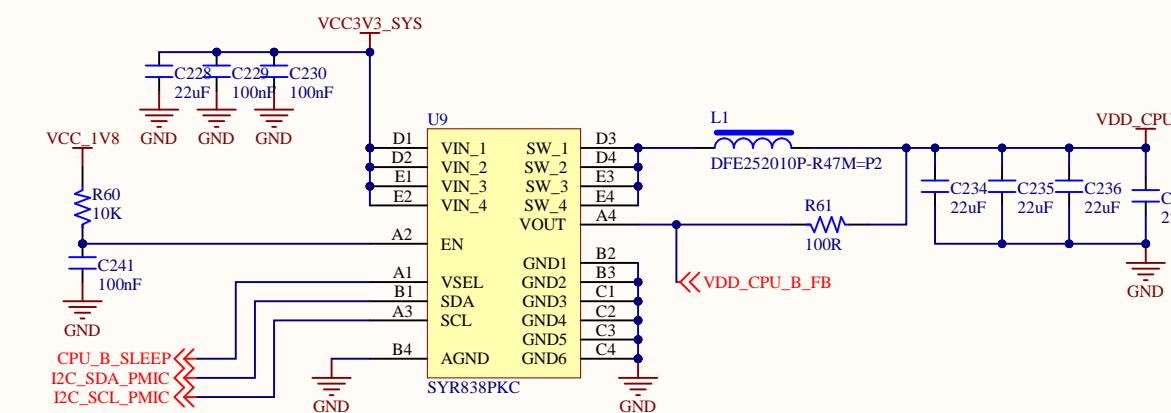
Regulator 0.9V



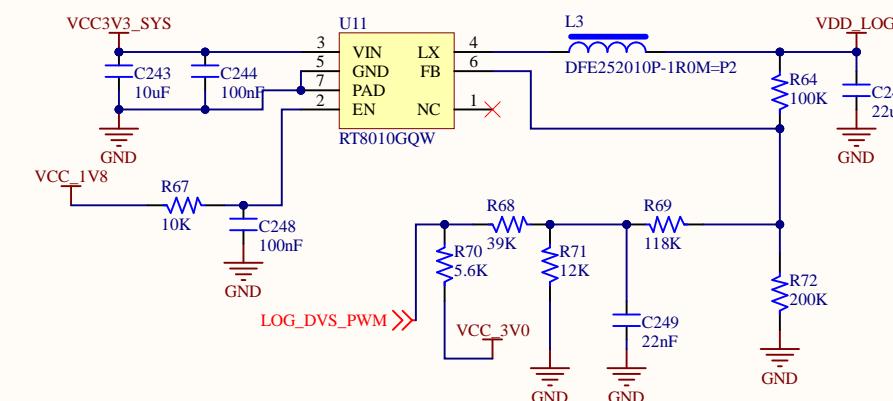
OVER TEMPERATURE PROTECTION



Regulators for CPU



Regulators for CPU Logic



TITLE: RK3399 SOM Board Design V1.0

DOCUMENT NAME: [17]-SP2_BUCKs_GPU_LOG_CPU-B.SchDoc

PROJECT CODE: A_V1.0 Sheet 19 Of 21

SCALE 1:1 TEMPLATE: A3 AUTHOR: ABC

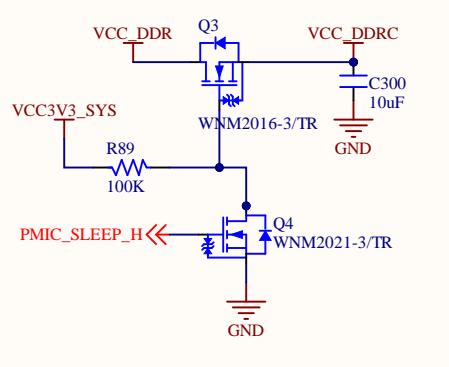
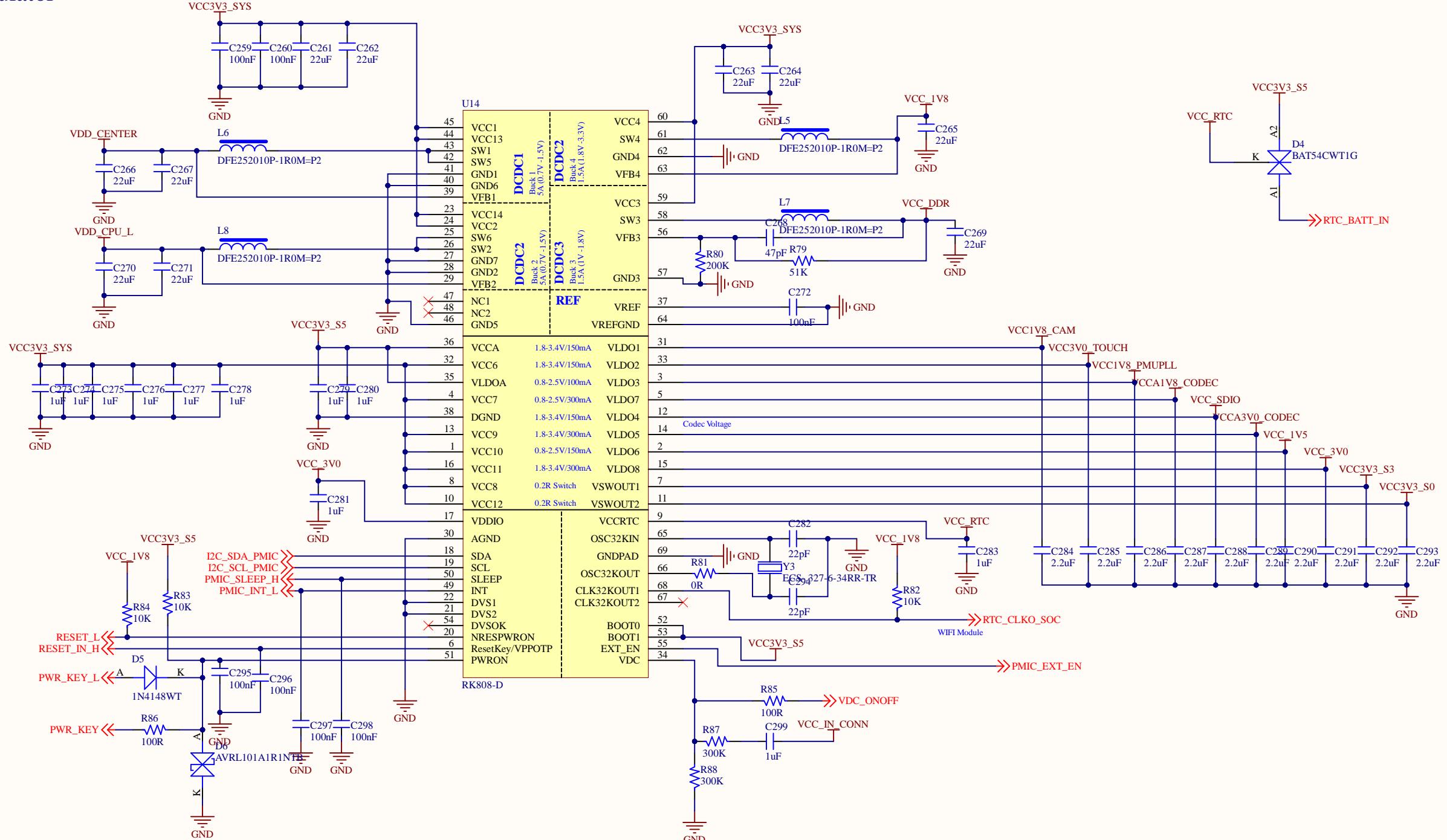
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ESTEEM PCB

PMIC RK808-D

PMIC RK808-D Regulator



TITLE: RK3399 SOM Board Design V1.0	
DOCUMENT NAME: [18]-SP3_PMIC_BLOCK.SchDoc	
PROJECT CODE:	A_V1.0
SCALE 1:1	TEMPLATE: A3 AUTHOR: ABC

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ESTEEM PCB

DIMM 260 DDR4 CONNECTOR

+12V Supply I/P

