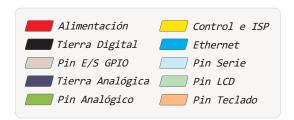


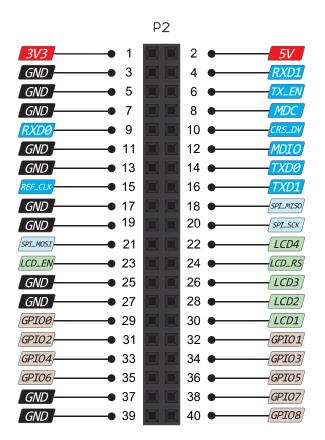
ASIGNACIÓN DE PINES



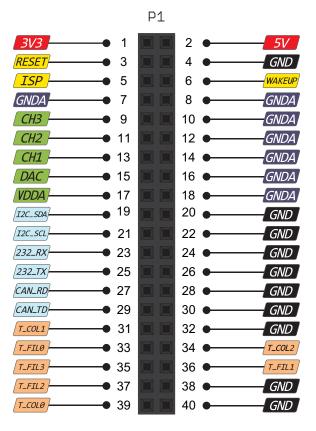








USB OTG USB DEBUG USB PARA DEBUG JP2 8.0 JP3 PWR JUMPERS RS485 JP1 BOTÓN DE RESET 1 144 RESET P2 P1 108 JP5 JUMPER DE MODO ISP LED R LED G LED B LED 1 LED 2 LED 3 CIMTEC 2 TEC 3 TEC 4



Tira de 40 pines hembra de 0.1"(2,54 mm) de espaciado

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PINES UTILIZADOS DEL NXP LPC4337 JBD144



