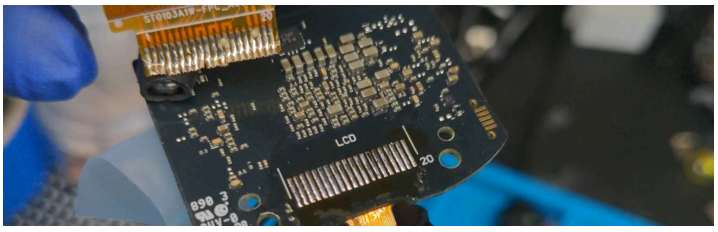
TASK URGENTLY:

- UPDATE SCH
- REMOVE OLD REFERENCE WE DON'T USE
- ADJUST PROJECT
- PRESET FILE VIEW 3D AND 2D (FOR IMPORT INSIDE VIEW CONFIGURATION PANEL)
- ADD DATASHEET TO EACH COMPONENT.
- Clean up everything that sucks. Fix the connections, there must be nothing negative that makes people think that the project looks like it was done by a junior or incompetent would be enough. **Make it professional.**

Task #1: Silkscreen

1. **Move Text Below JLPCB's Minimum 1mm to Another Layer:**
   - Remove any text from the silkscreen that is smaller than 1mm (as required by JLPCB) **without deleting it**.
   - Move this text to a **Document Layer** or **Notes Layer** (or create a new one) to keep the information without affecting the silkscreen.
2. **Use Comments for Additional Information:**
   - Add comments or annotations to a **Mechanical Layer** or **Layer** (e.g., "Notes Layer") to include any necessary details about the components or assembly that should be visible but not part of the silkscreen.
3. **Generate Complete Silkscreen in PDF:**
   - Export the **complete silkscreen** (including both **Top Overlay** and **Bottom Overlay**) as a PDF with all the component labels and markings.
4. **Generate PDF with Only Labels Not Included in Gerber:**
   - Create a **PDF** showing only the **labels** (component designators) that are present in the silkscreen but **not included in the Gerber files**.
   - Ensure this PDF includes labels for both **Top and Bottom** layers.

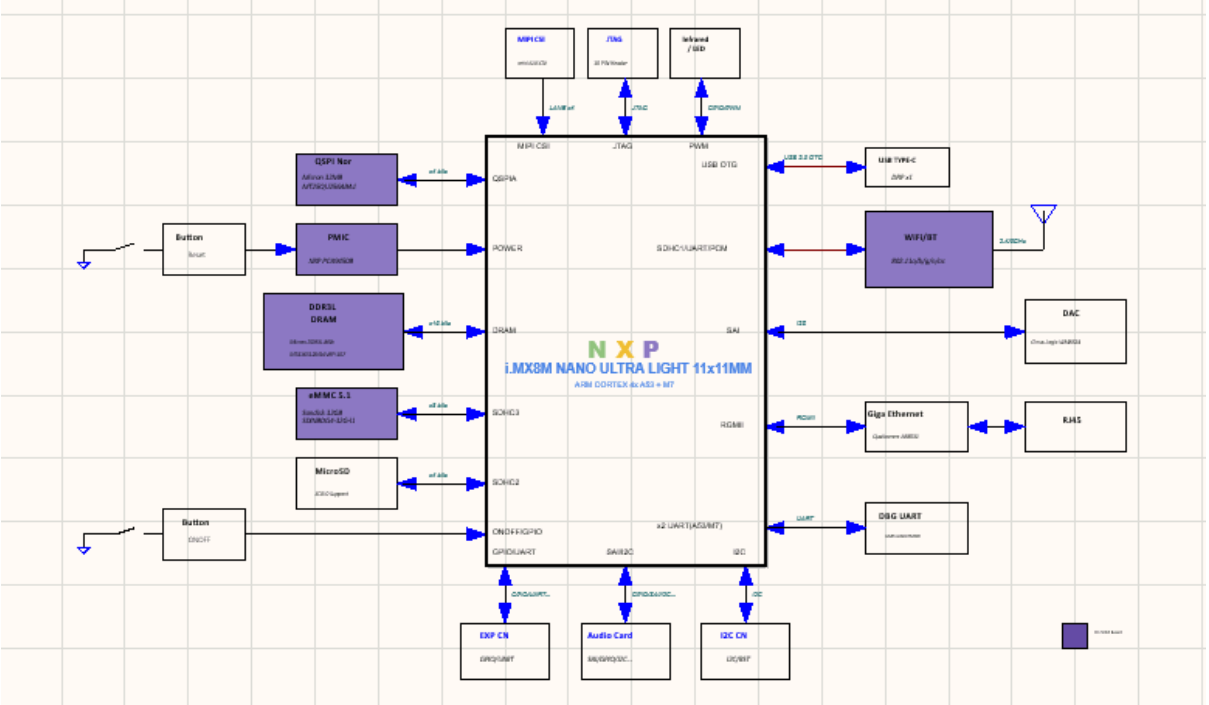
Recap this task: there must be no texts and things that jlpcb can't print, so move to a non-gerber layer to keep the silkscreen documented without texts



"Ref Bottom"

So it should look like this, the texts that can be printed are few like "LCD" and other few big ones at minimum, everything else is removed and moved to a documented layer, in the layer notes for factory production and in the pdf.

Task 2: # SCH Page02 Block Diagram
It should be solved by urgent tasks, update this. make our block diagram based on “Nova34” components hardware.



Task #3: SCH Page CPU MIMX8MN5DVPIZAA Missing!
The CPU page is missing, it should be the most important one and it should be page n3.
I don't think there are any more pages missing, there should be 14 or 15 if the speaker pads are also missing.

Task #4: Fix Display LCD Footprint and Pads
Separate from 3D body, remove circles and unnecessary stuff near, leave display body only in 3D view and must not be joined as footprint at all, display pads must be separated with near LCD as label, like in "Ref Bottom" picture

Task #5: USB Pads
Download firmware mode with usb pads

Task #6: Verify Wifi and Bluetooth

- **The selected WiFi/Bluetooth module is RTL8723DS-CG.**
- **Confirm that the module reference on the PCB matches the official datasheet.**
- **Ensure the correct wiring for communication interfaces (UART, SPI, or SDIO).**
- **Check that the RF trace impedance is properly designed.**
- **Ensure the antenna is correctly positioned to minimize interference.**
- **Verify proper grounding and isolation from other signal traces.**
- **Verify the power supply and filtering for the module.**
- **Check the footprint and pad connections for RTL8723DS-CG.**

- **Confirm that JLCPCB supports the selected components with no replacements needed.**

Task #7: Camera and Display (View 3D)
adjust display body3d in front side and with flex connector and same for camera (camera is in back side soldered but is flexed on frontside, see images reference). Missing camera 3d body. the display component placed at the bottom is a bit annoying, make sure it is flexed towards the front with camera

Task #8: Speaker pads
(Original: 20mm 8ohm 0.5 W speaker). Pads is already present in pcb but i want inside sch and speaker component selected **optionally** in bom (because speaker is externally).

Task #9: Check/Updated Schematic
Improve the cpu symbol on sch and above all carefully check that the selected cpu is documented in the electrical diagram and do a check on the official datasheet is consistent with the project in general on everything.

Task #10: Drc/Erc Rules
0 errors drc/erc + report pdf

Task #11: JLCDFM Analysis
Report pdf

Task #1: Production data
- **Screenshots of jlpcb simulated order Complete + stencyl.**
- Fabrication drawing.
- Assembly drawing.
- Bill of materials (BOM)
- IPC netlist.
- Gerber files.

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  only the components we choose and jlpcb owns them, do not replace them
- NC drill files.
- Pick and place files.
- ODB++