

# Nova34

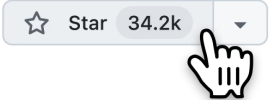
(Nova34 - Reference Board)


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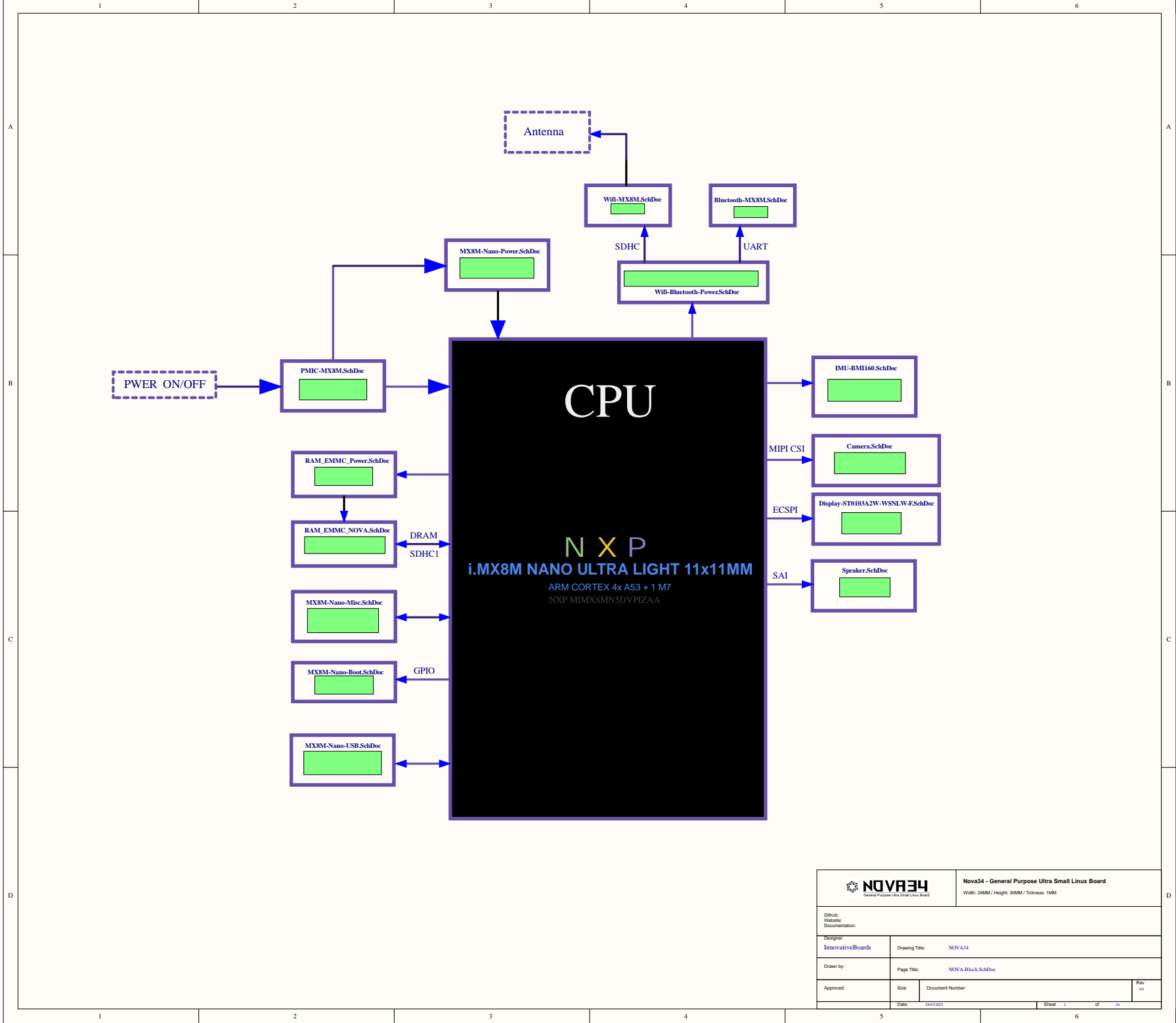
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## Revision History

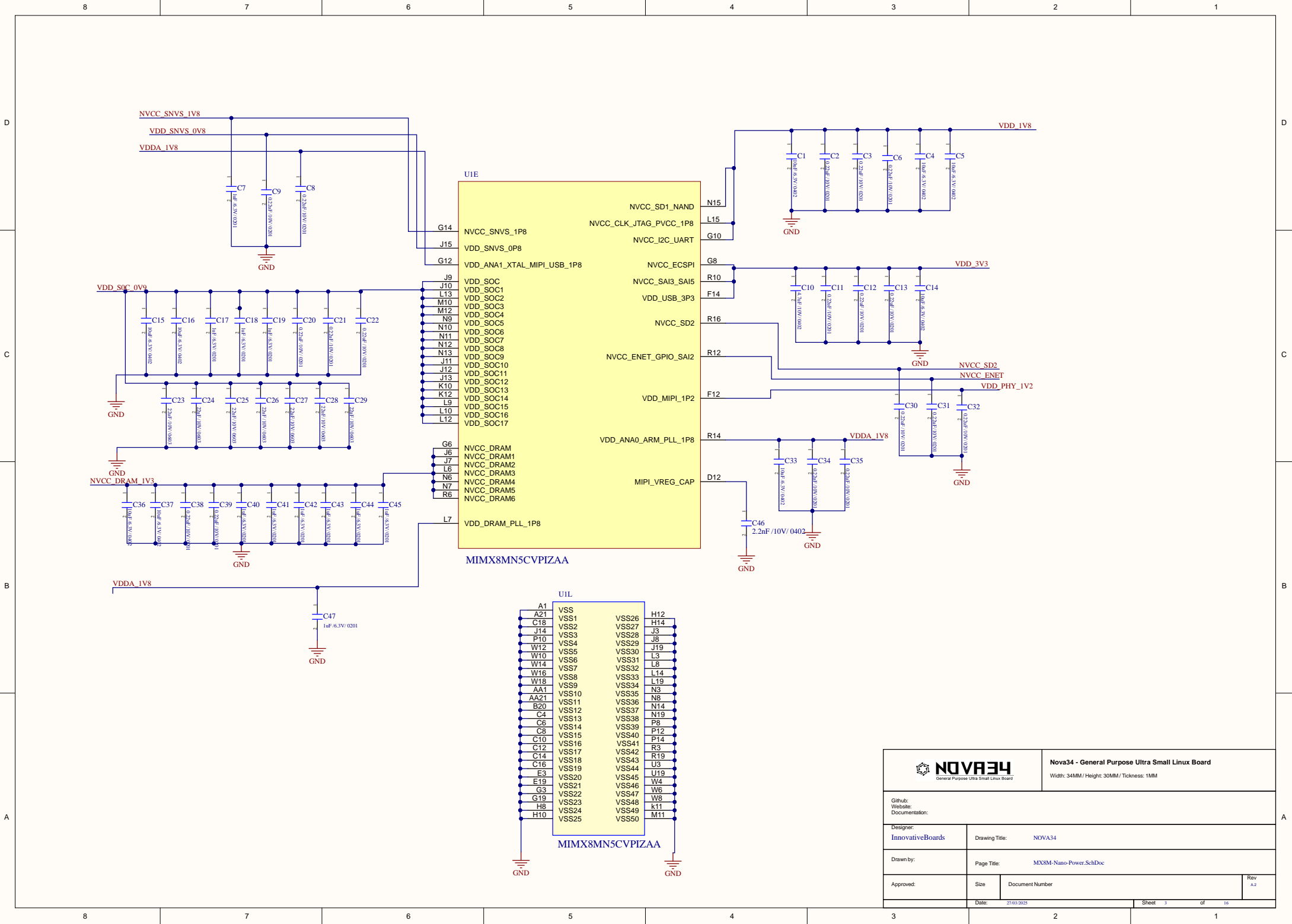
Rev. Code	Date	By	Description
A	2022-01-01		Initial version release
A.1.4	2025-28-03		Schematic completed




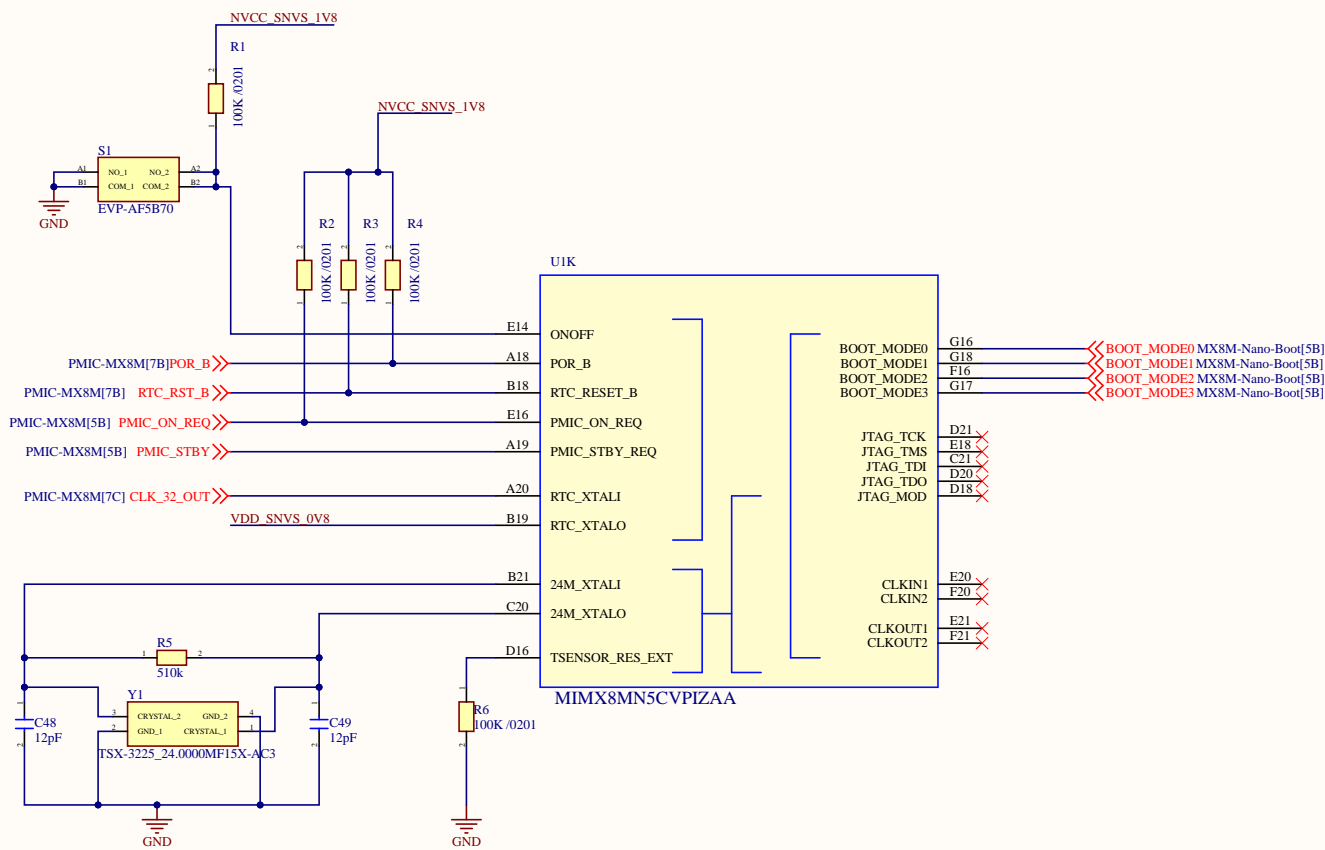
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Github: Website: Documentation:			
Designer: <b>InnovativeBoards</b>		Drawing Title: <b>NOVA34</b>	
Drawn by:		Page Title: <b>Cover-Page.S4Dxc</b>	
Approved:	Size	Document Number	Rev: <b>A.2</b>
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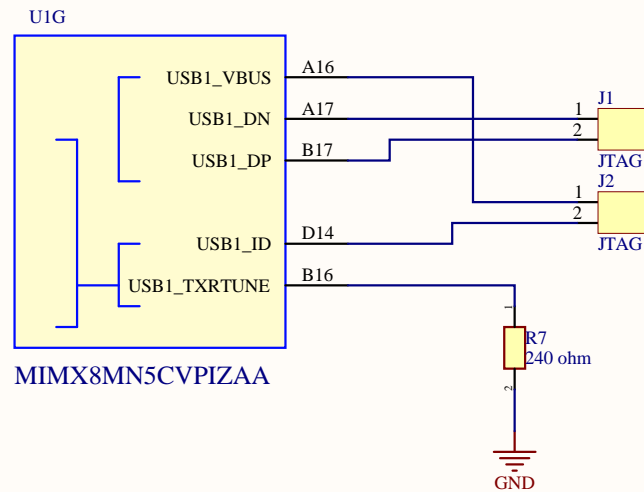
 <b>NOVA34</b> <small>General Purpose Ultra Small Linux Board</small>		<b>Nova34 - General Purpose Ultra Small Linux Board</b>  <small>Width: 34MM / Height: 30MM / Thickness: 1MM</small>	
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 General Purpose Ultra Small Linux Board		<b>Nova34 - General Purpose Ultra Small Linux Board</b> Width: 34MM / Height: 30MM / Thickness: 1MM	
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Designer: <b>InnovativeBoards</b>		Drawing Title: NOVA34	
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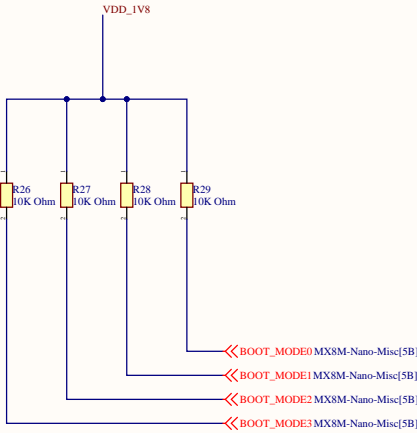
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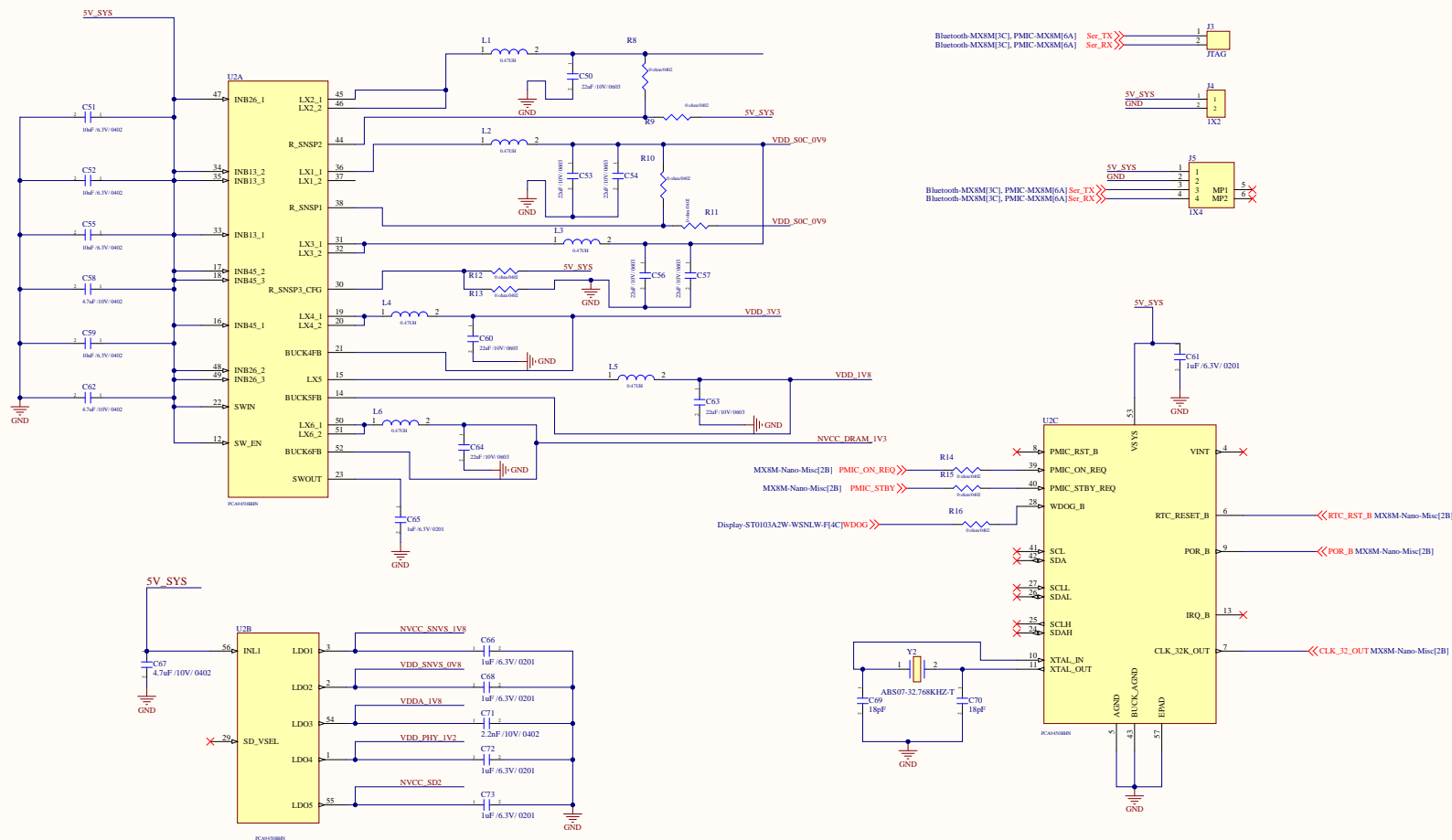


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i.MX8M Nano Boot Mode

BOOT_PIN[3]	BOOT_PIN[2]	BOOT_PIN[1]	BOOT_PIN[0]	Boot Modes
BOOT_MODE3 (TEST_MODE)	BOOT_MODE2 (JTAG_TRST_B)	BOOT_MODE1	BOOT_MODE0	Function
0	0	0	0	Boot From Internal Fuses
0	0	0	1	USB Serial Download
0	0	1	0	USDHC3 (eMMC boot only, SD3 8-bit)
0	0	1	1	USDHC2 (SD boot only, SD2)
0	1	0	0	NAND 8-bit single device 256 page
0	1	0	1	NAND 8-bit single device 512 page
0	1	1	0	QSPI 3B Read
0	1	1	1	QSPI Hyperflash 3.3V
1	0	0	0	ecSPI Boot
1	0	0	1	Reserved
1	0	1	0	Reserved
1	0	1	1	Reserved
1	1	0	0	Reserved (Boot on I2C connected to BOOT PIN[3:2])
1	1	0	1	Reserved
1	1	1	0	Infinite Loop Mode
1	1	1	1	Test Mode





		<b>Nova34</b> General Purpose Ultra Small Linux Board	
GitHub: Website: Documentation:		Nova34 - General Purpose Ultra Small Linux Board Width: 34mm / Height: 20mm / Thickness: 1mm	
Diagram: <b>InnovativeBoards</b>		Drawing Title: NOVA34	
Drawn by:		Page Title: PMIC-MXRM SoD	
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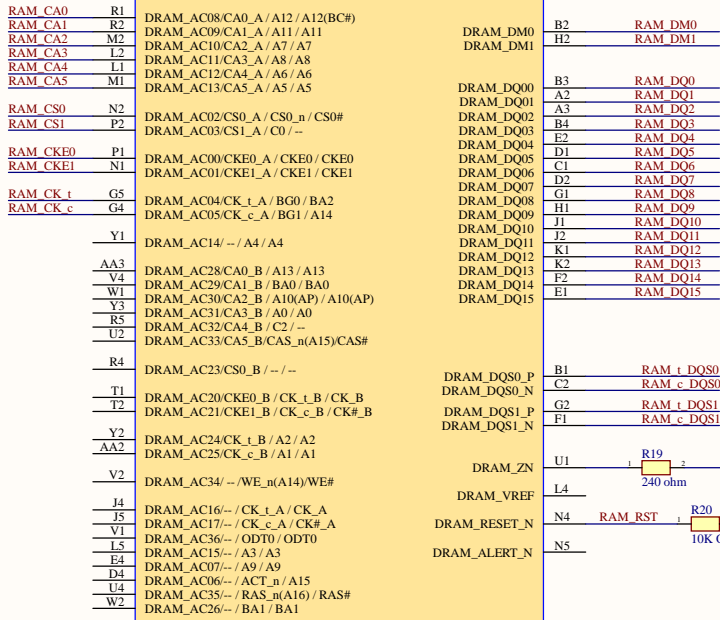
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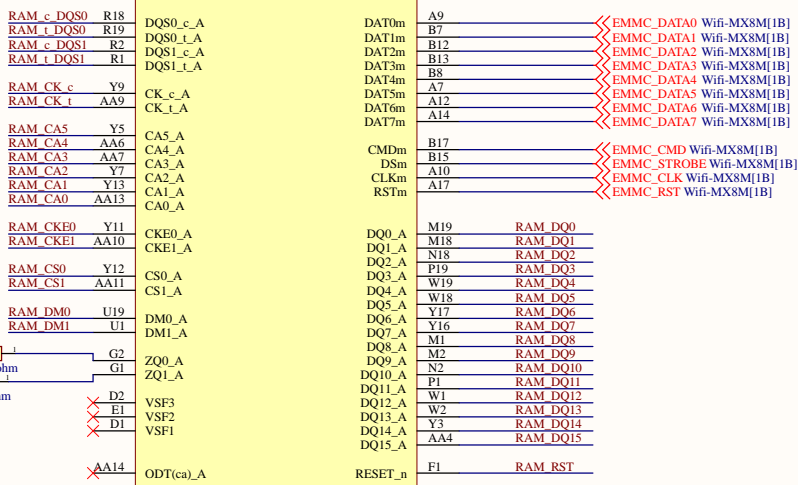
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U1A




MIMX8M5CVP1ZAA

U3A



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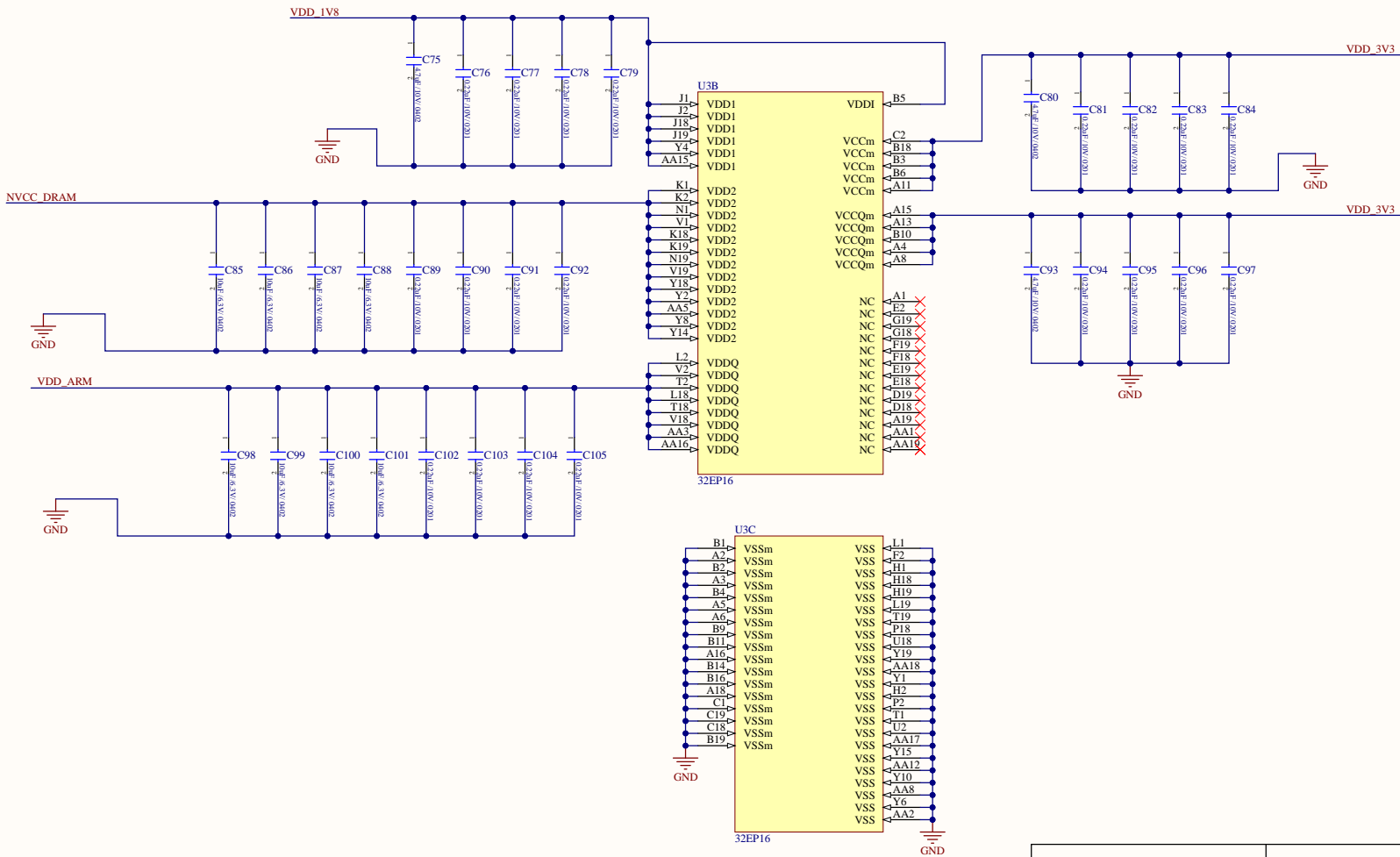
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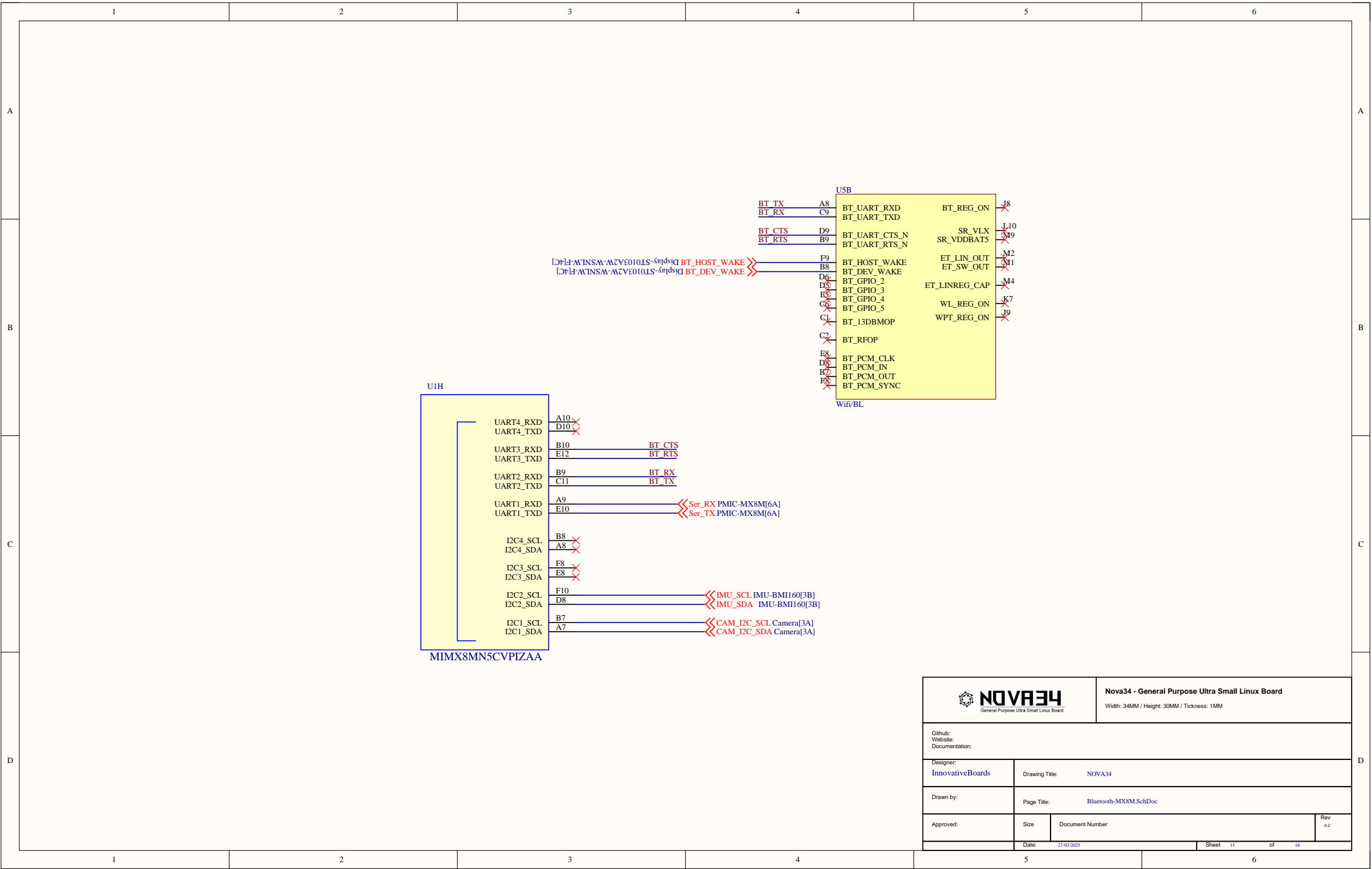
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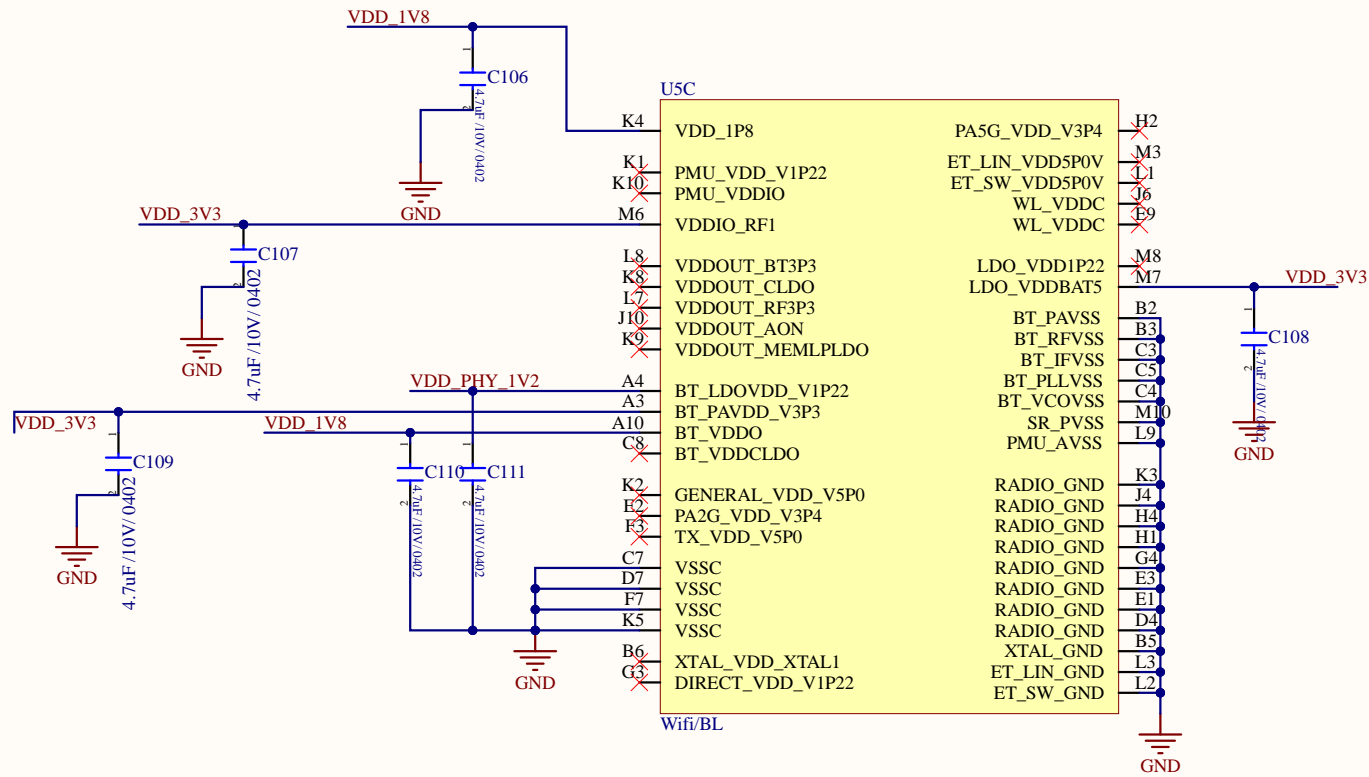
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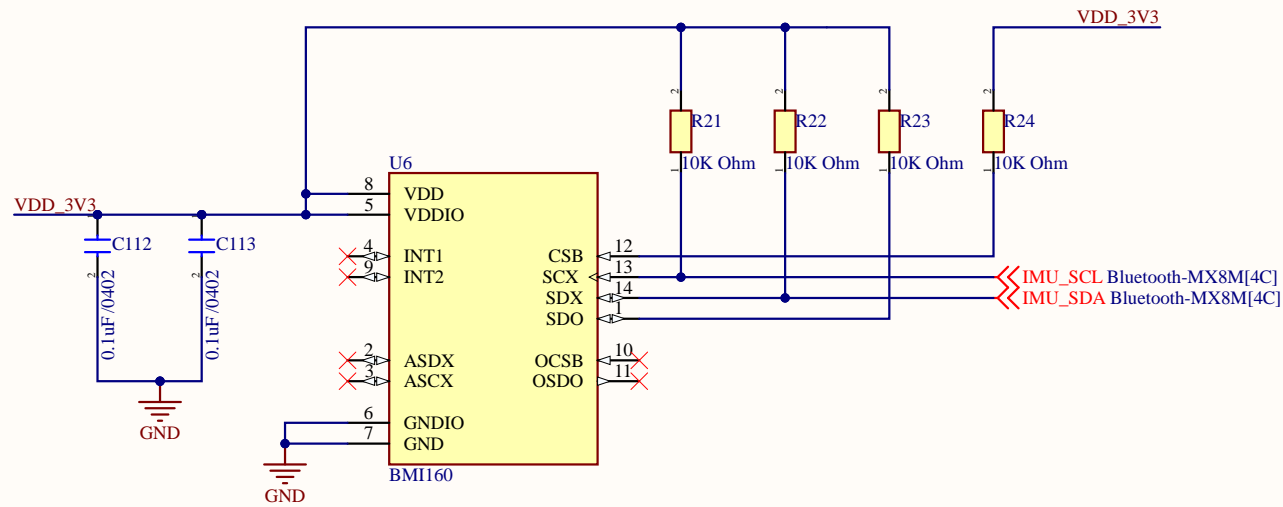
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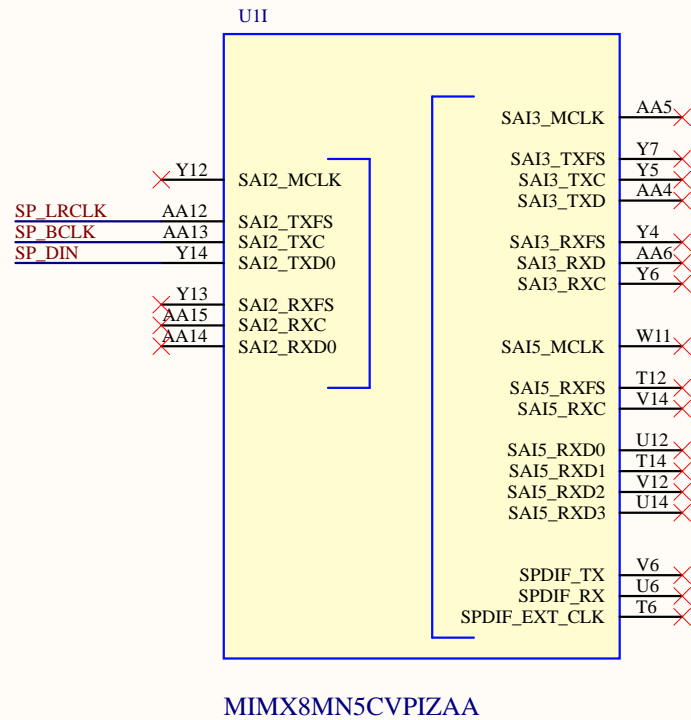
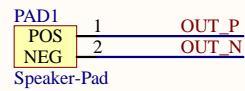
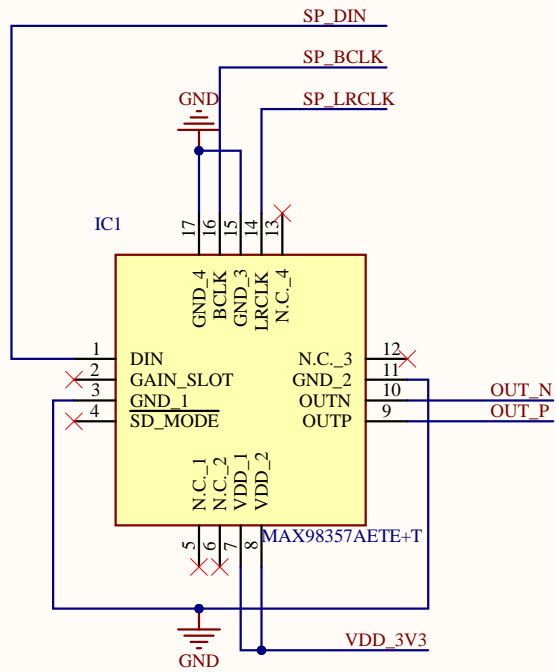
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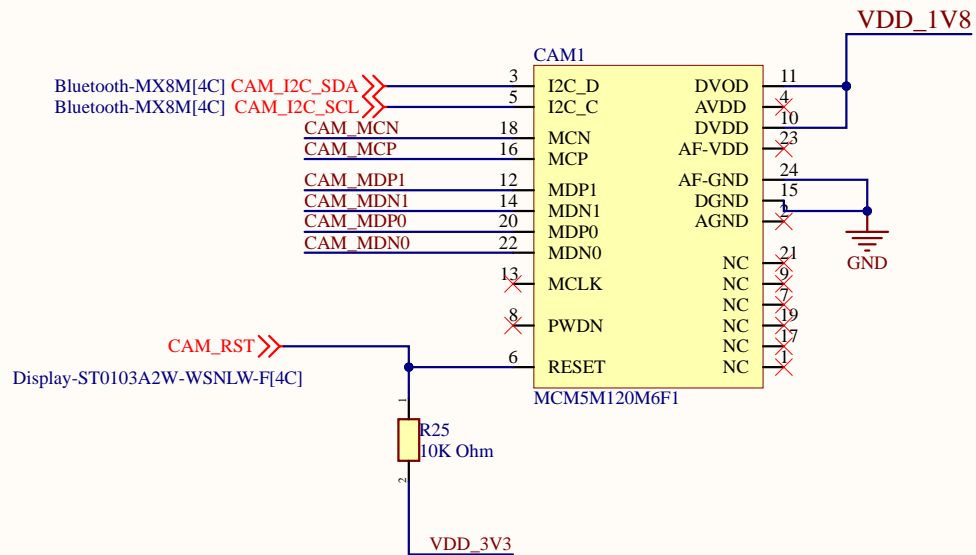
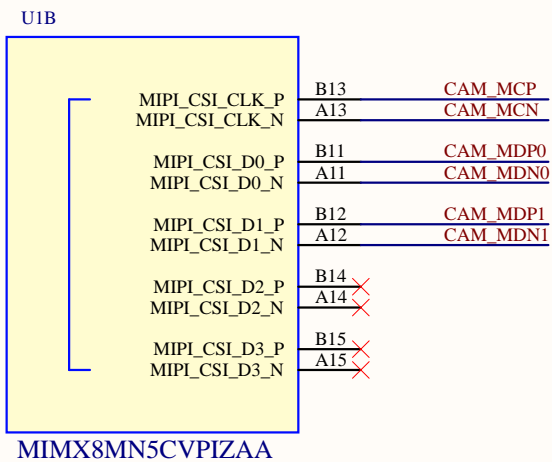
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