DFM analysis report_JLCDFM

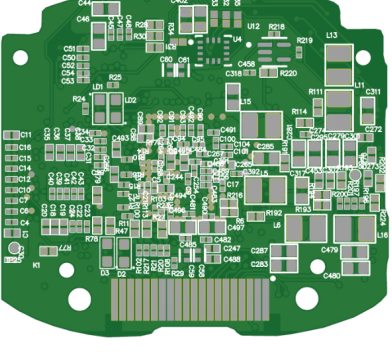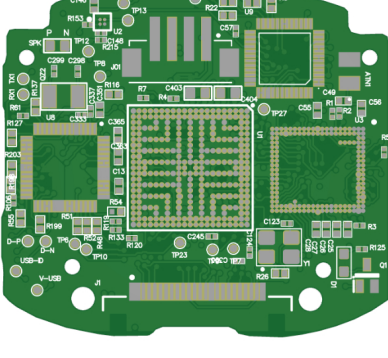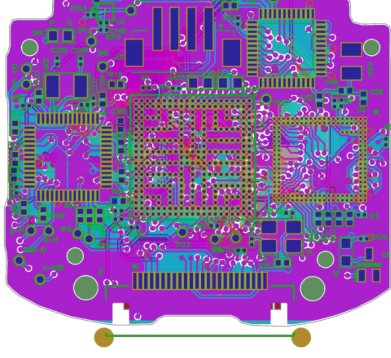|	
File name:	Gerber.zip
Report generated at:	2025-03-04 11:01:20
PCB layers:	8
PCB size:	3.41x3.11cm
Analyze project:	☑ PCB DFM ☐ SMT DFM

PCB DFM>Routing layer analysis

Analyze project	Analysis results	PCB screenshot	Layer distribution	Statistics
Sharp trace corner (Check for sharp corners in traces)	No	No	No	Danger：0 Warning：0 Good：0
BGA pad (Check BGA pads on the board)	null Warning		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：0 Warning：50 Good：0
Via placed within a pad (Check if there is a via placed within a pad)	No	No	No	Danger：0 Warning：0 Good：0
Trace to board edge (Detect traces too close to the board edge)	No	No	No	Danger：0 Warning：0 Good：0
Trace spacing (Measure spacing between adjacent parallel traces)	0.1mm Danger		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Bot.gbr Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：2 Warning：20 Good：12
Unconnected trace end (Free-standing trace ends not connected to pads)	null Warning		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：0 Warning：2 Good：0
Trace width (Trace width information)	0.15mm Good		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Bot.gbr Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：0 Warning：0 Good：100
Fiducial (Detect fiducial marks on the board)	null Warning		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Bot.gbr Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：0 Warning：4 Good：0
Pad to board edge (Measure distance of pads from the board edge)	0.09mm Danger		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Bot.gbr Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：2 Warning：4 Good：22
Pad spacing (Measure pad to pad spacing)	0mm Danger		Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Bot.gbr Gerber\Anki_vector_headboard_ultra_2_Copper_Signal_Top.gbr	Danger：67 Warning：33 Good：0
Plated through-hole to trace clearance (Measure clearance of plated through-holes to traces)	No	No	No	Danger：0 Warning：0 Good：0
Annular ring (Annular ring width of pads compared to holes)	No	No	No	Danger：0 Warning：0 Good：0
tht to smd (Detect clearance of vias to pads)	No	No	No	Danger：0 Warning：0 Good：0
Via to pad (Detect clearance of vias to pads)	No	No	No	Danger：0 Warning：0 Good：0

PCB DFM>Soldermask layer analysis

Analyze project	Analysis results	PCB screenshot	Layer distribution	Statistics
Soldermask bridge (Detect distance between parallel soldermask opening edges)	0.01mm Danger		Gerber\Anki_vector_headboard_ultra_2_Soldermask_Top.gbr Gerber\Anki_vector_headboard_ultra_2_Soldermask_Bot.gbr	Danger：69 Warning：31 Good：0
Solder mask opening exposing trace (Detect clearance of solder mask openings to nearby traces)	0.01mm Danger		Gerber\Anki_vector_headboard_ultra_2_Soldermask_Top.gbr Gerber\Anki_vector_headboard_ultra_2_Soldermask_Bot.gbr	Danger：100 Warning：0 Good：0
Soldermask opening with multiple segments (Check if solder mask openings are constructed from multiple geometric shapes)	null Warning		Gerber\Anki_vector_headboard_ultra_2_Soldermask_Top.gbr Gerber\Anki_vector_headboard_ultra_2_Soldermask_Bot.gbr	Danger：0 Warning：33 Good：0
Negative soldermask expansion (Detect solder mask openings smaller than their corresponding pads)	0.02mm Warning		Gerber\Anki_vector_headboard_ultra_2_Soldermask_Top.gbr	Danger：0 Warning：1 Good：0

PCB DFM>Silkscreen layer analysis

Analyze project	Analysis results	PCB screenshot	Layer distribution	Statistics
Silkscreen to pad (Detect clearance of silkscreen to pads)	0mm Danger		Gerber\Anki_vector_headboard_ultra_2_Legend_Top.gbr Gerber\Anki_vector_headboard_ultra_2_Legend_Bot.gbr	Danger：100 Warning：0 Good：0
Silkscreen to hole (Detect clearance of silkscreen to holes)	No	No	No	Danger：0 Warning：0 Good：0
Silkscreen line width (Check silkscreen line width)	0.04mm Danger		Gerber\Anki_vector_headboard_ultra_2_Legend_Top.gbr Gerber\Anki_vector_headboard_ultra_2_Legend_Bot.gbr	Danger：100 Warning：0 Good：0

PCB DFM>Drill layer analysis

Analyze project	Analysis results	PCB screenshot	Layer distribution	Statistics
Missing plated through-hole (Detect top and bottom pads at the same location without plated through-holes)	Unanalyzed	No	No	No
Plated through-hole spacing (Measure spacing between plated through-holes)	Unanalyzed	No	No	No
Short slot detection (Detect slots shorter than twice their width)	Unanalyzed	No	No	No
Slot width check (Measure slot width)	Unanalyzed	No	No	No
Via to PTH spacing (Measure spacing of vias to plated through-holes)	Unanalyzed	No	No	No
Unconnected via (Detect isolated unconnected vias)	Unanalyzed	No	No	No