Sima Jeddi

Education

2015 | 2017 M.Sc. in Electrical Engineering - Digital Electronic Circuits, Amirkabir University of Technology, Tehran, Iran.

GPA: 3.72/4.

Thesis: Network traffic prediction for resource allocation in network function virtualization.

Advisor: Dr. Saeed Sharifian.

2011 | 2015 **B.Sc.** in Electrical Engineering - Electronics, Amirkabir University of Technology, Tehran, Iran.

GPA: 3.89/4.

Thesis: Design and implementation of a baby sitter using motion and voice sensors.

Advisor: Dr. Saeed Sharifian.

Publications

S. Jeddi, S. Sharifian, "A hybrid wavelet decomposer and GMDH-ELM ensemble model for Network function virtualization workload forecasting in cloud computing".

Journal of Applied Soft Computing - Elsevier, December 2019, DOI: 10.1016/j.asoc.2019.105940

S. Jeddi, S. Sharifian, "A Water Cycle Optimized Wavelet Neural Network Algorithm for Demand Prediction in Cloud Computing".

Journal of Cluster Computing - Springer, Feb 2019, DOI: 10.1007/s10586-019-02916-2

Honors & Awards

Ranked Three, class of 2017, Electrical Engineering Department, Amirkabir University of Technology, Tehran.

Recipient of the Grant for Master's Thesis, Iran's National Elites Foundation (INEF) for outstanding academic success, Iran, 2015-2017.

Recipient of the Grant for Graduate Studies, Iran's National Elites Foundation (INEF) for outstanding academic success, Iran, 2015-2017.

Exceptional Talent Student Award, Exempted From the Nationwide Graduate Entrance Examinations for graduate studies at Amirkabir University of Technology, Tehran, Iran, 2015.

Ranked within the top 5% of my graduating class, class of 2015, Electrical Engineering Department, Amirkabir University of Technology, Tehran.

Recipient of the grant for undergraduate studies, from Iran's National Elites Foundation (INEF) for outstanding academic success, Iran, 2011-2015.

Ranked within the top 10%, Iran's Universities Entrance Exam for B.Sc. Degree, 2011.

Bronze Medal, National Olympiad in Physics, Tehran, Iran, 2010.

Teaching Experience

Logic Circuits course, Head Teaching Assistant, Department of Electrical and Computer Engineering, University of Tabriz, Spring 2018.

Microprocessor Systems and Interface Course, Head Teaching Assistant, Department of Electrical Engineering, Amirkabir University of Technology, Spring 2016.

Skills

Programming Python, R, C/C++, Java, VHDL/Verilog, HTML/CSS

Languages

Tools Jupyter Notebook, Docker, MATLAB

Languages Persian (Native) English, Turkish (Fluent), French

Test Scores TOEFL iBT: 102/120

Reading(28/30), Listening(25/30), Speaking(23/30), Writing(26/30)

GRE General: 319/340

Verbal Reasoning(149/170), Quantitative Reasoning(170/170)

Analytical Writing (3.5/6)

Courses

Some Relevant Coursework.

• Stochastic Optimization: A.

• Statistical Pattern Recognition: A.

• Statistical Learning Theory: A.

• Bio Inspired Artificial Intelligence: A.

• Numerical Analysis: A.

Online courses.

Jul 2018 • Divide and Conquer, Sorting and Searching, and Randomized Algorithms. Stanford University, Coursera

Aug 2018 • Graph Search, Shortest Paths, and Data Structures.

Stanford University, Coursera

Sep 2018 • Greedy Algorithms, Minimum Spanning Trees, and Dynamic Programming. Stanford University, Coursera