# NATIONAL DIPLOMA: ENGINEERING: COMPUTER SYSTEMS Qualification code: NDCY03 - NQF Level 6

Campus where offered: Soshanguve South Campus (day classes offered during the week and

on Saturdays)

Last year of new intake: 2019

Teach-out (phase-out) date: 31 December 2023

Students registered for this qualification should complete their studies according to the teach-out date prescribed for the qualification, subject to the stipulations of Regulation 3.1.11 and 3.1.13 in the Students' Rules and Regulations.

Information on phased-out programmes can be obtained from the TUT website, www.tut.ac.za.

#### **CURRICULUM**

Consult the 2019 Faculty Prospectus for the full contents of the qualification.

#### **FIRST YEAR**

CODE	SUBJECT	CREDIT	PREREQUISITE SUBJECT(S)			
FIRST SEMESTER						
COS101T CSK101T DSY131C EEN111C ELC111B MAT141F PGG111T	Communication Skills I Computer Skills I Digital Systems I Electrical Engineering I Electronics I Mathematics I Programming I	(0,036) (0,055) (0,083) (0,083) (0,083) (0,083) (0,083)				
TOTAL CREDITS FOR THE SEMESTER:		0,506				
SECOND SEMESTER						
DSY231C ELC211B MAT251F NSY211T PGG211T PJT101B	Digital Systems II Electronics II Mathematics II Network Systems II Programming II Projects I	(0,083) (0,083) (0,083) (0,083) (0,083)	Digital Systems I Electronics I Mathematics I Programming I Programming I			
TOTAL CREDITS FOR THE SEMESTER:		0,498				
TOTAL CREDITS FOR THE FIRST YEAR:		1,004				

#### **SECOND YEAR**

SUBJECT

CODE

FIRST SEMESTER							
DSY341C NSY311T OSY301T PGG311T SYA201T	Digital Systems III Network Systems III Operating Systems III Programming III Systems Analysis II	(0,083) (0,083) (0,083) (0,083) (0,083)	Digital Systems II Network Systems II Programming II Programming II				



PREREQUISITE SUBJECT(S)

CREDIT

# plus one of the following subjects:

DPC201T MMA301T ORS311T	Digital Process Control II Mathematical Applications III Operational Research III	(0,083) (0,083) (0,083)	Mathematics II Mathematics I			
TOTAL CRED	ITS FOR THE SEMESTER:	0,498				
SECOND SEM	MESTER					
DBR311T	Database Principles III	(0,083)	Programming II Systems Analysis II			
LOD311B SFE311T	Logic Design III Software Engineering III	(0,083) (0,083)	Digital Systems III Programming III Systems Analysis II			
	plus two of the following subjects that	were not take	en in the previous semesters:			
DPC201T DPC301T MMA301T ORS311T	Digital Process Control II Digital Process Control III Mathematical Applications III Operational Research III	(0,083) (0,083) (0,083) (0,083)	Digital Process Control II Mathematics II Mathematics I			
TOTAL CREDITS FOR THE SEMESTER:		0,415				
TOTAL CREDITS FOR THE SECOND YEAR:		0,913				
THIRD YEAR						
CODE	SUBJECT	CREDIT	PREREQUISITE SUBJECT(S)			
FIRST SEMES		CREDIT	PREREQUISITE SUBJECT(S)			
FIRST SEMES	STER	(0,500)	PREREQUISITE SUBJECT(S)			
FIRST SEMES On completio	STER n of all the above subjects.		PREREQUISITE SUBJECT(S)			
FIRST SEMES On completio	STER n of all the above subjects.  Work-Integrated Learning I ITS FOR THE SEMESTER:	(0,500)	PREREQUISITE SUBJECT(S)			
FIRST SEMES On completio EXP1ECS TOTAL CREDI	STER n of all the above subjects.  Work-Integrated Learning I ITS FOR THE SEMESTER:	(0,500)	PREREQUISITE SUBJECT(S)  Work-Integrated Learning I			
FIRST SEMES On completio  EXP1ECS  TOTAL CREDI  SECOND SEM  EXP2ECS PJD301B	STER n of all the above subjects.  Work-Integrated Learning I STS FOR THE SEMESTER:  MESTER  Work-Integrated Learning II	(0,500) 0,500 (0,500)				



1,083

3,000

TOTAL CREDITS FOR THE THIRD YEAR:

TOTAL CREDITS FOR THE QUALIFICATION:

## SUBJECT INFORMATION (OVERVIEW OF SYLLABUS)

The syllabus content is subject to change to accommodate industry changes. Please note that a more detailed syllabus is available at the Department or in the study guide that is applicable to a particular subject. At time of publication, the syllabus content was defined as follows:

С

## **COMMUNICATION SKILLS I (COS101T)**

#### CONTINUOUS ASSESSMENT

#### (Subject custodian: Department of Applied Languages)

Communication theory, non-verbal communication (body language, etc.), oral presentations, interviews, developing leadership and participation skills. Technical reports and correspondence. (Total tuition time:  $\pm 20$  hours)

#### **COMPUTER SKILLS I (CSK101T)**

#### CONTINUOUS ASSESSMENT

## (Subject custodian: End User Computing Unit)

Students have to acquire practical skills as end-users in operating systems and MS Office Suite applications (MS Word, MS Excel and MS PowerPoint) on an introductory level, as well as in MS Access Essentials. Students will do online and computer-based tests. The modules are mapped with SAQA. (Total tuition time: ± 40 hours)

D

## **DATABASE PRINCIPLES III (DBR311T)**

#### 1 X 4-HOUR COMPUTER-BASED

#### (Subject custodian: Department of Computer Science)

An introduction to databases and database management principles. Theoretical principles are applied in the query language SQL, using Oracle SQL. Students' insight and skills are tested in the development, design and implementation of a relational database. (Total tuition time: ± 60 hours)

## **DESIGN PROJECT III (PJD301B)**

# **CONTINUOUS ASSESSMENT**

## (Subject custodian: Department of Computer Systems Engineering)

The planning, design and implementation of an industry-related project by applying the knowledge obtained and the tools students were introduced to in the programme. The project should deal with an actual computer science problem and should include hardware and software elements. This subject is supported by short project management and entrepreneurship programmes. (Total tuition time: ± 10 hours)

#### DIGITAL PROCESS CONTROL II (DPC201T)

#### 1 X 3-HOUR PAPER

## (Subject custodian: Department of Computer Systems Engineering)

Introduction to basic digital control techniques using an emulated PLC. Various realistic and practical projects are studied using the printer port as PLC. (Total tuition time: ± 80 hours)

#### **DIGITAL PROCESS CONTROL III (DPC301T)**

# 1 X 3-HOUR PAPER

# (Subject custodian: Department of Computer Systems Engineering)

A detailed examination of the functional operations of a PLC, as used in factory automation. An introduction to robotics, electromechanical and sensory tactics and methods. (Total tuition time: ± 80 hours)

## **DIGITAL SYSTEMS I (DSY131C)**

## 1 X 3-HOUR PAPER

# (Subject custodian: Department of Computer Systems Engineering)

History and overview of digital systems, Number systems and codes, Binary arithmetic, Boolean algebra, Basic logic gates (AND, OR, NOT, NAND, NOR, XOR), Physical properties of logic gates (technology, fan-in, fan out, propagation delay), Networks of logic gates, 2-level networks (AND-OR, OR-AND, NAND-NAND, NOR-NOR), Elimination of timing hazards/glitches, Combinational systems, Arithmetic functions (adders, subtracters, carry look ahead), Introduction to memory elements. (Total tuition time: ± 70 hours)

#### **DIGITAL SYSTEMS II (DSY231C)**

#### 1 X 3-HOUR PAPER

## (Subject custodian: Department of Computer Systems Engineering)

Un-clocked and clocked memory devices (latches, flip flops), Level vs edge sensitive and master-slave devices, Basic flip flops (SR, D, JK, T), Asynchronous flip flop inputs (preset, clear), Timing constraints (setup time, hold time) and propagation delays, Sequential logic circuits: data registers, shift registers, counters; Introduction to Finite state machines (FSM), CPLDs, FPGAs, ROMs, RAM, FLASH. (Total tuition time: ± 70 hours)



#### **DIGITAL SYSTEMS III (DSY341C)**

(Subject custodian: Department of Computer Systems Engineering)

1 X 3-HOUR PAPER

Structure of a computer system (CPU, memory, I/O devices on a bus); CPU families used in microcontrollers (4-bit, 8-bit, 16-32-bit) and microprocessors; Basic I/O devices (timers/counters, GPIO, A/D, D/A), Polled I/O vs Interrupt driven I/O, Vectored and prioritised Interrupts, DMA transfers, MMU, Memory architectures and caches; Introduction of embedded application designs. (Total tuition time: ± 70 hours)

Е

#### **ELECTRICAL ENGINEERING I (EEN111C)**

1 X 3-HOUR PAPER

(Subject custodian: Department of Computer Systems Engineering)

Students are introduced to the correct use of SI units and their applications, the construction and maintenance of batteries, a network analysis of direct current circuits and AC theory, a study of various measuring instruments. An investigation into the effects of magnetic lines of force, the application and use of magnetic fields, inductance and the factors affecting it, capacitors and their operation. (Total tuition time:  $\pm$  70 hours)

# **ELECTRONICS I (ELC111B)**

1 X 3-HOUR PAPER

(Subject custodian: Department of Computer Systems Engineering)

Introduction to electronic components, analysis and design using measuring instruments, diodes and rectification, simple power supplies, DC operating point of single-stage bipolar junction- and field-effect transistor amplifiers and basic operational amplifier configurations. Theory supported by assessed practical experiments in a laboratory, including soldered and proto-board projects. (Total tuition time: ± 70 hours)

#### **ELECTRONICS II (ELC211B)**

1 X 3-HOUR PAPER

(Subject custodian: Department of Computer Systems Engineering)

Modelling of electronic components and their application in circuit analysis and design. Unregulated and regulated linear power supplies with transistor and operational amplifier error correction, short-circuit protection and heat sink principles. Small-signal modelling of transistor amplifiers. Theory is supported by assessed project and practical experiments in a laboratory. (Total tuition time: ± 80 hours)

L

#### **LOGIC DESIGN III (LOD311B)**

1 X 3-HOUR PAPER

(Subject custodian: Department of Computer Systems Engineering)

The development of logic circuits using VHDL. The student is guided through the complete design cycle of a logic circuit using the prescribed development environment. This involves the development of the logic circuit in VHDL and the implementation onto target hardware. The development of software consists of problem setting, state diagrams, timing diagram analysis and the writing of the VHDL. The next phase is the simulation of the program. The final phase consists of the downloading onto silicon and de-bugging of the software. (Total tuition time: ± 80 hours)

М

#### MATHEMATICAL APPLICATIONS III (MMA301T)

1 X 3-HOUR PAPER

(Subject custodian: Department of Mathematics and Statistics)

First-order differential equations. Higher-order differential equations. Basic mathematical modeling. Laplace transforms. Systems of differential equations. Numerical solutions of differential equations. Fourier Series. (Total tuition time: ± 90 hours)

# MATHEMATICS I (MAT141F)

1 X 3-HOUR PAPER

(Subject custodian: Department of Mathematics and Statistics)

Basic mathematics. Differentiation. Integration. Matrices and determinants. Vectors. Data handling. Complex numbers or mensuration. (Total tuition time: ± 90 hours)

# **MATHEMATICS II (MAT251F)**

1 X 3-HOUR PAPER

(Subject custodian: Department of Mathematics and Statistics)

Revision of differentiation (Mathematics I). Differentiation of functions with more than one variable. Further integration. Numerical methods. First-order ordinary differential equations. Matrices (Gauss elimination). (Total tuition time:  $\pm$  90 hours)



N

#### **NETWORK SYSTEMS II (NSY211T)**

1 X 3-HOUR PAPER

# (Subject custodian: Department of information Technology)

This subject covers various aspects and technologies involved in data communications and networking. Students are introduced to topics, such as network topologies, transmission fundamentals, contention protocols, data compression techniques, data security and integrity, flow-control protocols and the various IEEE standards. The subject is aimed at giving students a solid understanding of local area networks (LANs), although aspects of wide area networks (WANs) are also covered briefly. (Total tuition time: ± 80 hours)

#### **NETWORK SYSTEMS III (NSY311T)**

1 X 3-HOUR PAPER

# (Subject custodian: Department of Information Technology)

This subject covers the TCP/IP protocol suite in detail, including protocols such as IPv4, IPv6, TCP, UDP, ICMP, DNS, FTP. Other networking concepts like packet addressing, forwarding, and routing are also covered. The student will be equipped with skills to plan IP addresses using VLSM and to configure networks. The practical component concentrates on basic design of data networks, IP address planning, creating VLANs, configuration of routers using dynamic protocols like RIP, OSPF, etc. (Total tuition time: ± 80 hours)

0

#### **OPERATING SYSTEMS III (OSY301T)**

1 X 3-HOUR PAPER

## (Subject custodian: Department of Computer Systems Engineering)

The development of the operating system as a control programme and resource manager. Principles to take into consideration when designing a modern operating system, such as memory management, process management, scheduling and input/output. The LINUX operating system. CPU scheduling, parallelism, secondary memory management, LINUX applications. (Total tuition time: ± 80 hours)

#### OPERATIONAL RESEARCH III (ORS311T)

1 X 3-HOUR PAPER

# (Subject custodian: Department of Computer Systems Engineering)

Aim/Purpose: To introduce students to the scientific approach to solving management science problems. Objectives: To be introduced to linear programming processes, Network modules, queuing and decision analysis, project scheduling, decision theory, forecasting, queuing models, simulation, inventory control. Key topics: Linear Programming, distribution and assignment problems, Network modules, project scheduling. (Total tuition time: ± 72 hours)

Р

#### PROGRAMMING I (PGG111T)

1 X 4-HOUR COMPUTER-BASED

# (Subject custodian: Department of Computer Science)

This subject accommodates students from a broad spectrum of disciplines and interests. It includes a theoretical as well as a practical component. Aim: This subject provides overview coverage of introductory C++ programming. Objectives: This is a career-focused information and communication technology qualification that will enable a graduate to design and create ICT software solutions using basic object-orientated concepts and technologies. Key topics: Basic C++ programs, classes, problem solving, OOP, methods, data manipulation, arithmetic operators, decision structures, loop structures, functions. (Total tuition time: ± 72 hours)

## PROGRAMMING II (PGG211T)

1 X 4-HOUR COMPUTER-BASED

# (Subject custodian: Department of Computer Science)

Aim/Purpose: To cover advanced OOP (Object Oriented Programming) principles, including inheritance and abstract programming, as well as other advanced concepts in C++. Objectives: To enable students to be able to create and manipulate one- and two-dimensional arrays, manipulate a collection of characters as strings; Read and write data from text files; Apply advanced class features. Key topics: One- and Two-Dimensional Arrays; Strings and Character Manipulation; Text Files, Additional Class Features; Inheritance; Polymorphism; Dynamic Memory Allocation. (Total tuition time: ± 72 hours)



#### PROGRAMMING III (PGG311T)

#### 1 X 4-HOUR COMPUTER-BASED

## (Subject custodian: Department of Computer Science)

Aim: The student is introduced to a modern C++ Rapid Application Development Tool for Win32 with the purpose of creating a human interface for pre-engineered C and assembly applications. Objectives: The student must be able to manipulate standard Windows components, graphical images, multiple forms, grid structures, selection structures, menu systems and dynamically created objects. The integration of C as well as assembly language routines including pre-manufactured as well as self-manufactured objects. A medium sized C++ GUI project is expected to be completed towards the end of the semester. Key topics: Advanced OOP, C++, C, ASM, dynamic object instantiation, event-driven programming, Back-end classes, strategic solution planning, systematic program design, flat file data handling. (Total tuition time: ± 72 hours)

# PROJECTS I (PJT101B)

#### CONTINUOUS ASSESSMENT

## (Subject custodian: Department of Computer Systems Engineering)

Use of instruments and equipment, such as multimeter, oscilloscope, power supply and function generator. Measurement of alternating and direct current, voltage and frequency. Component identification, application, measurement and testing. Reading basic schematic diagrams. Construction and testing of an electronic project. Stripping and insulating conductors. Wiring and placing components. Basic health and safety. Laboratory policies and procedures. Basic hand skills such as soldering, metal working including drilling. Building of a project into an enclosure. Web programming using appropriate Web system engineering environment such as http/html/php/MySqL/FORMS, which includes direct socket connections. (Total tuition time: ± 70 hours)

S

#### **SOFTWARE ENGINEERING III (SFE311T)**

#### 1 X 3-HOUR PAPER

## (Subject custodian: Department of Computer Science)

The purpose of this subject is to present the essential knowledge and skills needed to solve the customer and the user problems through the use of domain analysis which helps to have the background information as well as to gather and validate requirements. This subject enables students to learn how to apply software engineering skills into real life situation by understanding what constitutes a good requirement, applying fundamental design principles, gaining in-depth knowledge of object-oriented development using Unified Modeling Language, translating requirements and designs into good quality programs and to test the programs effectively. (Total tuition time: ± 60 hours)

#### SYSTEMS ANALYSIS II (SYA201T)

#### 1 X 3-HOUR PAPER

## (Subject custodian: Department of Computer Science)

A detailed study of the five phases of the systems development life cycle (SDLC), giving the student an indepth understanding of how information technology supports operational and business requirements in today's competitive environment. The importance of communication, economic analysis and project planning skills in all phases of the SDLC is discussed. (Total tuition time: ± 60 hours)

w

# WORK-INTEGRATED LEARNING I (EXP1ECS) WORK-INTEGRATED LEARNING II (EXP2ECS)

WORK-INTEGRATED LEARNING WORK-INTEGRATED LEARNING

# (Subject custodian: Department of Computer Systems Engineering)

Students experience the IT- or engineering-related industry by becoming involved in its day-to-day operations. (Total tuition time: not available)

