

TYPE 2AB EVB Quick Start Guide

Note: Details on Type 2AB EVB board can be found on [muRata® website¹](https://www.murata.com/en-us/products/connectivitymodule/ultra-wide-band/qorvo).

Introduction

The muRata® Type 2AB EVB is an evaluation board using the muRata UWB/Bluetooth Low Energy module Type 2AB.

Type 2AB module contains a Qorvo QM33120 UWB transceivers and a Nordic nRF52840 BLE SoC.

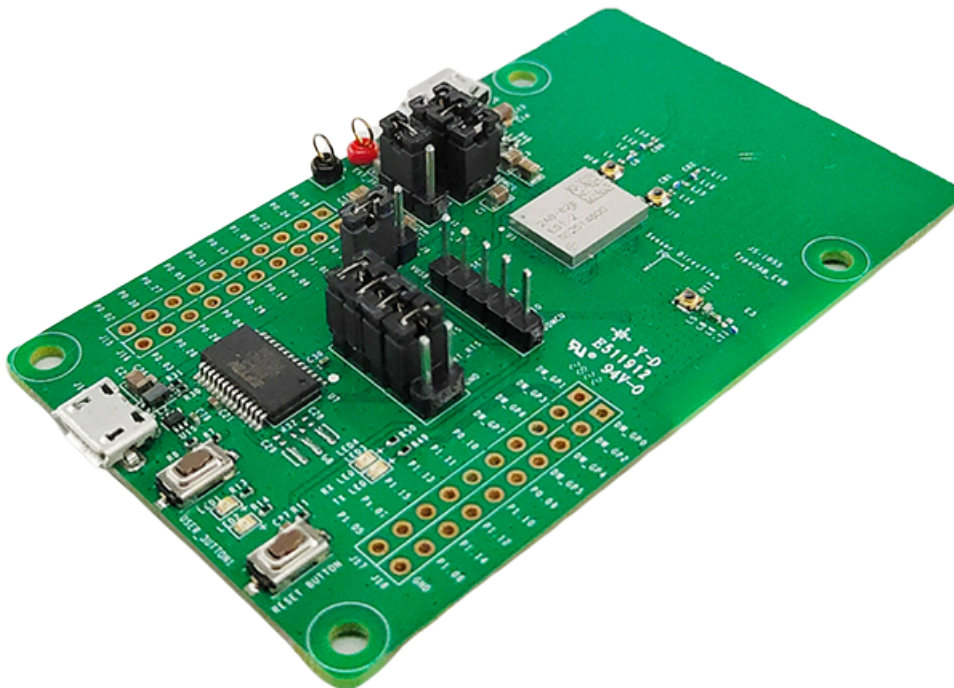


Figure 1.1: Type 2AB EVB Rev4.0 (JS-1055)

¹ <https://www.murata.com/en-us/products/connectivitymodule/ultra-wide-band/qorvo>

Jumpers configuration

Type 2AB EVB has multiple jumpers defining the power supply of the board.

Please refer to [muRata® UWB Module\(2AB\) EVB User Manual²](#) for further information.

Required Tools

Hardware Tools

The following hardware tools are needed to flash the binary into the target board over SWD interface:

- [J-Link debug/flash probe³](#)
- [J-Link adapter CortexM⁴](#)

Software Tools

To flash the development kit, you can use the [Segger J-Flash Lite⁵](#), which is a component of the **J-Link Software and Documentation Pack**. Follow the instructions below to download and install J-Flash Lite:

1. Go to the [Segger downloads page⁶](#).
2. Choose the latest version of the J-Link Software and Documentation Pack which is compatible with your operating system.
3. Download and execute the installer, then follow the installation procedures.
4. Once the software is installed, you can proceed with flashing your development kit.

Flashing the development kit

Warning: Type 2AB EVB boards are not shipped preprogrammed. UCI firmware needs to be flashed to continue.

1. To flash the board, it must be powered on by connecting a micro-USB cable to the board (or via external power depending on jumpers configuration). Refer to [muRata® UWB Module\(2AB\) EVB User Manual⁷](#) if needed.

For flashing or debugging, connect the JTAG connector to a J-Link Debug Probe by SEGGER® as follow:

² <https://www.murata.com/en-us/products/connectivitymodule/ultra-wide-band/qorvo#datasheet-and-documentations>

³ <https://www.segger.com/products/debug-probes/j-link/>

⁴ <https://www.segger.com/products/debug-probes/j-link/accessories/adapters/9-pin-cortex-m-adapter/>

⁵ https://wiki.segger.com/J-Flash_Lite

⁶ <https://www.segger.com/downloads/jlink/>

⁷ <https://www.murata.com/en-us/products/connectivitymodule/ultra-wide-band/qorvo#datasheet-and-documentations>

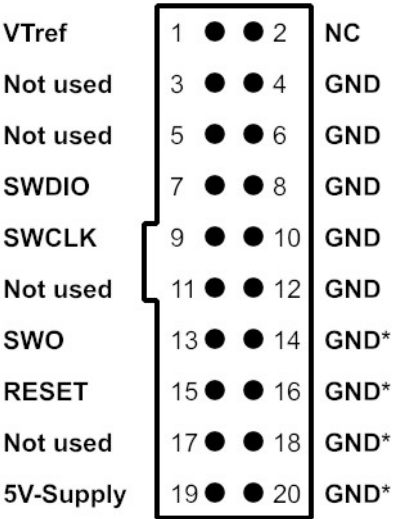


Figure 1.2: J-Link SWD Connector Pinout

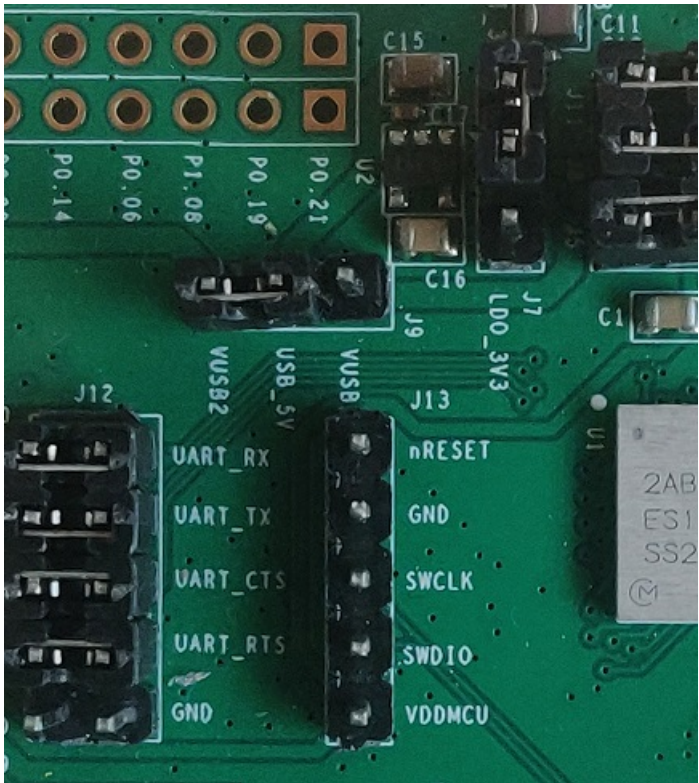


Figure 1.3: Type 2AB EVB SWD Connector

Following signal names from previous pictures, here is the connections to do:

J-Link	Type 2AB EVB
RESET	nRESET
GND	GND
SWCLK	SWCLK
SWDIO	SWDIO
VTref	VDDMCU

2. Locate the JFlashLite tool in your installation directory (e.g. C:\Program_Files\SEGGER\FlashLite.exe in Windows or /usr/bin/JFlashLite in Ubuntu) and launch the application.

Note: When you connect the development kit for the first time, you may be prompted to update firmware of the on-board programmer. Please approve the update by clicking **OK** and wait for the update to complete.

3. Upon starting JFlashLite, the Device and Interface selection dialog will appear. Select **NRF52840_xxAA** device by clicking on “...” button. On the right side of dialog window, select **SWD** Interface with clock speed of **4000 kHz**. Click **OK** to proceed.

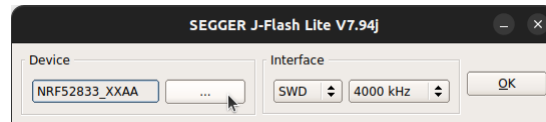


Figure 1.4: Select CPU settings.

4. Next, select the firmware file you wish to flash onto the board. Click the “...” button to browse for the firmware (e.g. SDK/Binaries/Type2AB_EVB/Type2AB_EVB-UCI-FreeRTOS.hex).

After selecting the file, click **Program Device** to start the flashing process.

Note: If the board was previously flashed with a different firmware version, it is recommended to erase the flash memory before programming the new firmware. To perform this, please click **Erase Chip** button and then proceed with flashing the new firmware.

Note: To evaluate the board with the Qorvo One TWR GUI application please ensure that you flashed the **UCI** hex file.

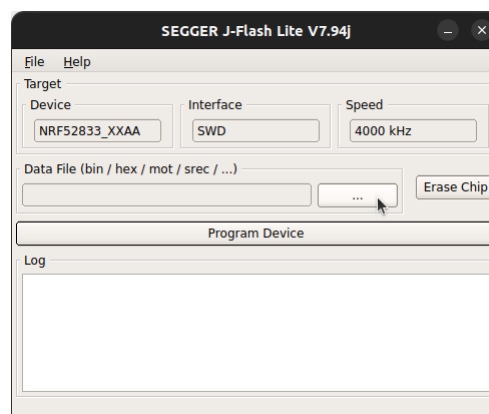


Figure 1.5: Select .hex file to flash.

5. Once the progress bar is completed, the device has been successfully flashed. Perform a power cycle by disconnecting and reconnecting the power supply to reset the board. Your device should be now ready to use.

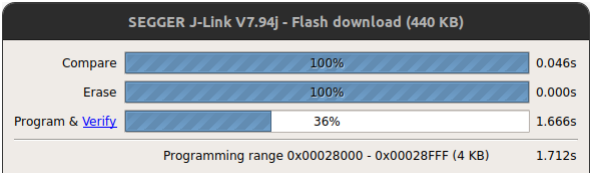


Figure 1.6: Programming progress window.

Type 2AB EVB Setup

Connect both Type 2AB EVB boards to a PC using USB cables.

Note: Make sure that the antennas face each other during the evaluation.

Ranging and AoA Evaluation

Install the **Qorvo UWB Explorer** GUI evaluation software, which is included in the package under the SDK/Tools/GUI directory.

Warning:

- **Linux**

- If you got the error:

```
dlopen(): error loading libfuse.so.2
```

Install libfuse2 using the following commands:

```
sudo apt update
```

```
sudo apt install libfuse2
```

- On Ubuntu 20.04, you may also need to install qt5-default:

```
sudo apt update
```

```
sudo apt install qt5-default
```

- On Ubuntu 22.04 & Ubuntu 24.04, you may also need to install qtbase5-dev:

```
sudo apt update
```

```
sudo apt install qtbase5-dev
```

- **macOS**

- if you got an alert when you open the application:

In System Settings, click Privacy & Security. Then scroll down to Security.

Under “Allow apps downloaded from”, select an option “App Store and identified developers”

Open the installed **Qorvo UWB Explorer** GUI software and select the **Localization** application from the start screen.

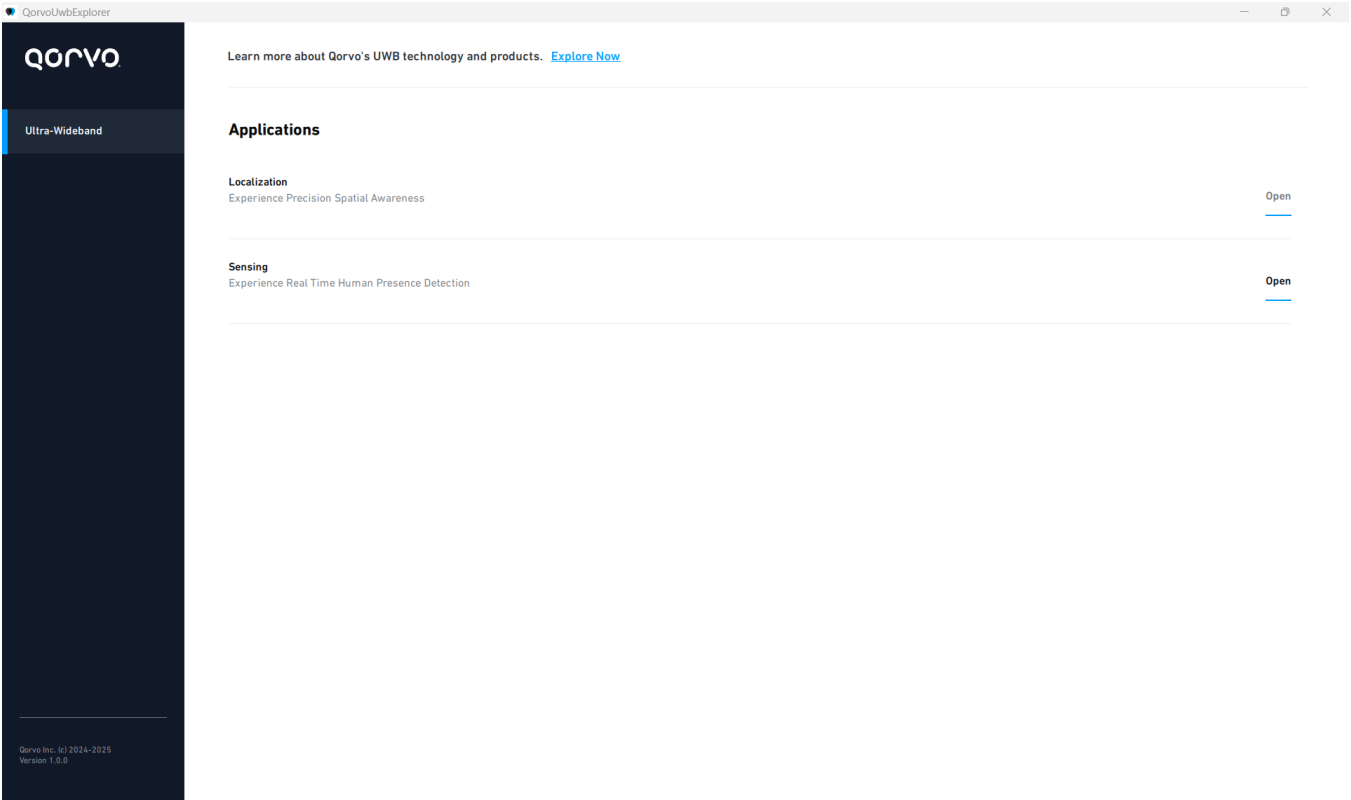


Figure 1.7: Qorvo UWB Explorer start screen

Upon starting the application, review the *Qorvo Software License*. If you agree to the terms and conditions, select **QM33120W DK** as the Development Kit and click **Next** to proceed.

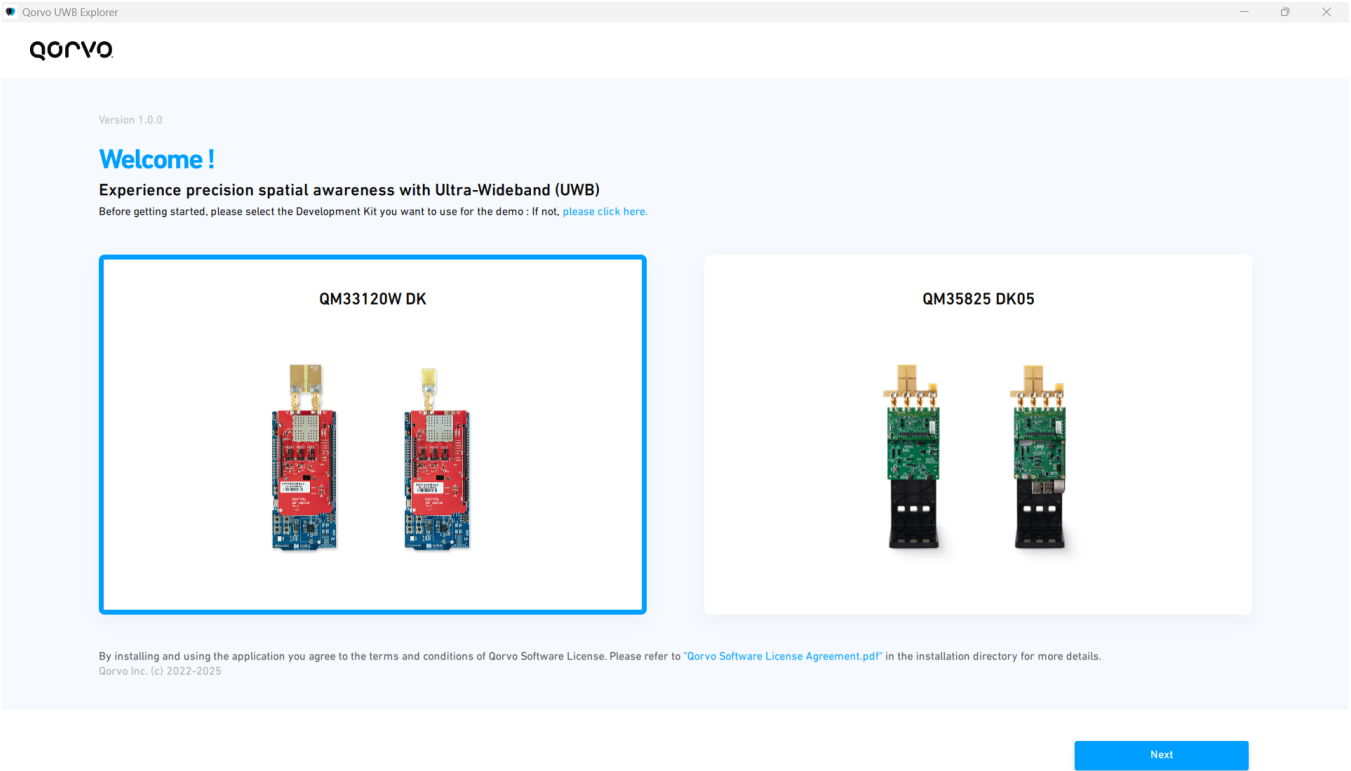


Figure 1.8: Welcome screen

The boards will be detected and displayed on the main screen.

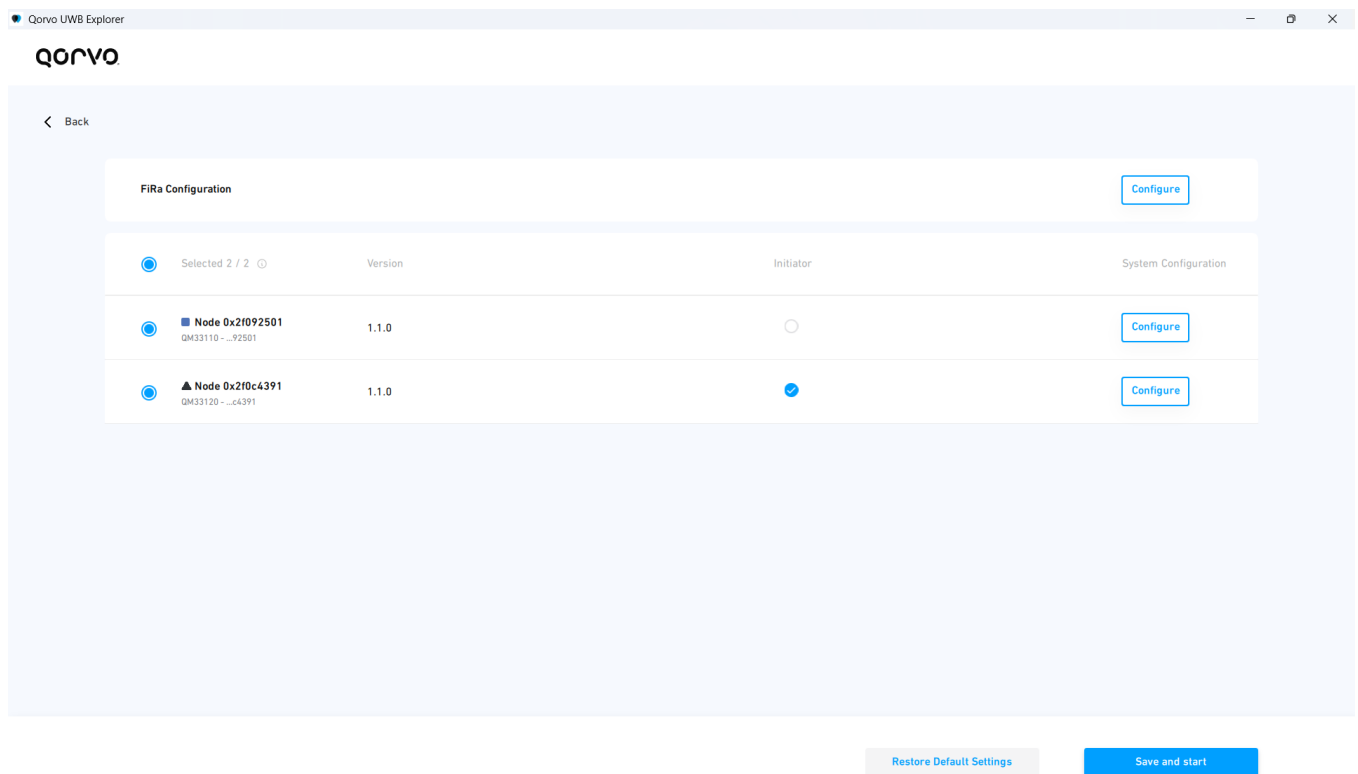


Figure 1.9: Boards detection

FiRa Configuration

Select the **Configure** button in the **FiRa Configuration** row to setup the ranging parameters. A new window will appear, allowing you to modify various FiRa settings, such as **UWB Channel** or **Ranging duration**.

FiRa Configuration

Initiator and Responder

Measurement scheme

DS-TWR Deferred Mode

Peer mode

One-to-Many

Channel

9

Slot Duration (ms)

2

Ranging Duration (ms)

200

Num. of slots in ranging rounds

25

Ranging Frame (RFRAME)

SP3

Frame preamble code

10

SFD ⓘ

2

PSDU data rate

6.81 Mbps

Pulse Repetition Freq. (PRF)

BPRF

Schedule mode

Time scheduled

Number of STS Segments

1

Ranging round hopping

☐

STS length

64 symbols

Reset all Configuration

Save

Figure 1.10: FiRa configuration

Apply desired setting and click **Save** button. You can also use **Reset all Configuration** to restore the default values.

Device configuration

To setup the device parameters of each board, click the **Configure** button located in the **System Configuration** column. A new window will appear with two configuration tabs: **Basic settings** and **Advanced settings**.

Basic settings

The **Basic settings** tab provides essential configuration options and calibration management.

▲ Device Configuration Node 0x2f0c4391

Initiator

Basic settings Advanced settings

Device	QM33120 (0xdeca0314)	Import settings	Choose file
ID	0x2f0c4391	Export settings	Export file
Firmware Version	1.1.0	Define address automatically	<input checked="" type="checkbox"/>
TX Power delta	-3dB: 0xc0c0c0c		
Antenna type	Custom		
Pulse shape	Standard		

Reset all Configuration Save

Figure 1.11: Basic device configuration

In the Basic settings tab, you can:

- **Load and export calibration files:** Import preset calibration configurations or export current settings
- **Configure TX Power:** Adjust transmission power levels for optimal performance
- **Set Pulse Power:** Control pulse power parameters for ranging operations

- **View device information:** Check firmware version and device details

Advanced settings

The **Advanced settings** tab provides detailed antenna configuration options for experienced users.

▲ Device Configuration Node 0x2f0c4391

Initiator

Basic settings **Advanced settings**

Antenna Path

Path	Port	Transceiver	LNA/PA
Path 0	1	TX	<input type="checkbox"/>
Path 1	1	RX	<input checked="" type="checkbox"/>
Path 2	2	RX	<input checked="" type="checkbox"/>

Antenna delay:

Path 0: 16392 Path 1: 16392

Antenna pair 0:

1st path: 1 2nd path: 2

PDof offset: 0

Axis: x axis

LUT Load

Reset all Configuration **Save**

Figure 1.12: Advanced device configuration

In the Advanced settings tab, you can configure:

- **Antenna paths:** Select and configure different antenna path configurations
- **Antenna pairs settings:** Set up antenna pair combinations for AoA measurements
- **Antenna delay:** Fine-tune antenna delay parameters for accurate ranging
- **Advanced timing parameters:** Adjust detailed timing settings for specialized applications

Note: Advanced settings are recommended to be modified by experienced users familiar with UWB technology. Incorrect configurations may impact ranging accuracy and device performance.

Calibration Setup

To ensure optimal ranging performance, the board's UWB chip requires calibration.

To understand calibration settings, refer to the **Calibration and Configuration** section in **DW3000 QM33 SDK Developer Manual**. However, to quickly evaluate the system you can use preset settings provided in the SDK.

Note:

- Configuration and calibration settings are non-volatile, they are retained after power cycle or firmware update.
- If you perform a Non-Volatile Memory (NVM) erase, such as a chip erase, it is crucial to reapply calibration and configuration to the device.

Warning: It is mandatory to perform the configuration procedure of the UWB chip when the development kit is used for the first time or when the SDK is upgraded to a higher version since the compatibility of the calibration data from one firmware version to another is not guaranteed.

To load the calibration file, open the **Device configuration** window and navigate to the **Basic settings** tab ([Basic device configuration](#)).

Then, click the **Choose file** button in the **Import settings** row to select and import the calibration file.

In the new dialog, select appropriate calibration file:

SDK/Tools/ufw-qorvo-tools/scripts/device/load_cal/calib_files/Type2AB_EVB/
patch_aoa.json

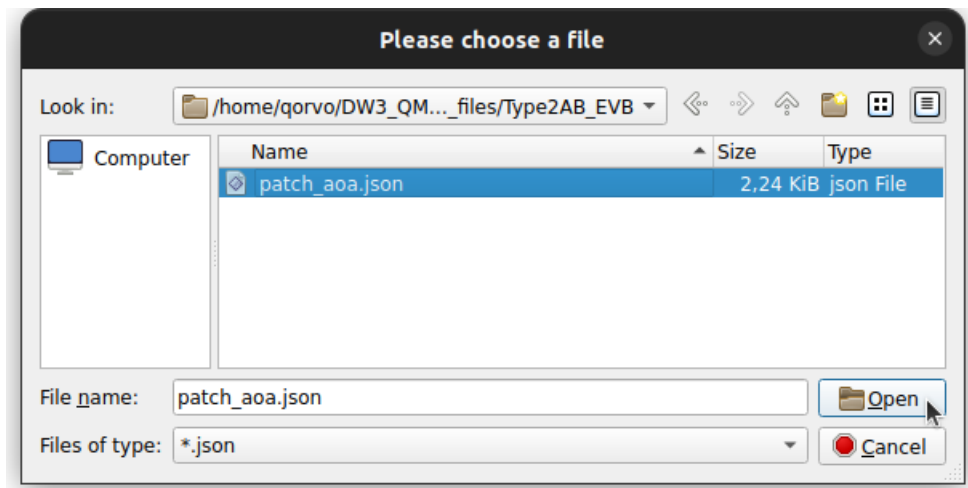


Figure 1.13: Device calibration file

Press **Save** button in **Device configuration** window to apply configuration and calibration. When calibration has changed, saving may take up to 10 seconds.

TWR Ranging

Press **Save and start** to start the ranging experience.

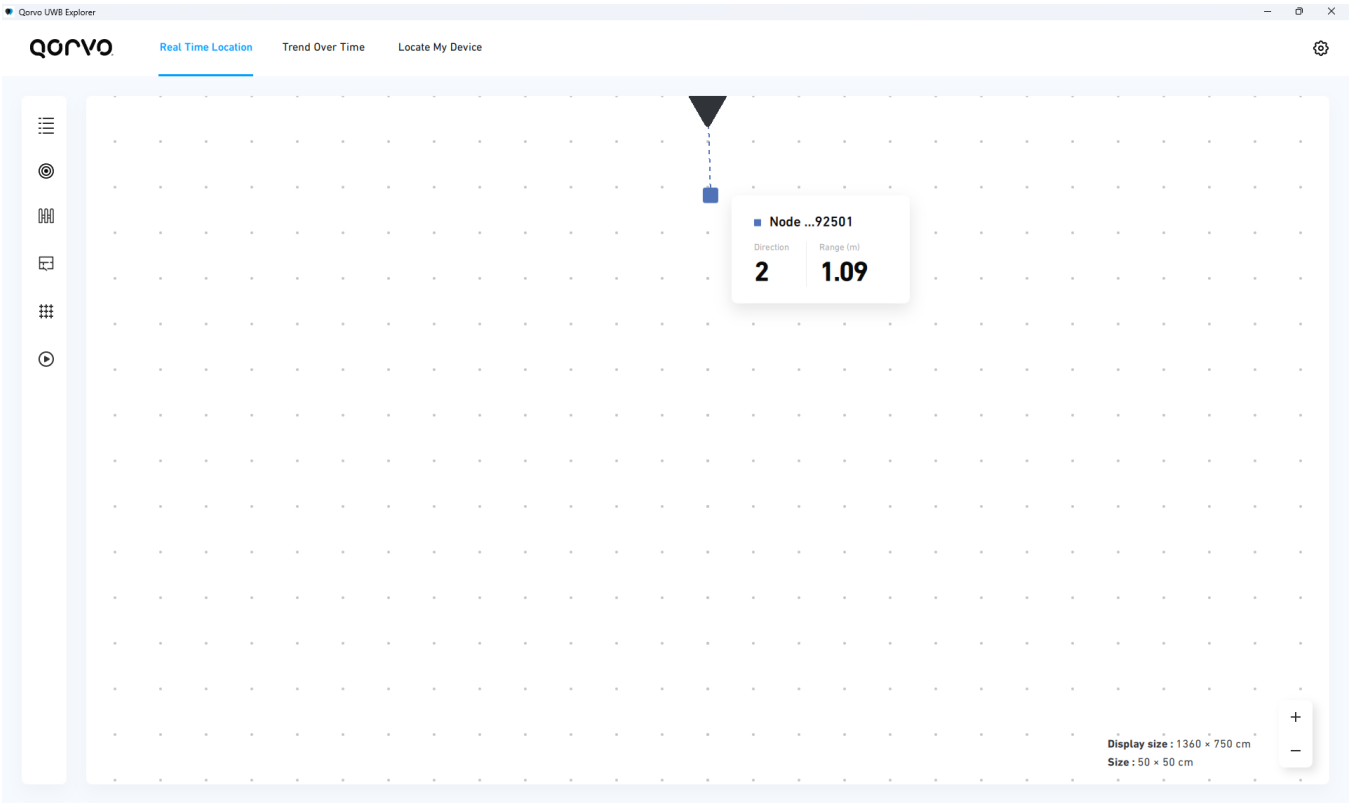


Figure 1.14: Real Time Location

Auto Calibration

The Qorvo One TWR GUI software provides an auto calibration feature that simplifies the calibration process on the development boards. This feature automatically adjusts the antenna delay and the PDoA offset in the NVM of the MCU.

Note: As antenna delay and PDoA offset values are stored in a specific section of the NVM, they are retained after power cycle or firmware update.

It is highly recommended to perform auto calibration as it improves distance and AoA performances.

Click on the **Auto Calibration** button located in the left bar.



Figure 1.15: Auto calibration icon

A new tab will appear, listing the different devices.

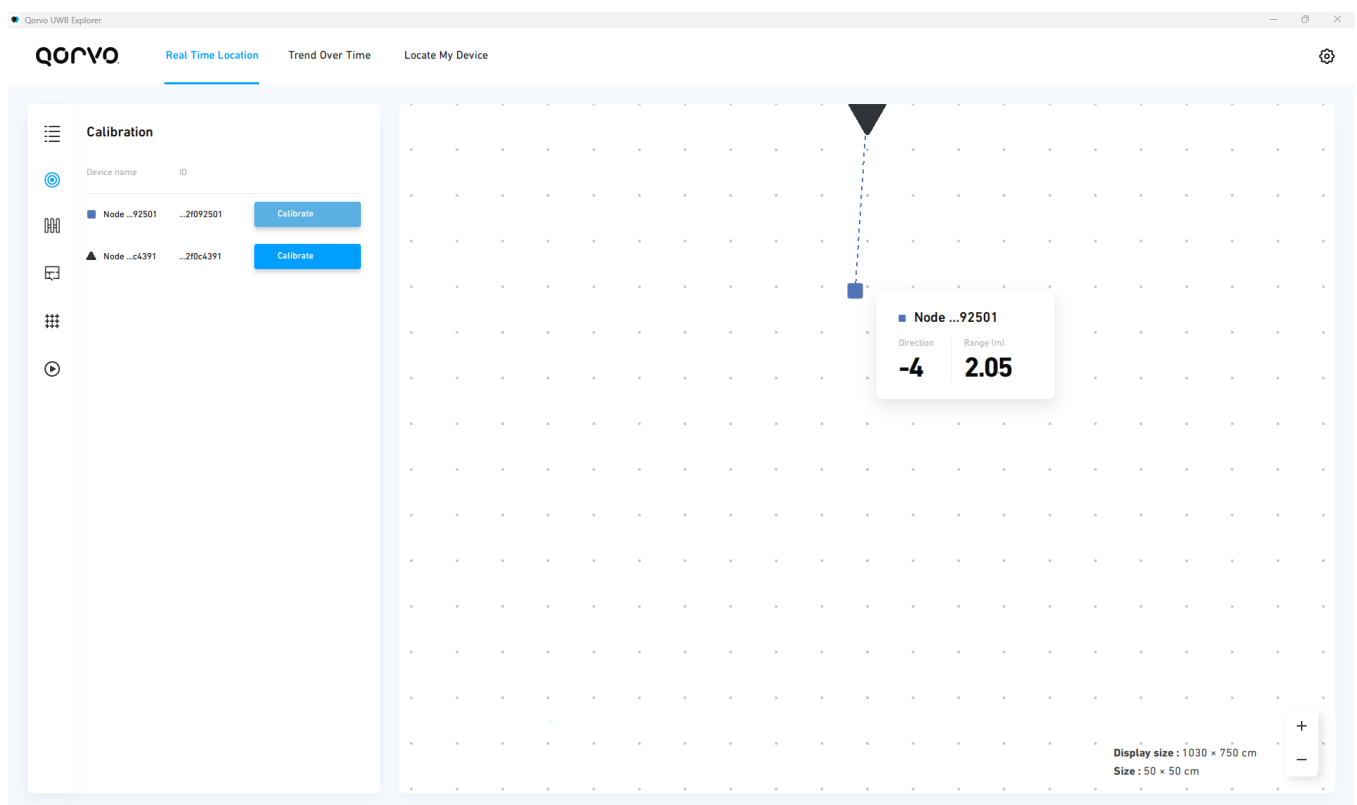


Figure 1.16: Auto Calibration tab

Click on the **Calibrate** button of the device to calibrate. A new window will appear, explaining the auto calibration process.

Note: It is important to follow the instructions displayed in the window as precisely as possible to ensure improved performance.

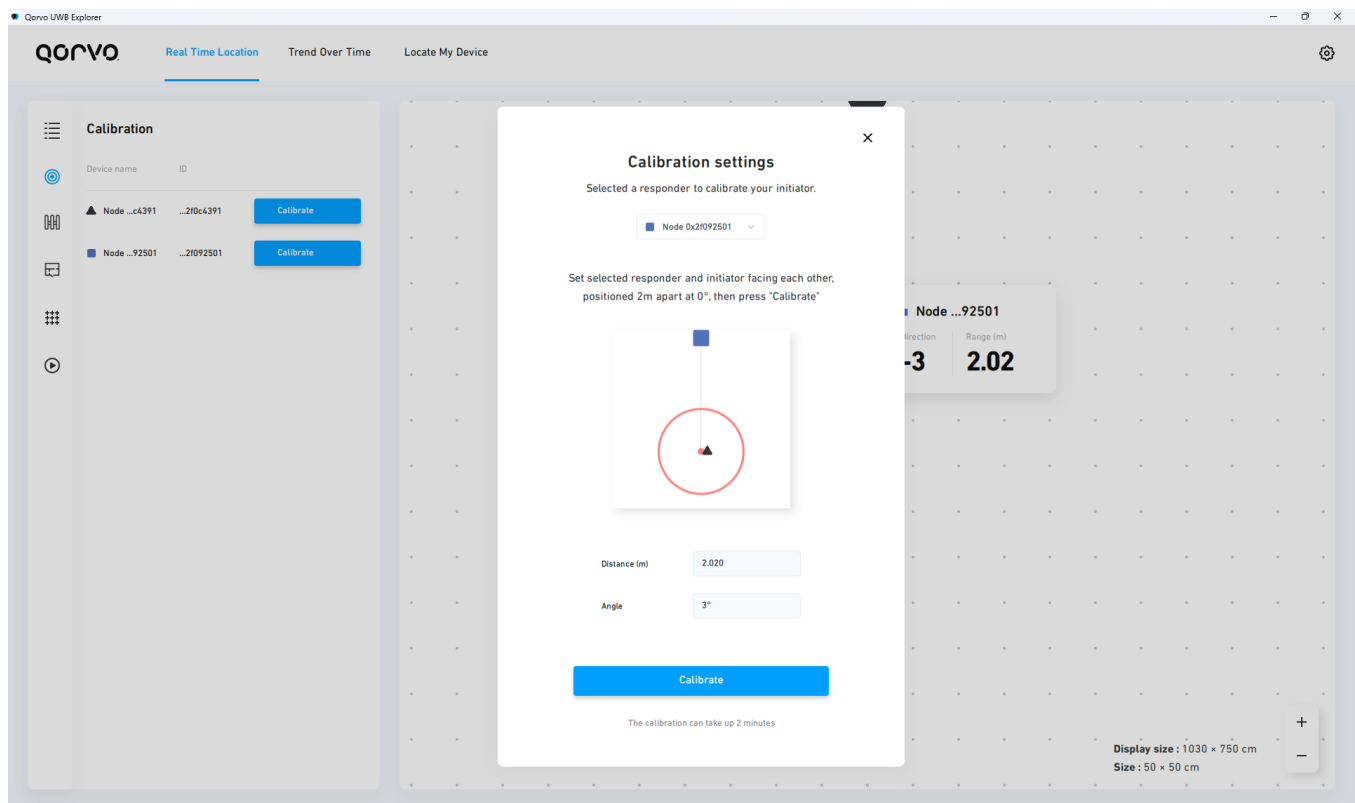


Figure 1.17: Auto Calibration window

Click on the **Calibrate** button to start the auto calibration. Once the auto calibration process is complete, the software will display a message indicating the success of the calibration.

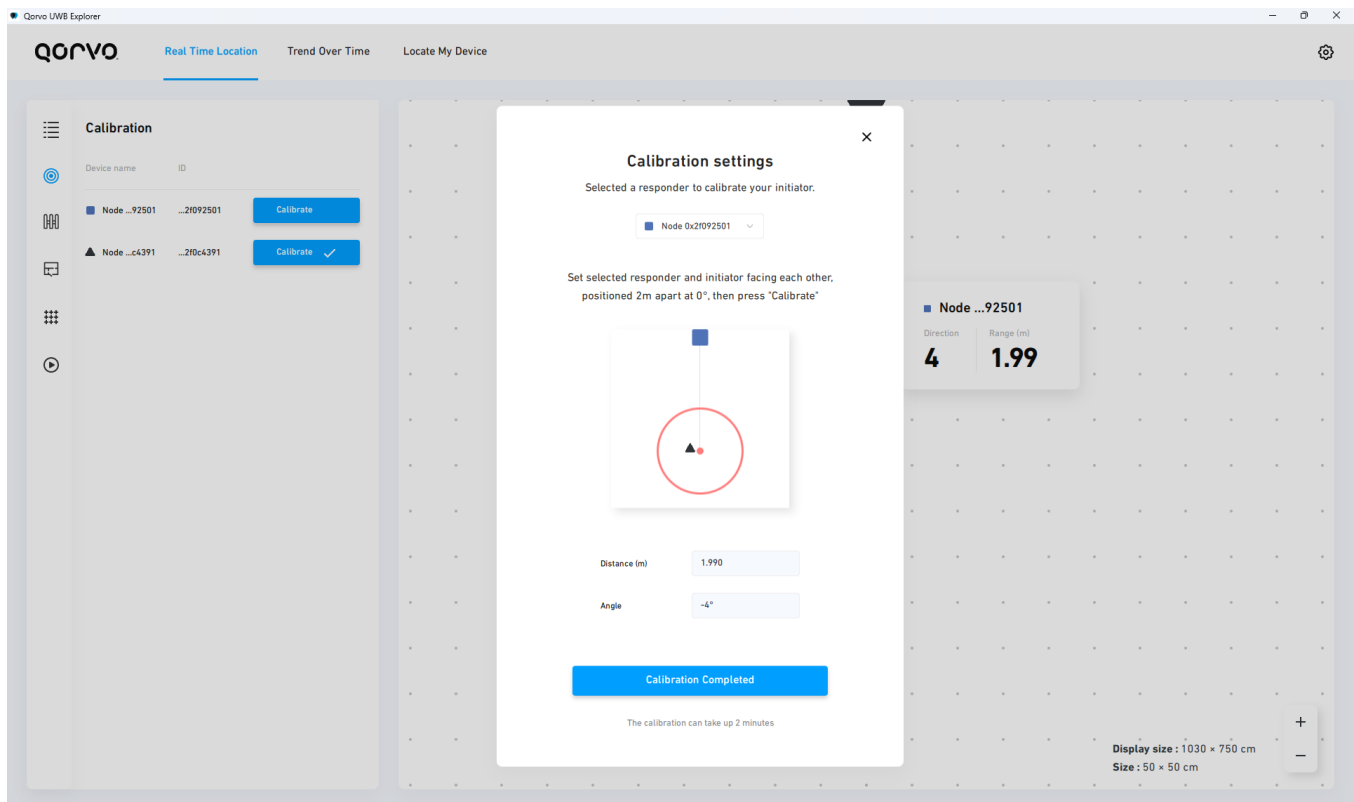


Figure 1.18: Auto Calibration completed

The device will be marked as calibrated in the Auto Calibration tab.

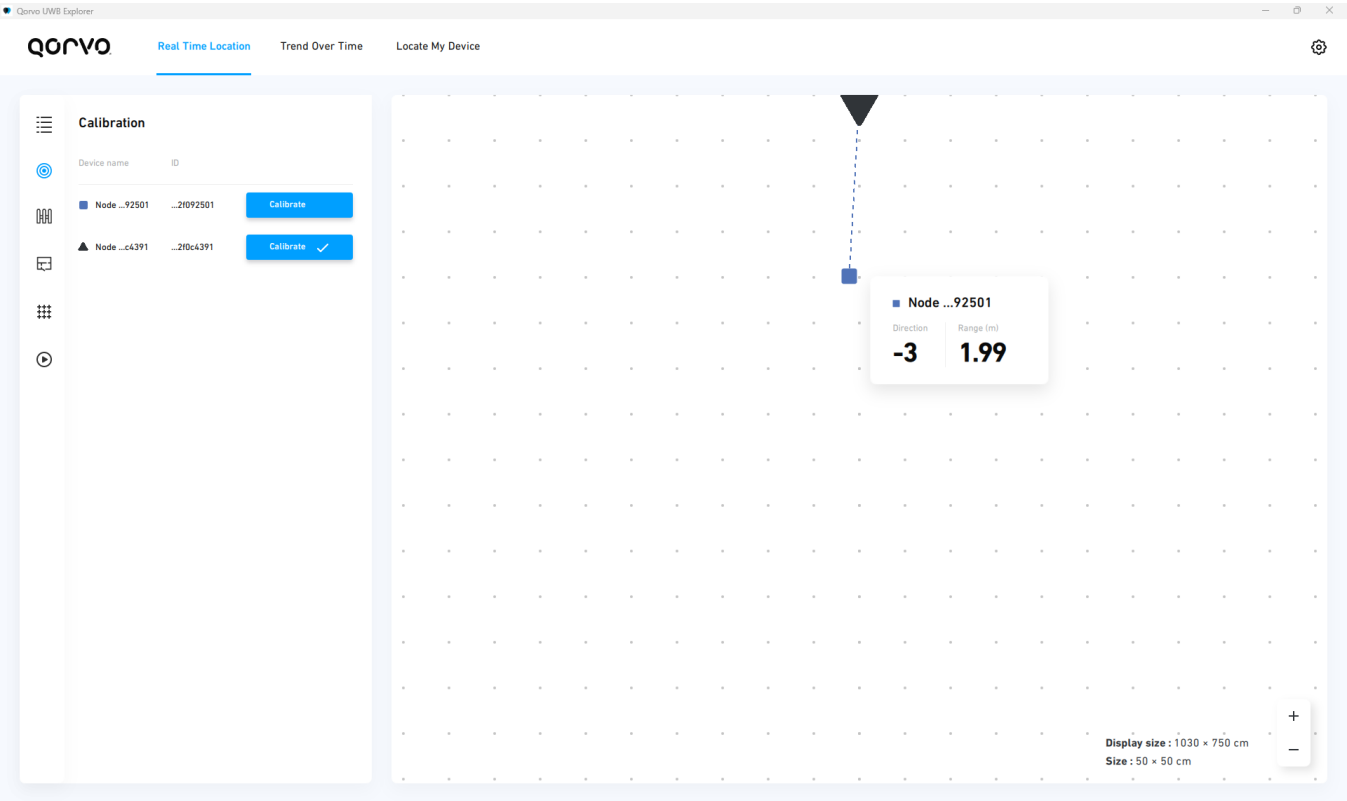


Figure 1.19: Auto Calibration tab with device calibrated

After completing the auto calibration, you can proceed with the TWR ranging or AoA evaluation as described in the previous section.

Revision History

Table 1.1: Revision History

Version	Date	Comment
DW3_QM33_SDK_1.1.1	2025-08-13	<ul style="list-style-type: none"> Updated <i>Ranging and AoA Evaluation</i> section to document the transition from Qorvo One TWR GUI to the new Qorvo UWB Explorer GUI.
DW3_QM33_SDK_1.1.0	2025-07-22	<ul style="list-style-type: none"> Added <i>macOS</i> instructions for Qorvo One TWR GUI installation in the <i>Ranging and AoA Evaluation</i> section.
DW3_QM33_SDK_1.0.0	2024-08-22	<ul style="list-style-type: none"> Added <i>Required Tools</i> section. Added <i>Flashing the development kit</i> section. Updated <i>Ranging and AoA Evaluation</i> section: * Content divided into subsections: <i>Fira Configuration</i>, <i>Device Configuration</i> and <i>TWR Ranging</i>. * Added information about calibration and configuration. * Updated GUI images to the latest version. Updated target board images and their descriptions. Removed <i>Type 2AB EVB Flash</i> section and moved it to the <i>Flashing the development kit</i> section. Removed reference to discontinued <i>Type2AB JS-0989 EVB</i> board from the <i>Introduction</i> section.
DW3_QM33_SDK_0.1.1	2022-11-03	Initial release