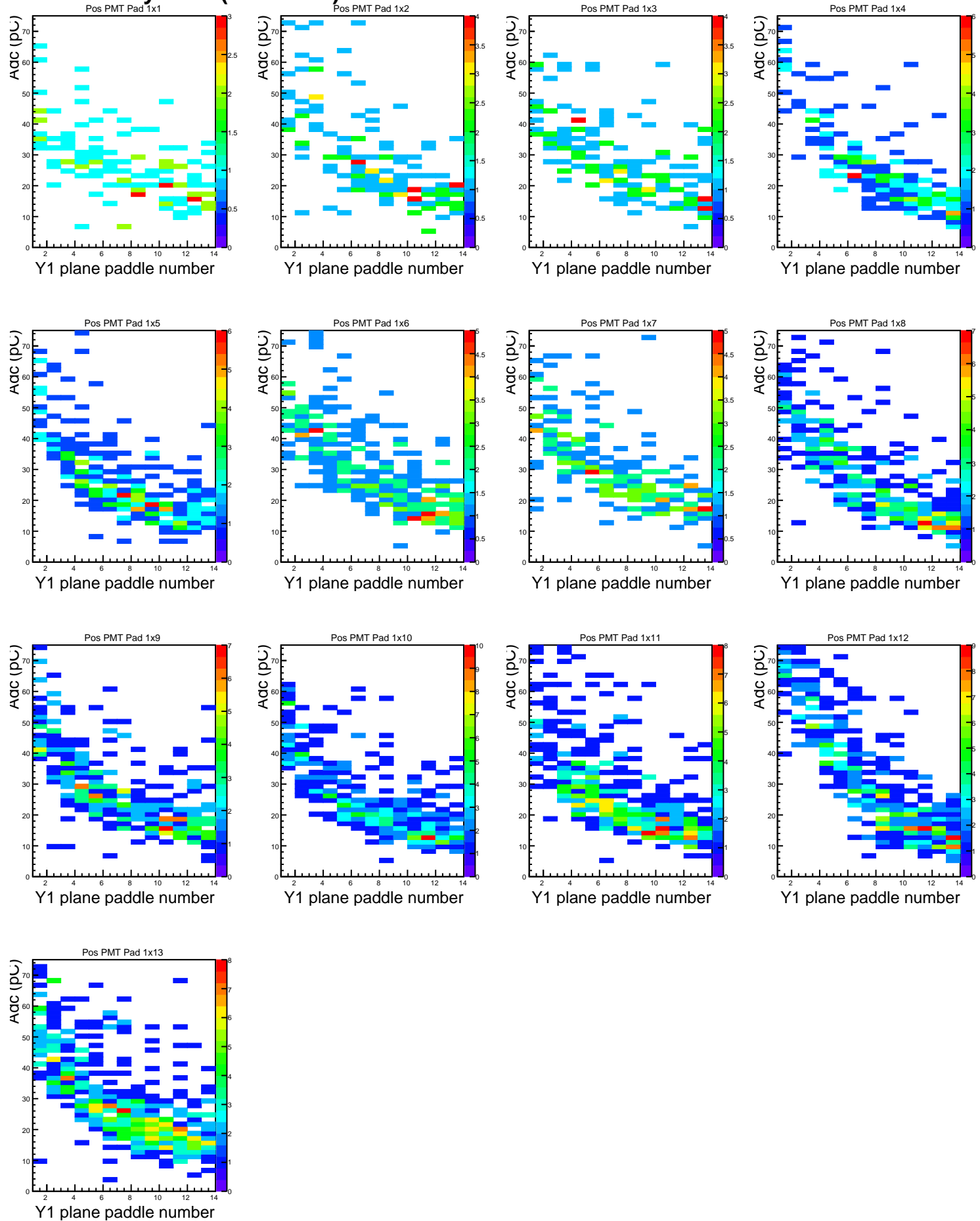
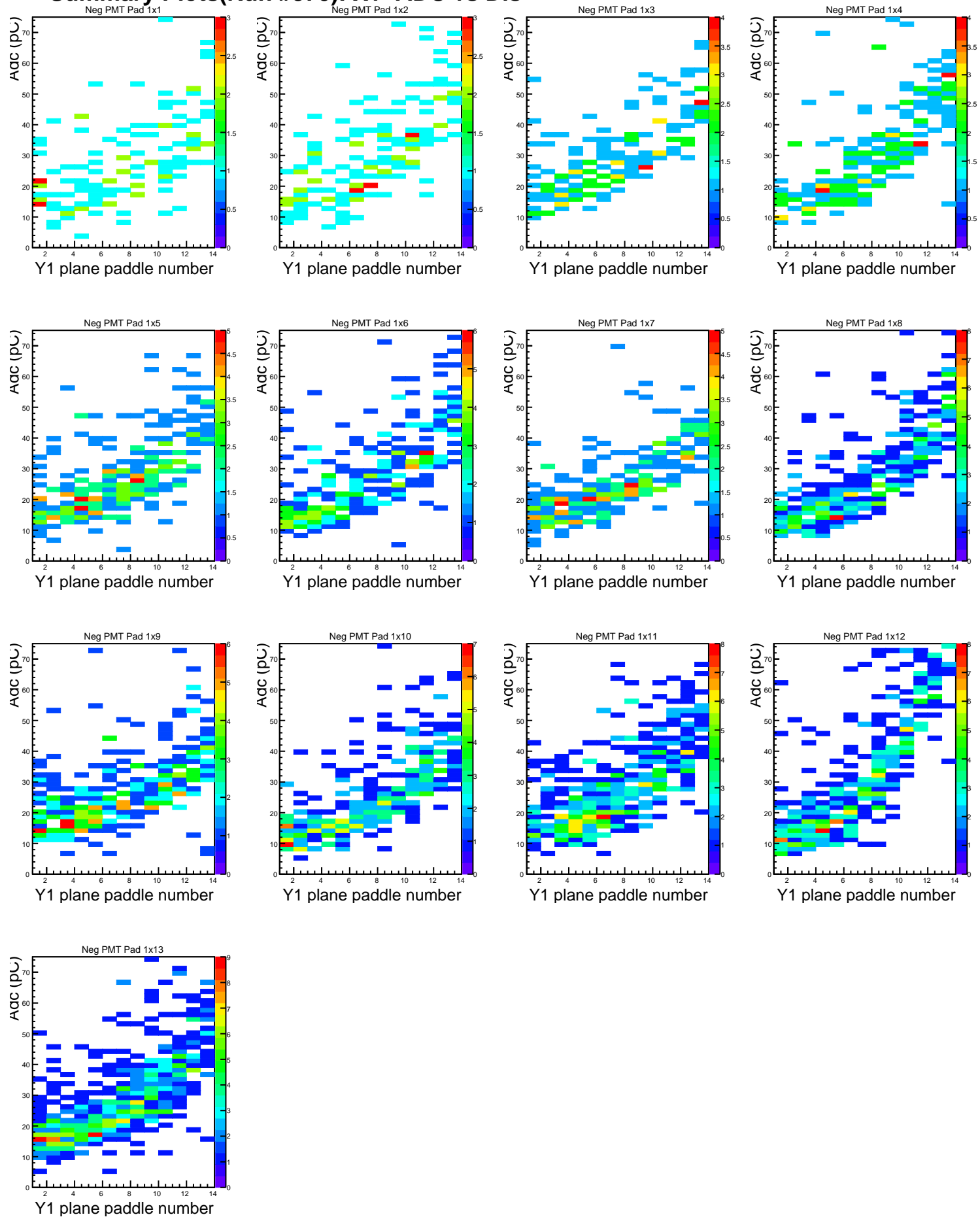


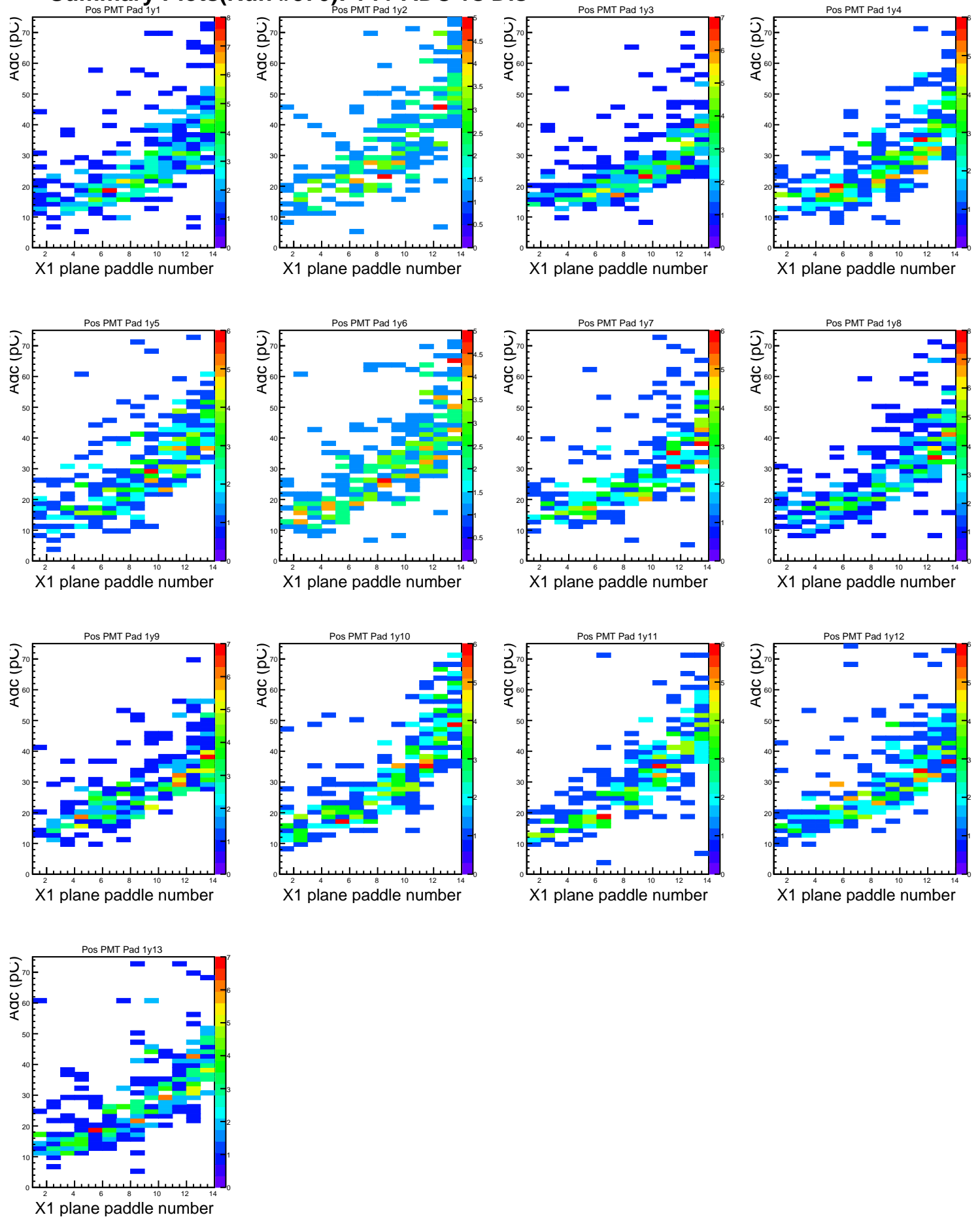
Summary Plots(Run #370): X1+ ADC vs Dis



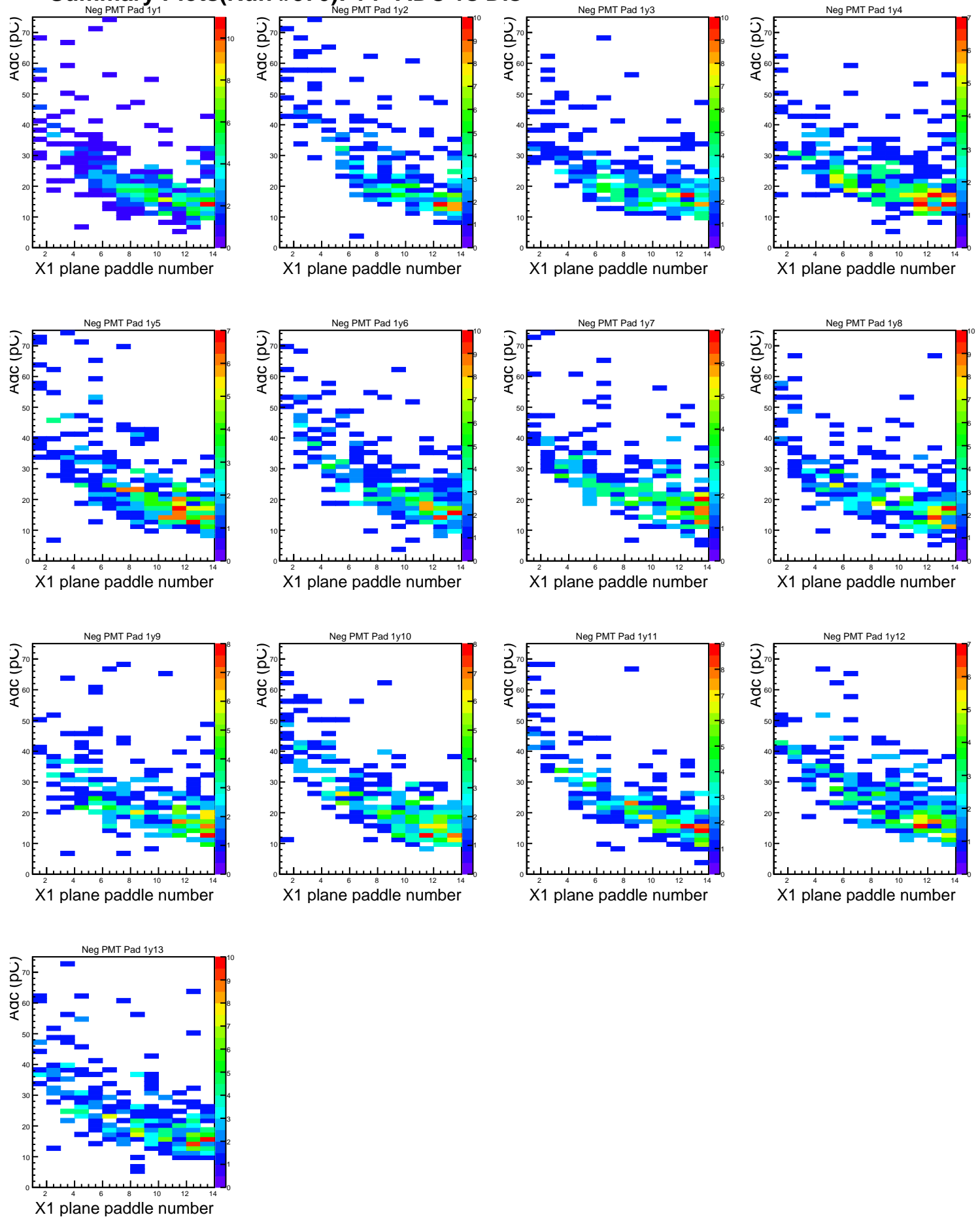
Summary Plots(Run #370): X1- ADC vs Dis



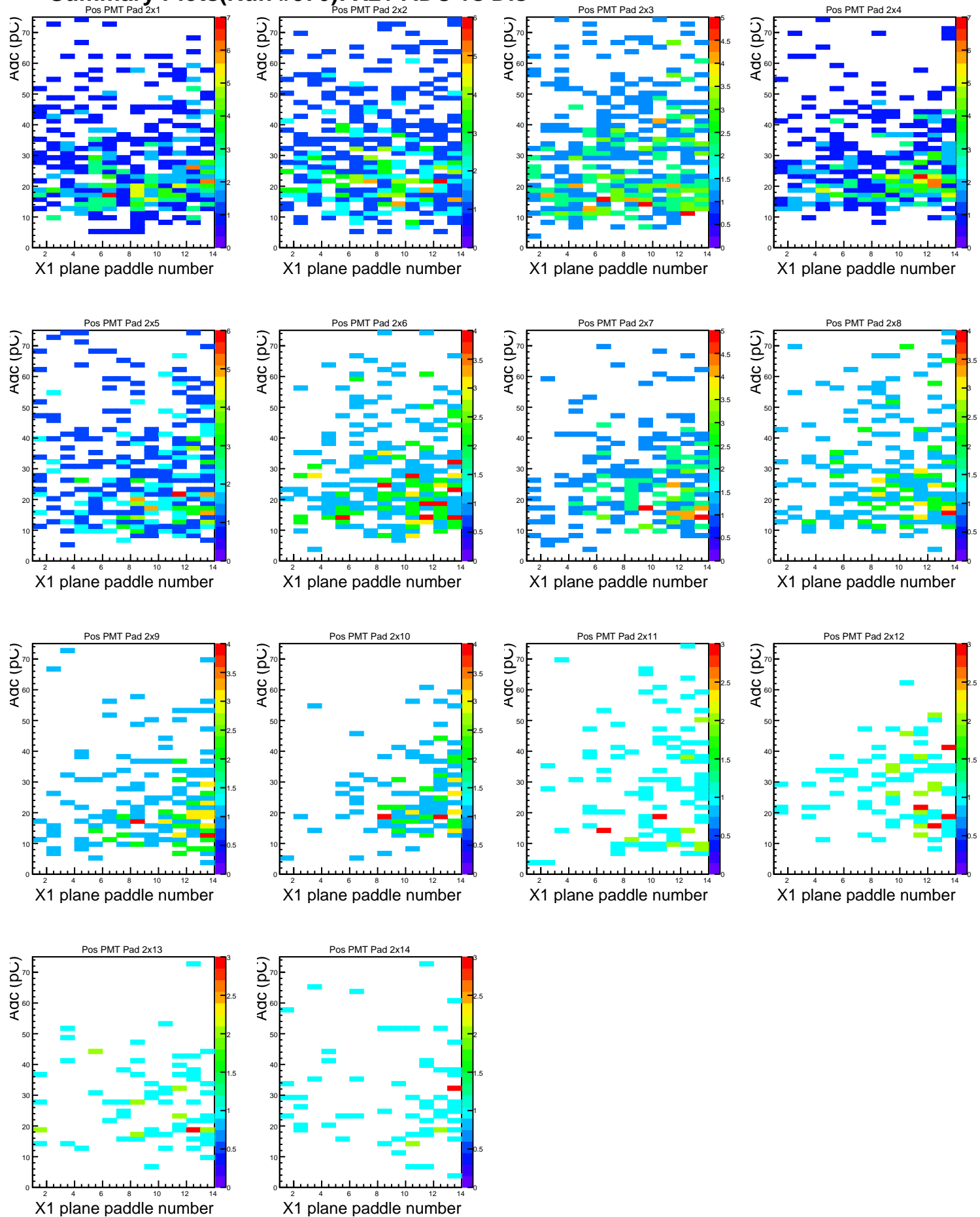
Summary Plots(Run #370): Y1+ ADC vs Dis



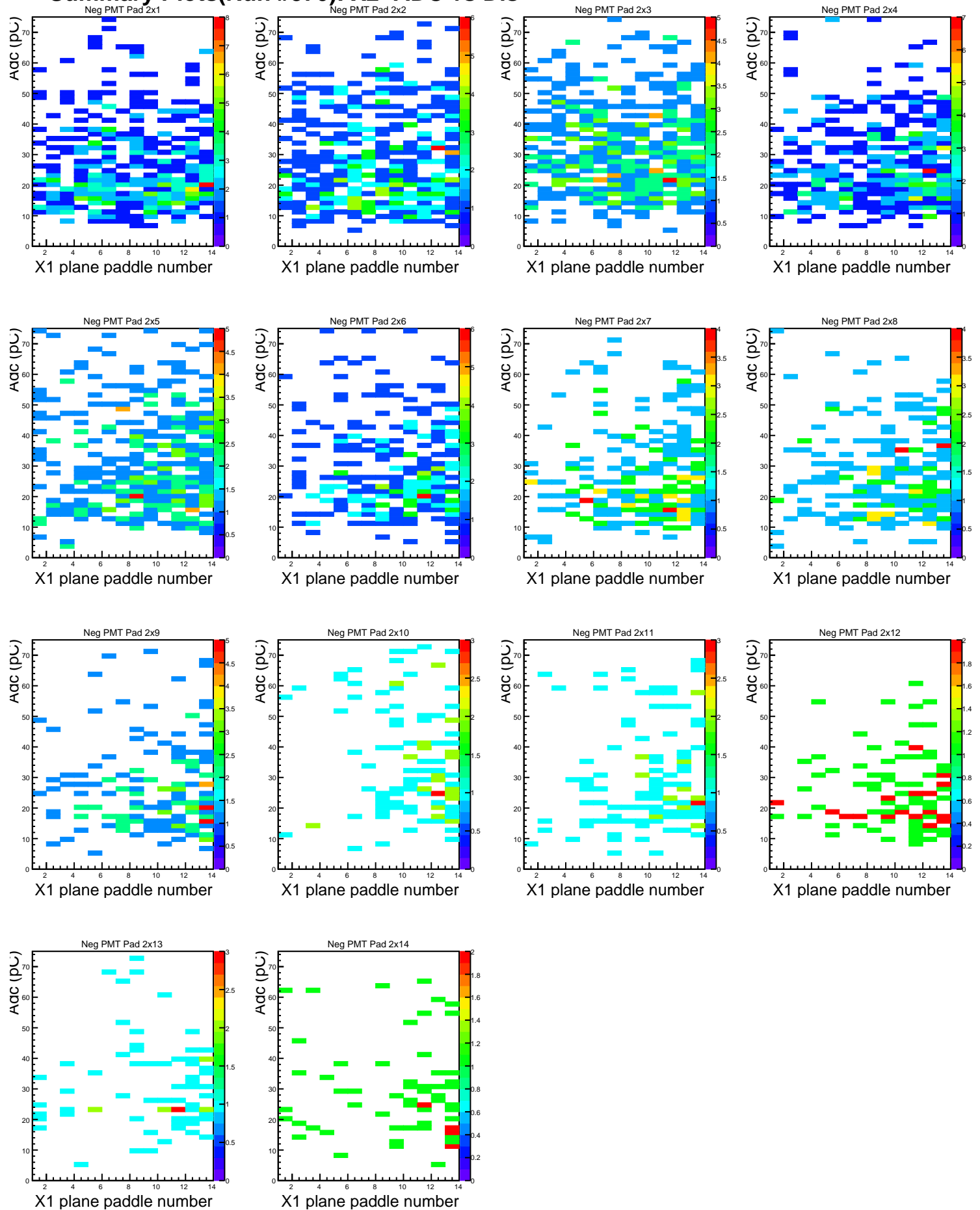
Summary Plots(Run #370): Y1- ADC vs Dis



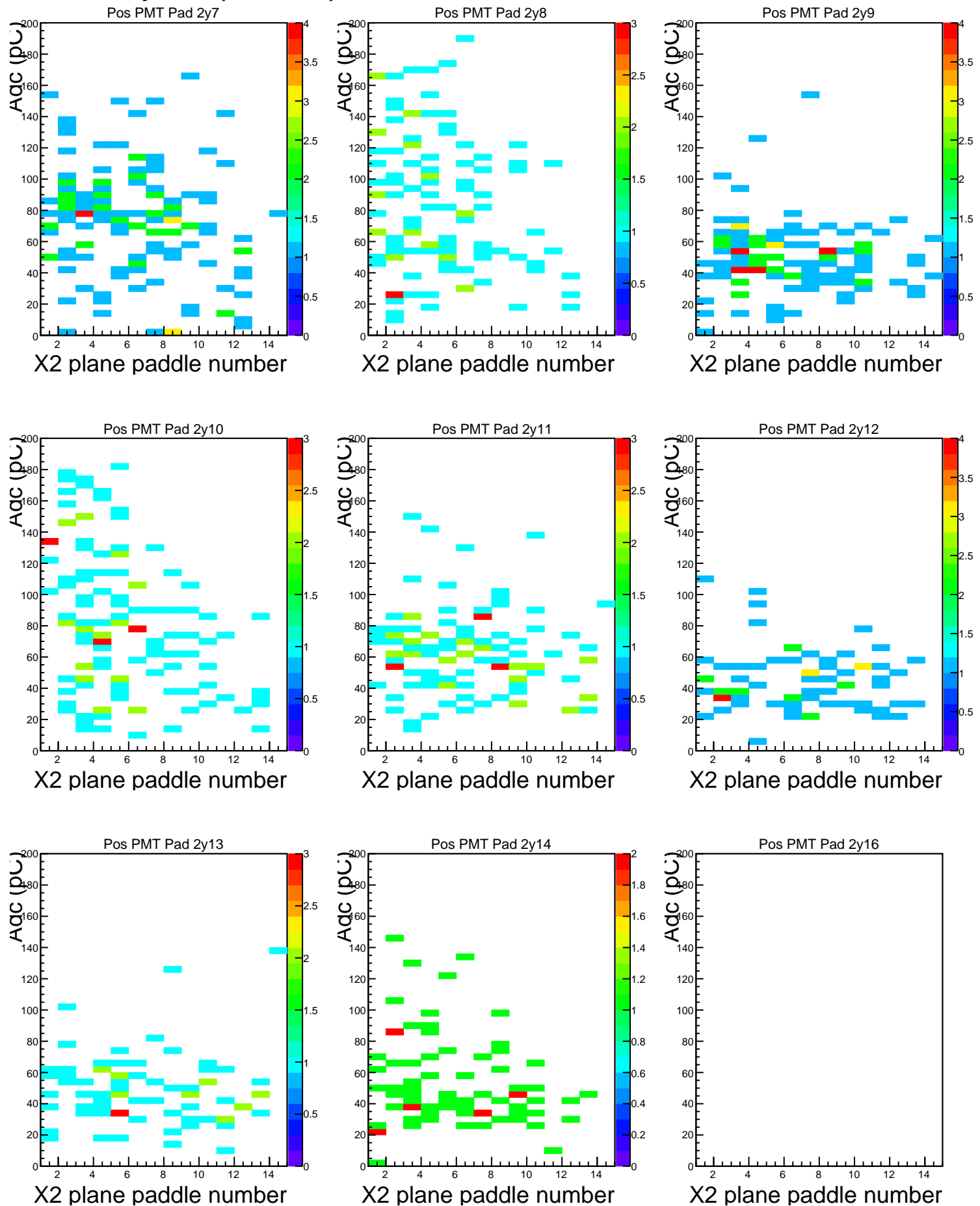
Summary Plots(Run #370): X2+ ADC vs Dis



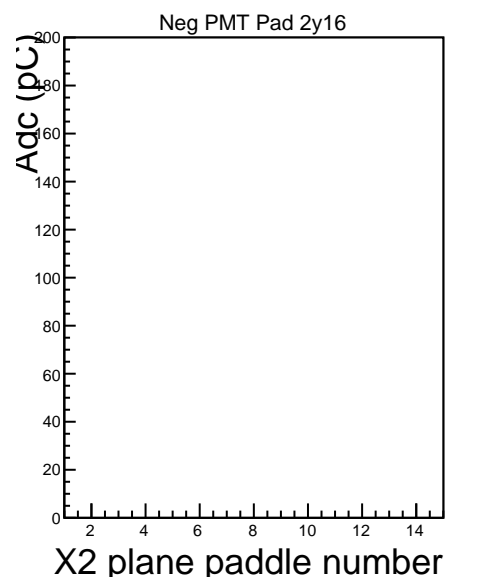
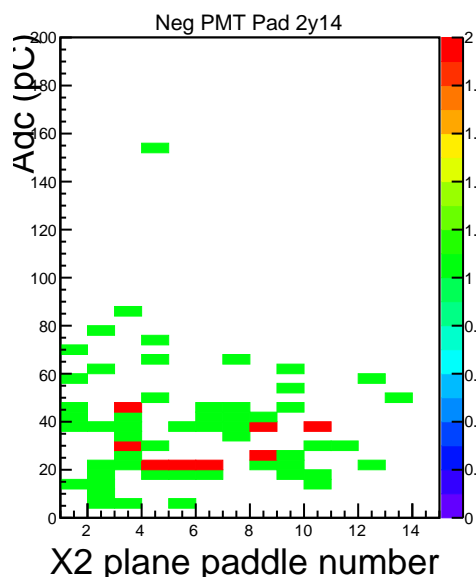
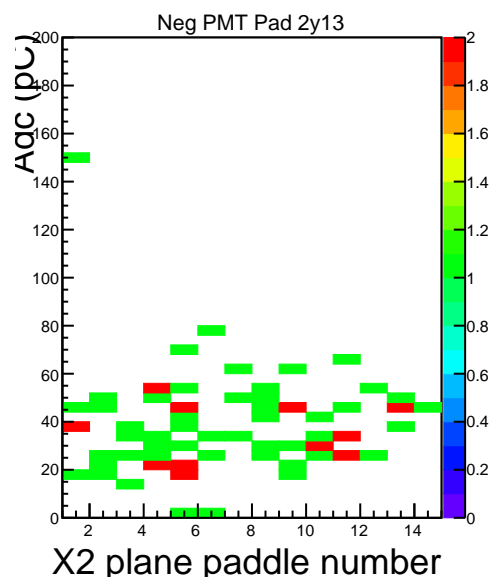
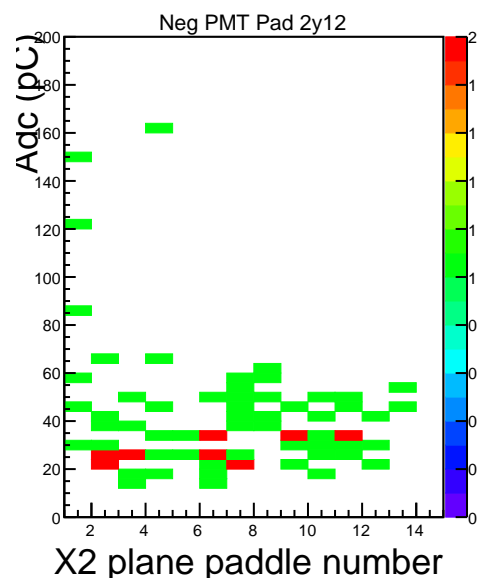
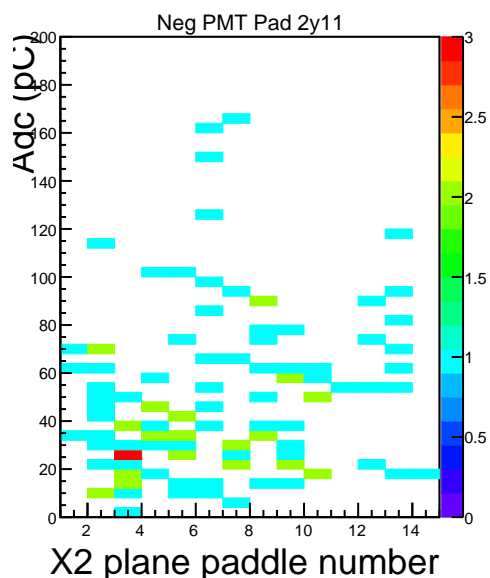
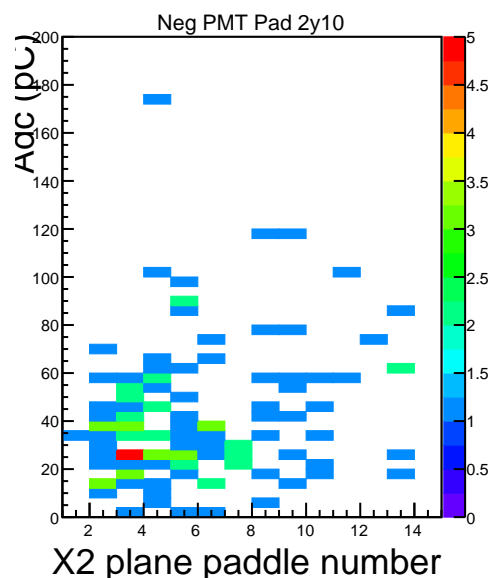
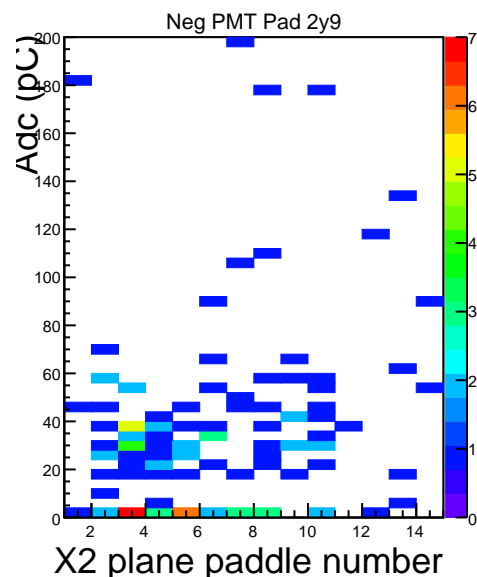
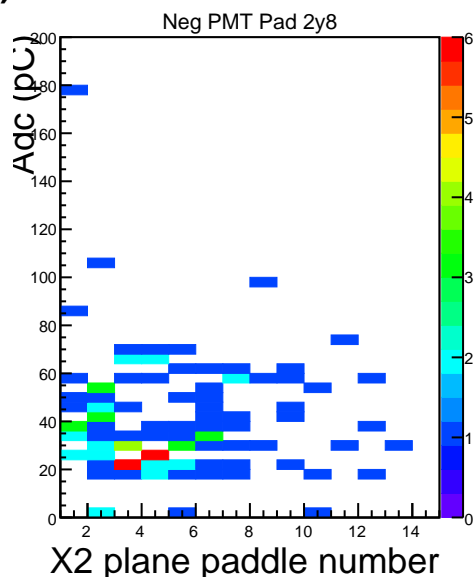
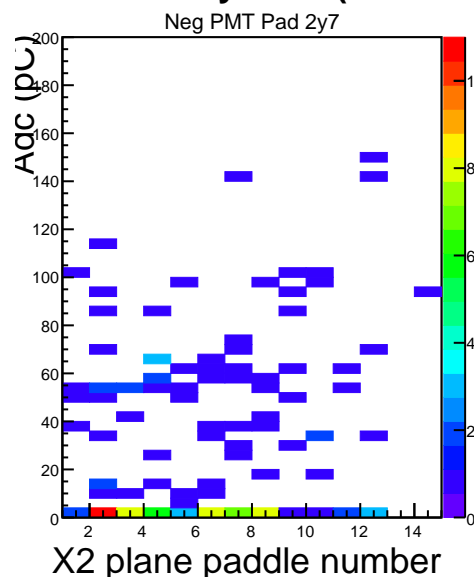
Summary Plots(Run #370): X2- ADC vs Dis



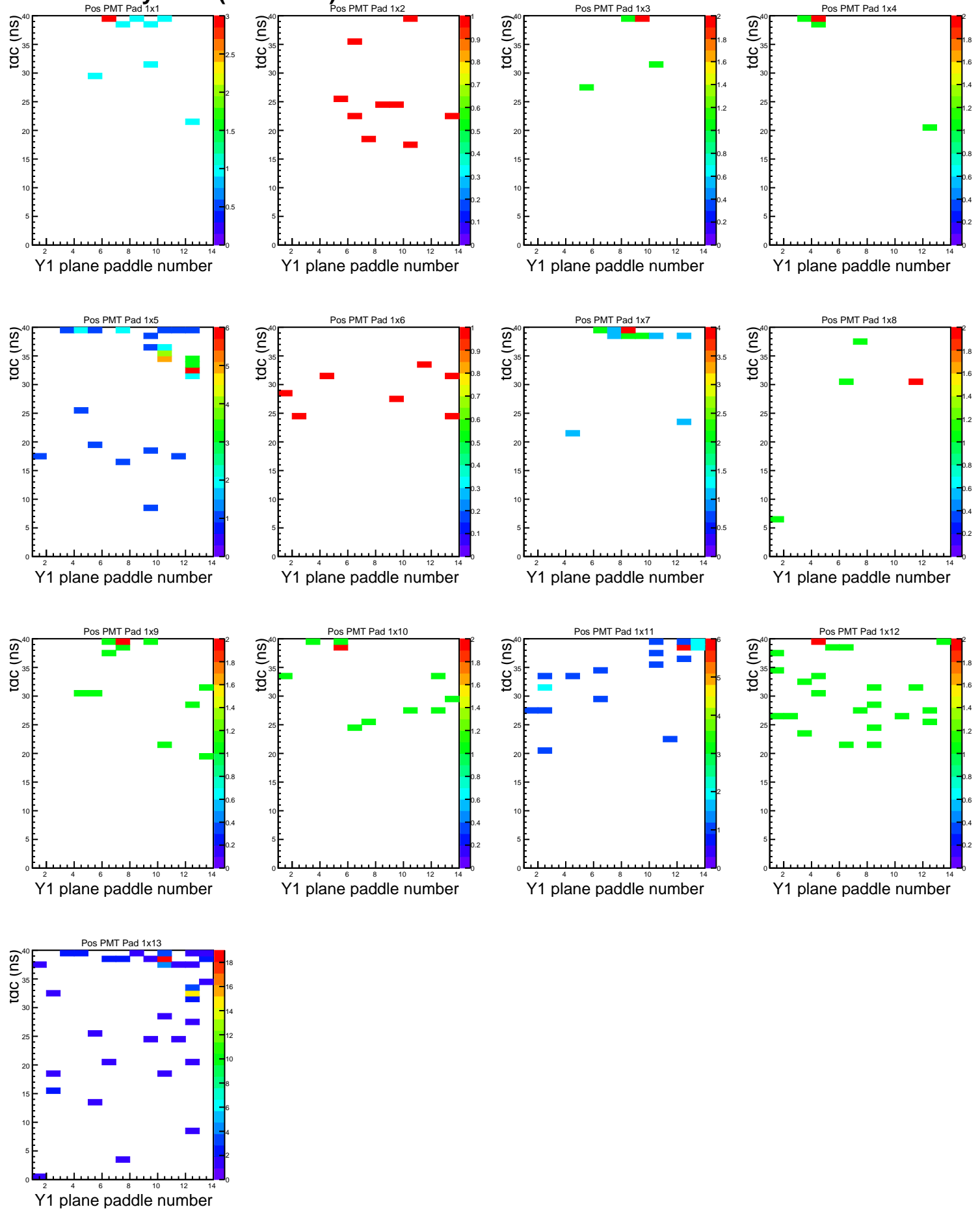
Summary Plots(Run #370): Y2+ ADC vs Dis



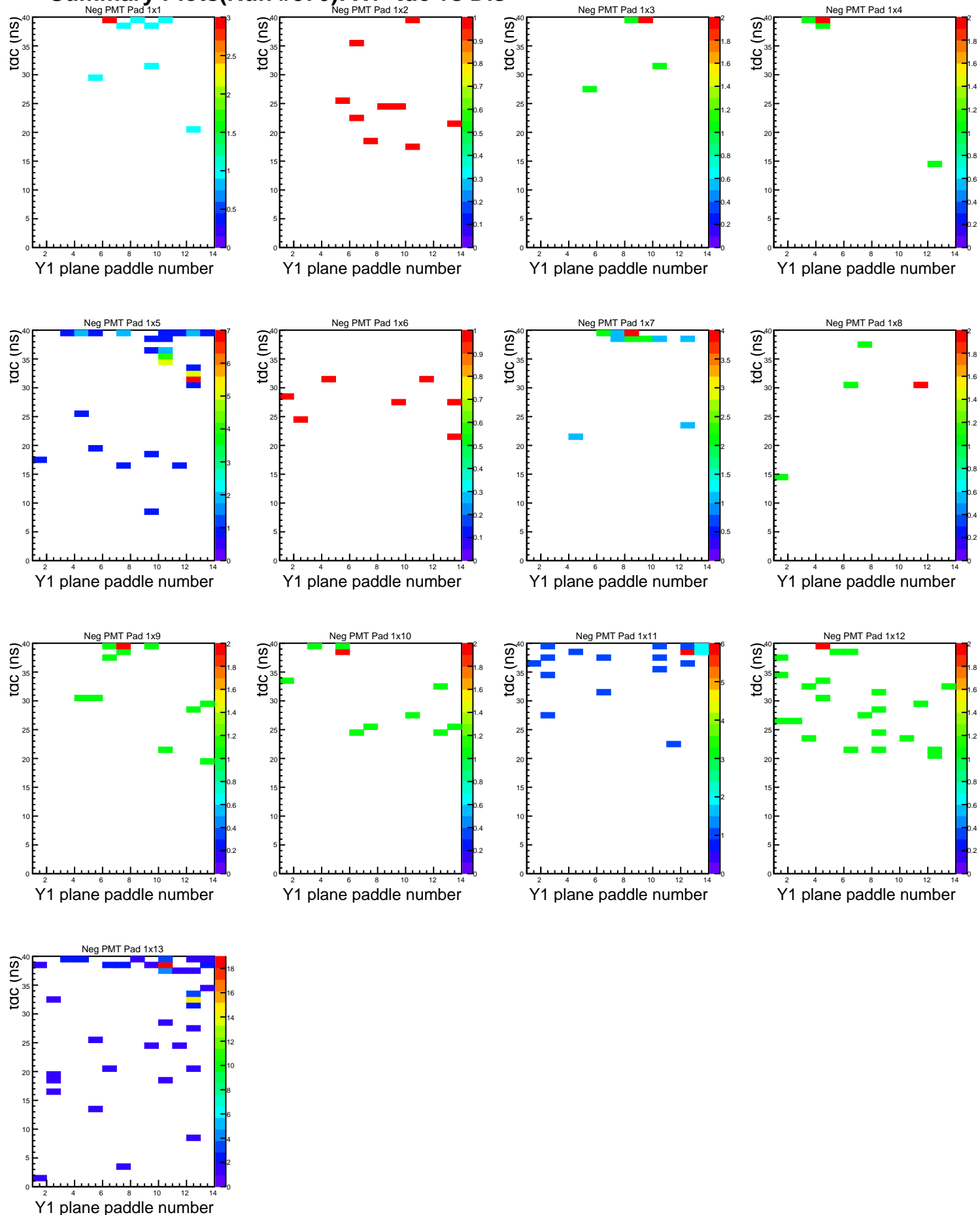
Summary Plots(Run #370): Y2- ADC vs Dis



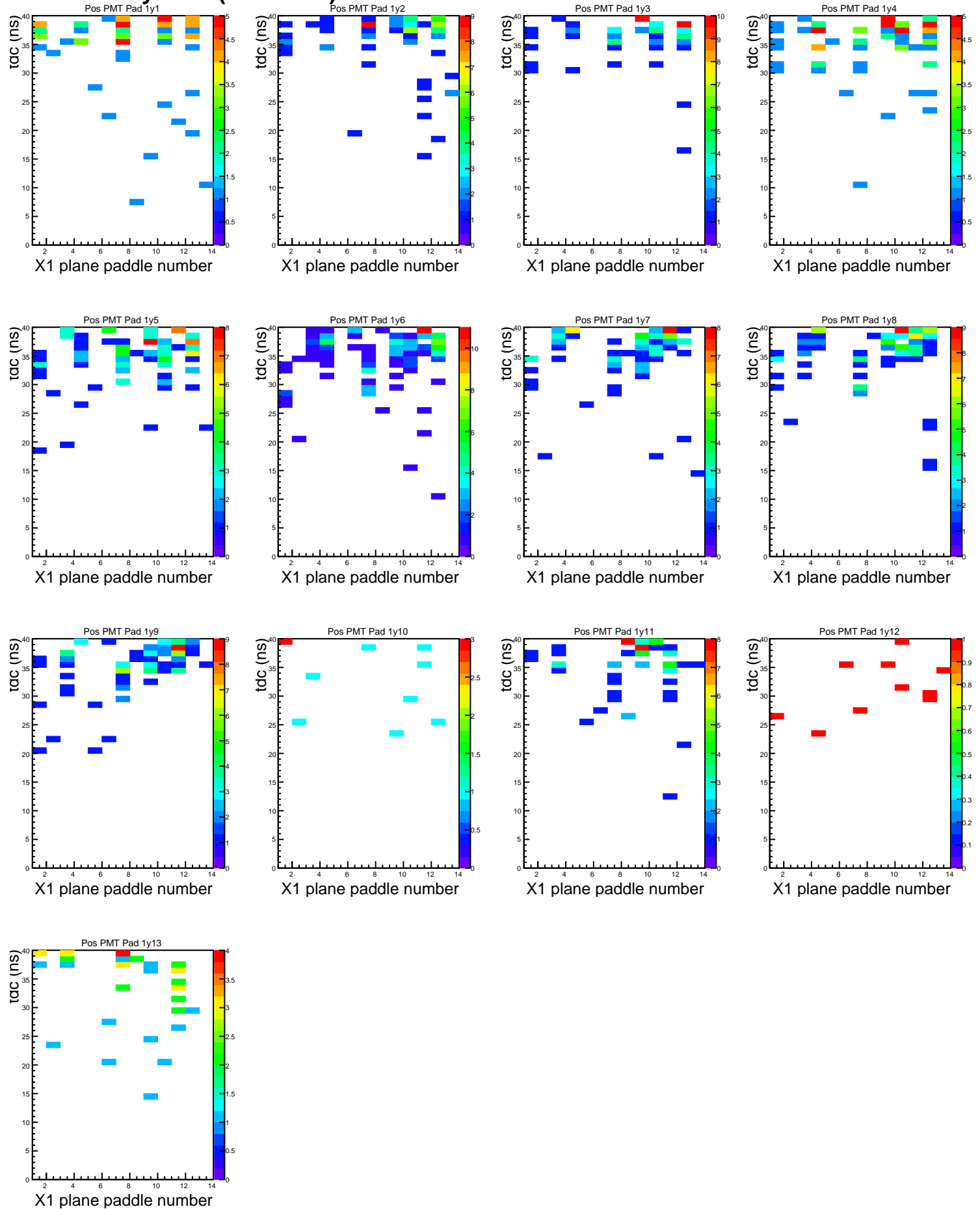
Summary Plots(Run #370): X1+ tdc vs Dis



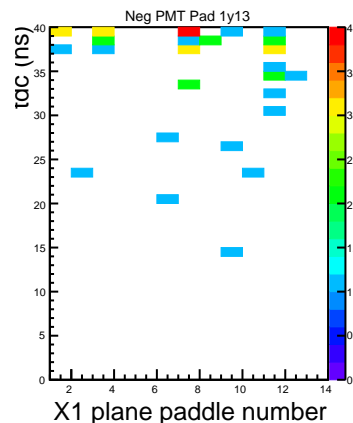
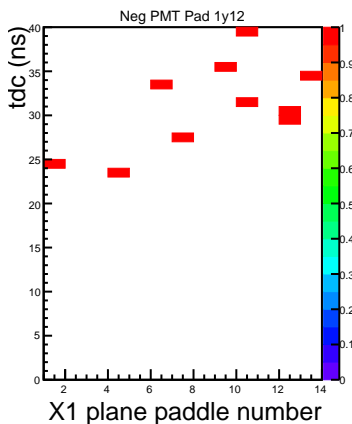
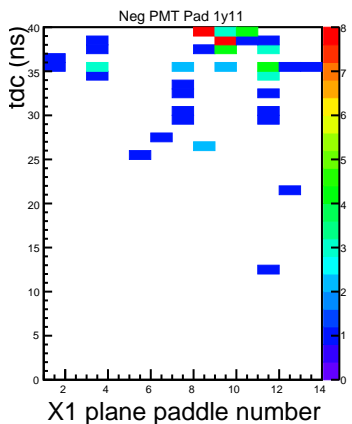
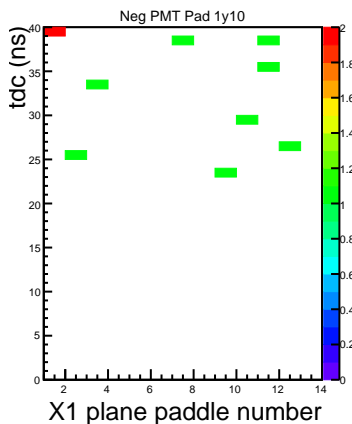
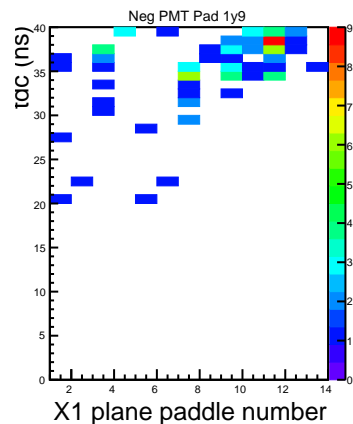
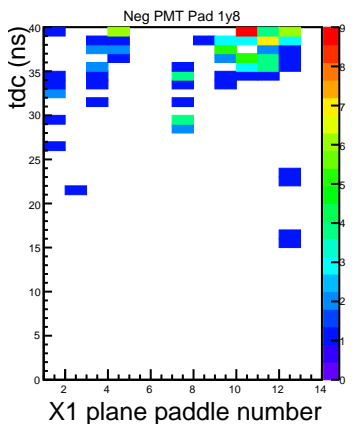
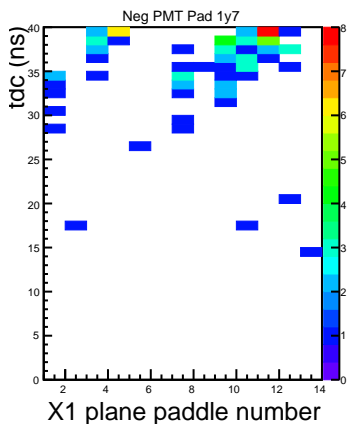
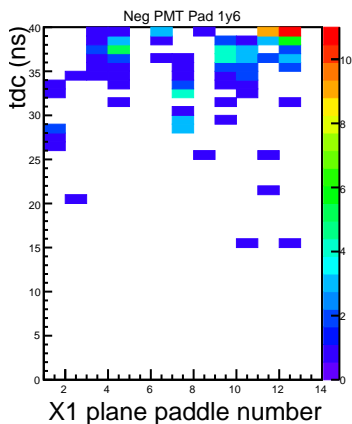
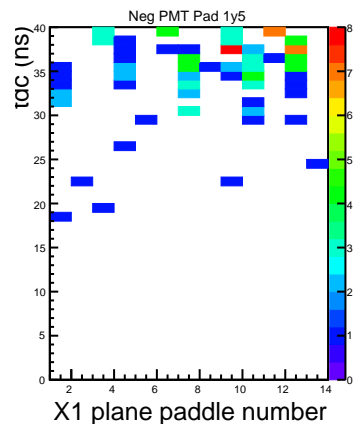
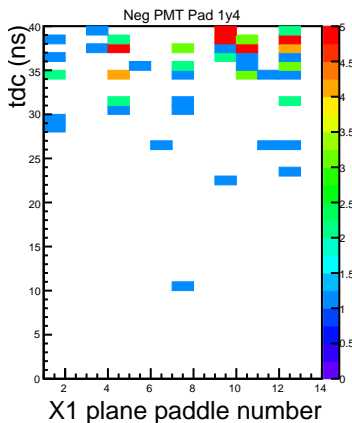
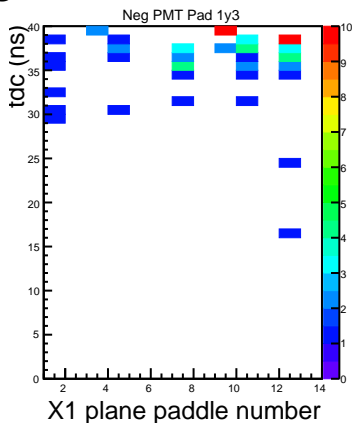
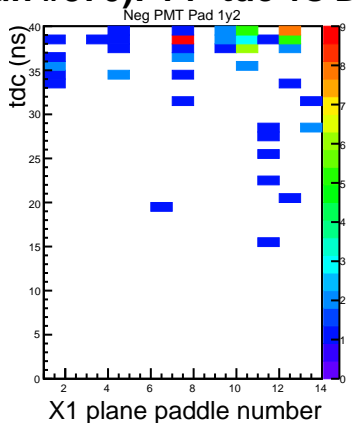
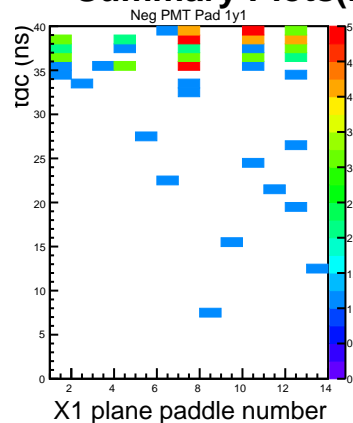
Summary Plots(Run #370): X1- tdc vs Dis



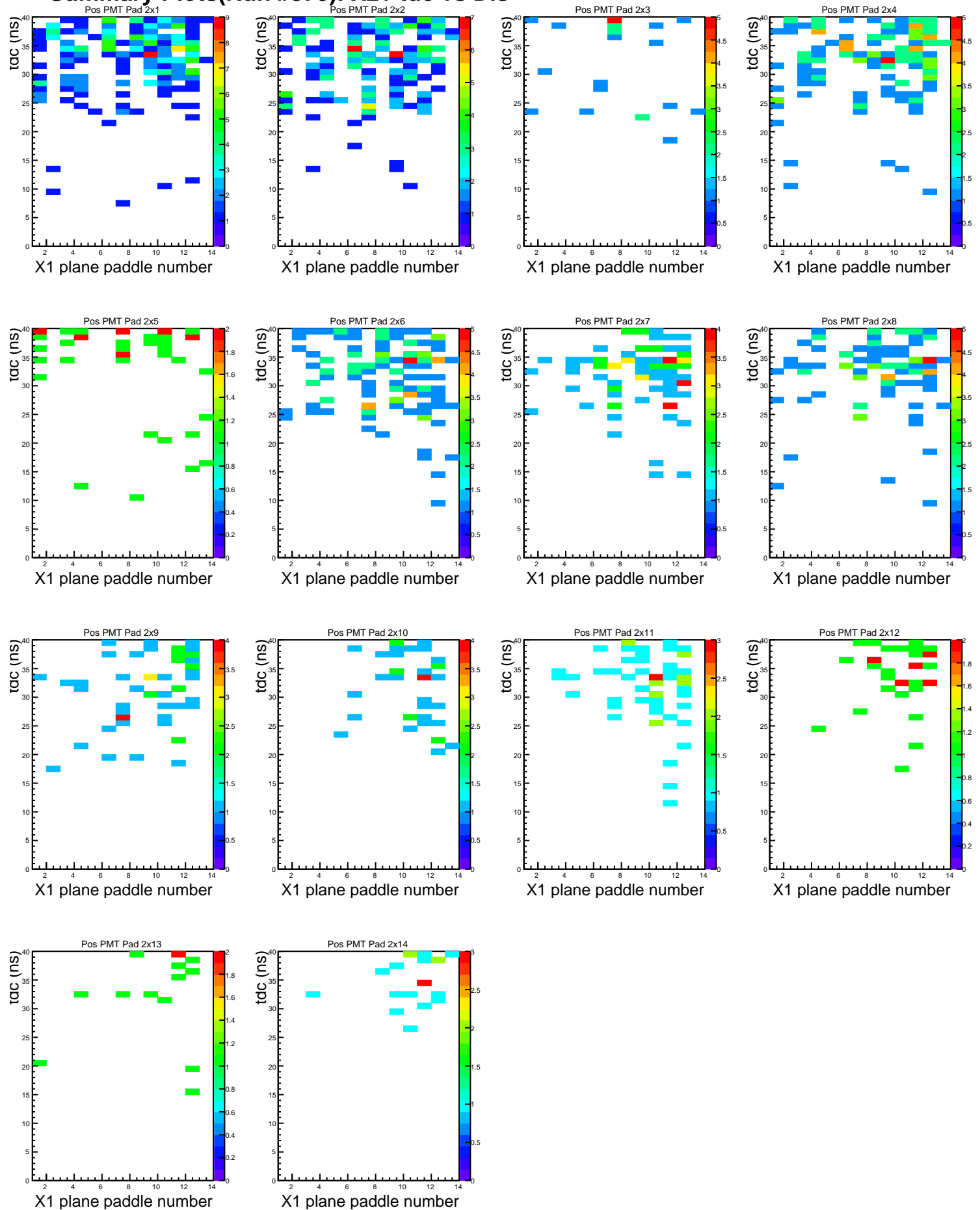
Summary Plots(Run #370): Y1+ tdc vs Dis



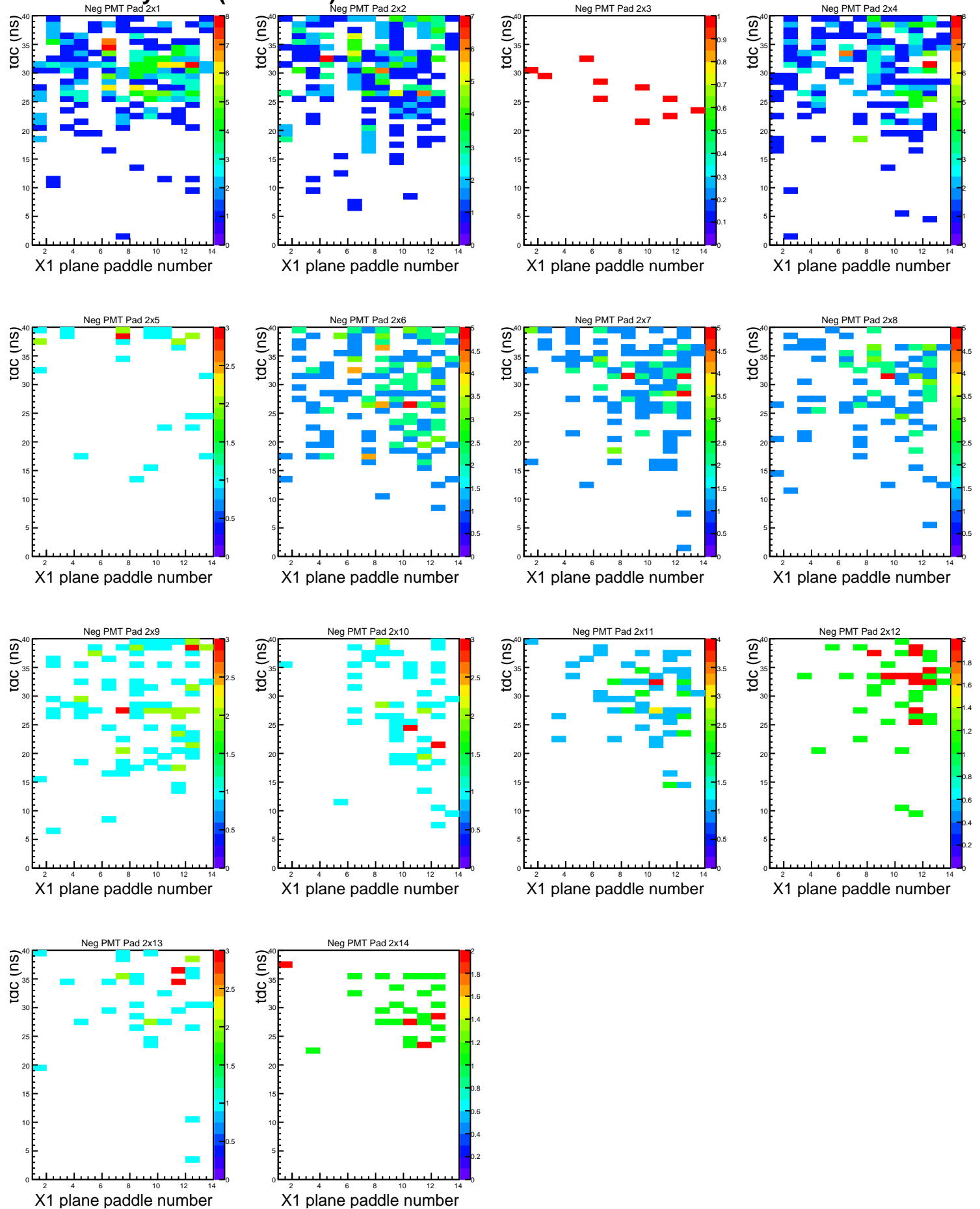
Summary Plots (Run #370): Y1- tdc vs Dis



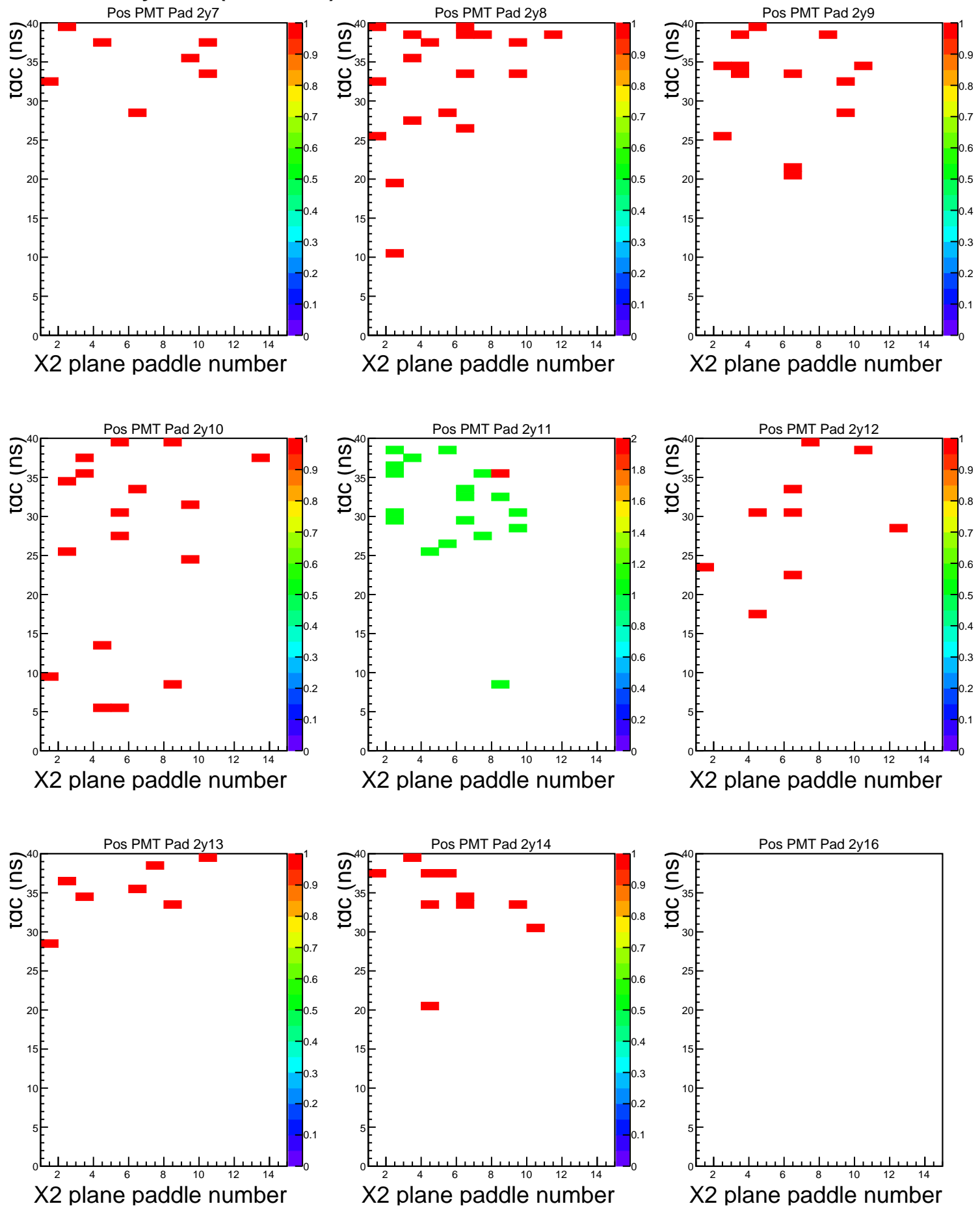
Summary Plots (Run #370): X2+ tdc vs Dis



Summary Plots(Run #370): X2- tdc vs Dis



Summary Plots(Run #370): Y2+ tdc vs Dis



Summary Plots(Run #370): Y2- tdc vs Dis

