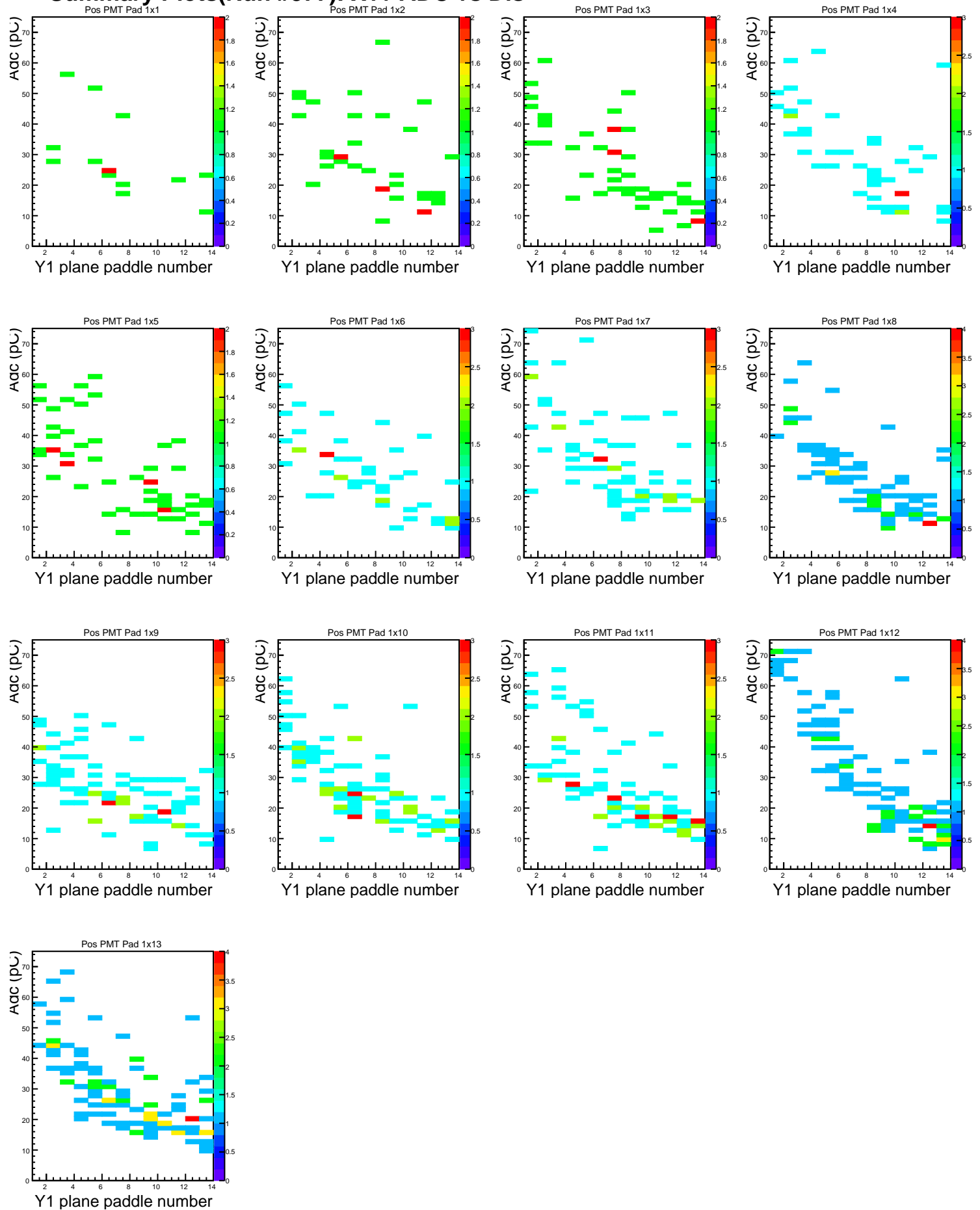
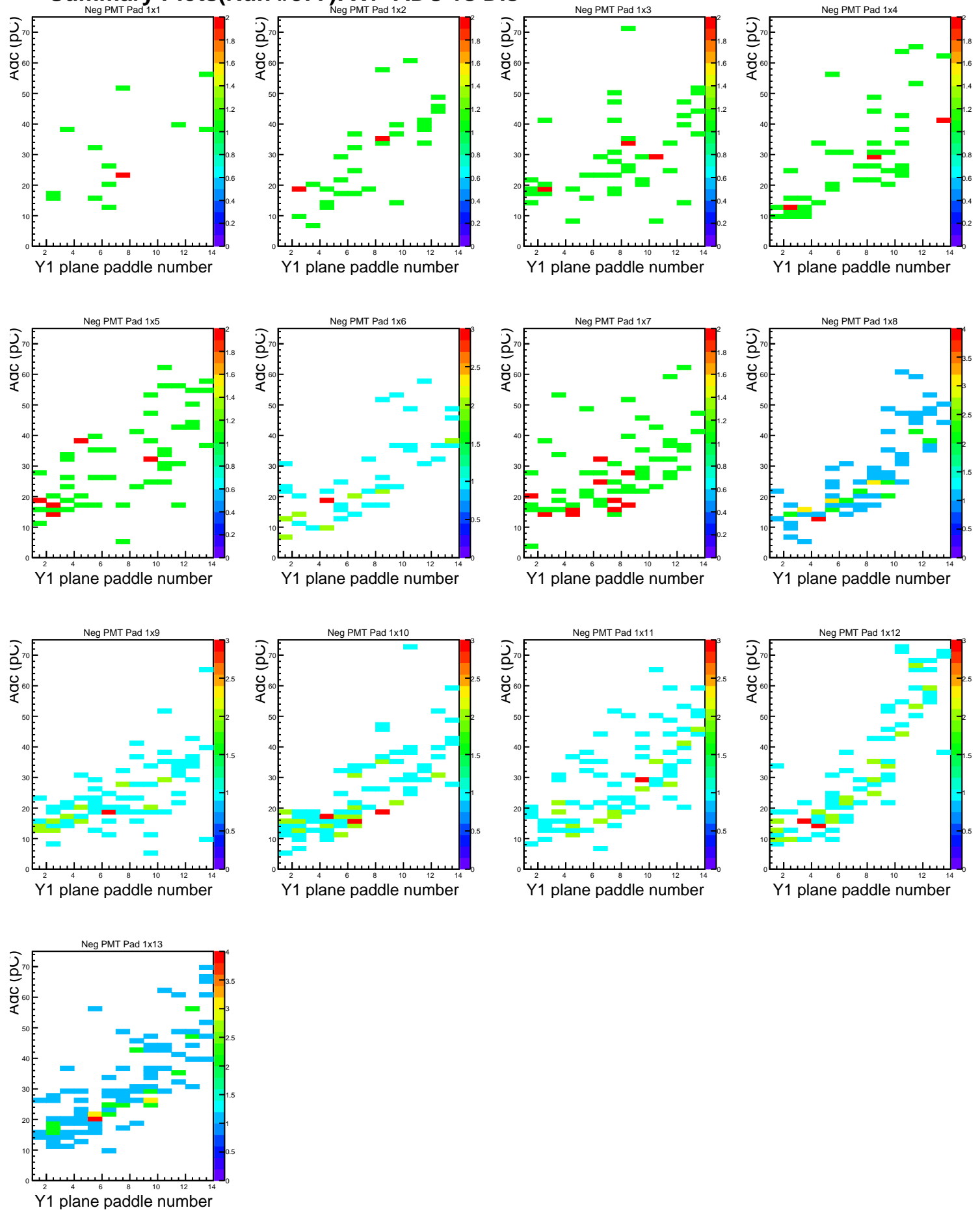


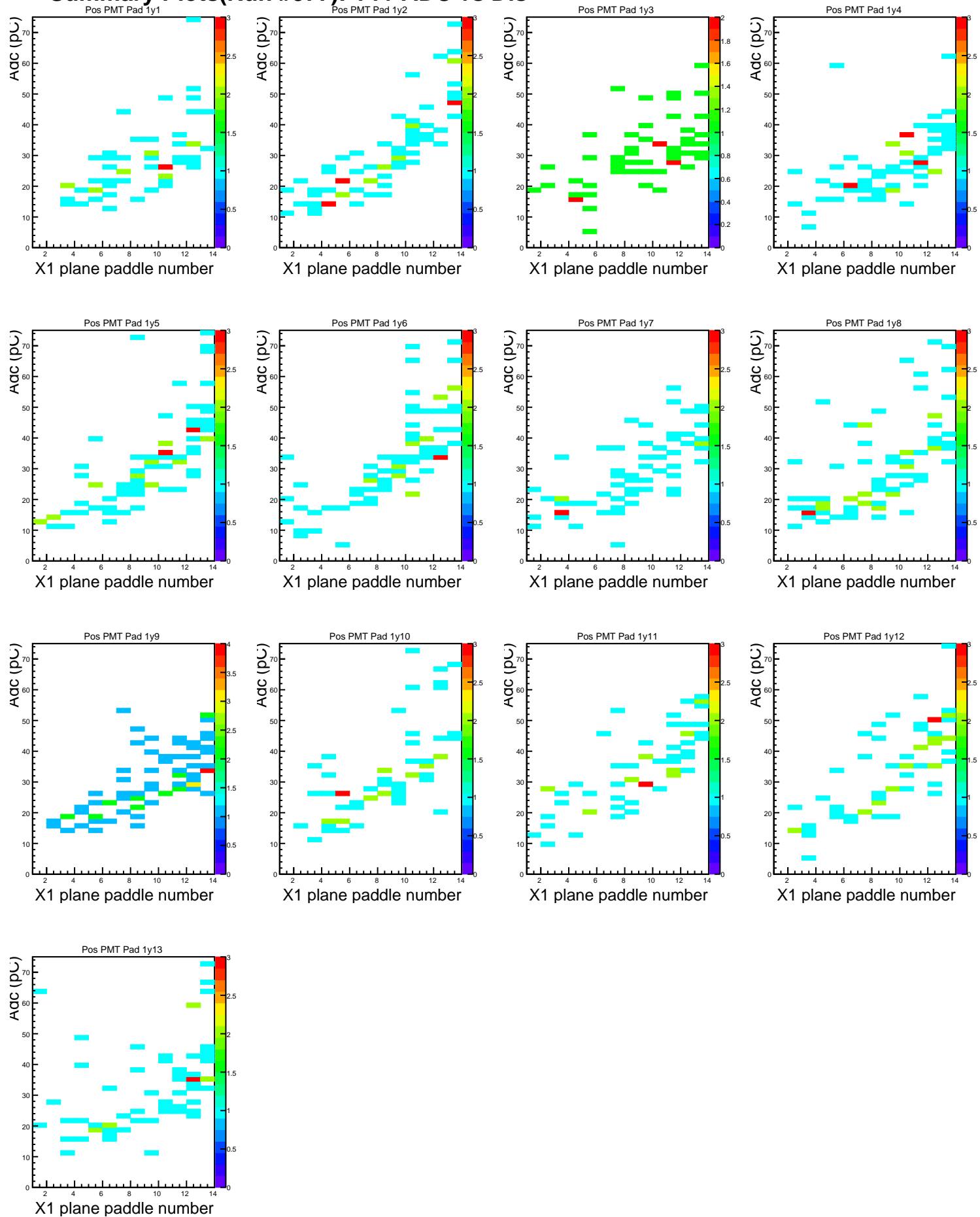
Summary Plots(Run #377): X1+ ADC vs Dis



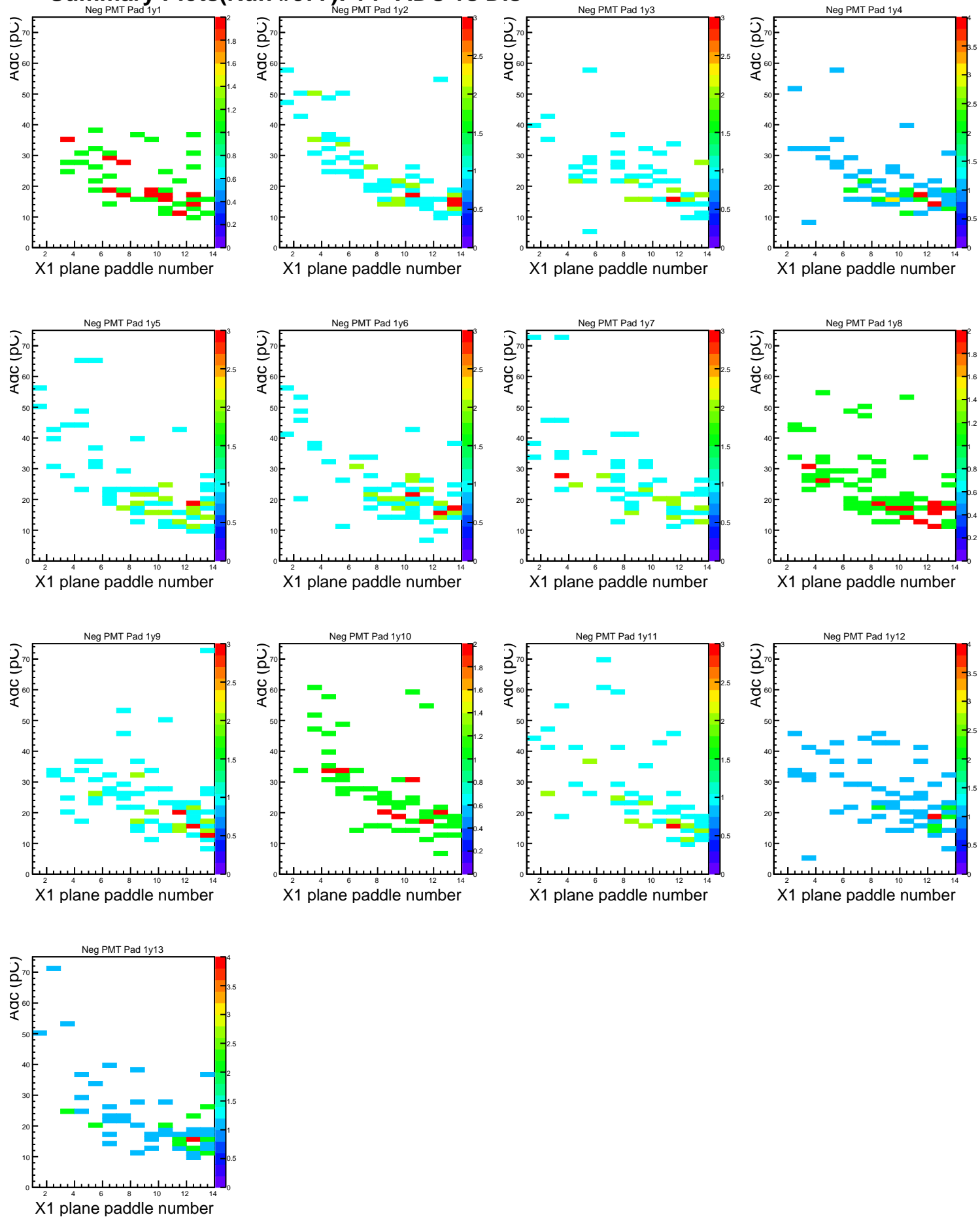
Summary Plots(Run #377): X1- ADC vs Dis



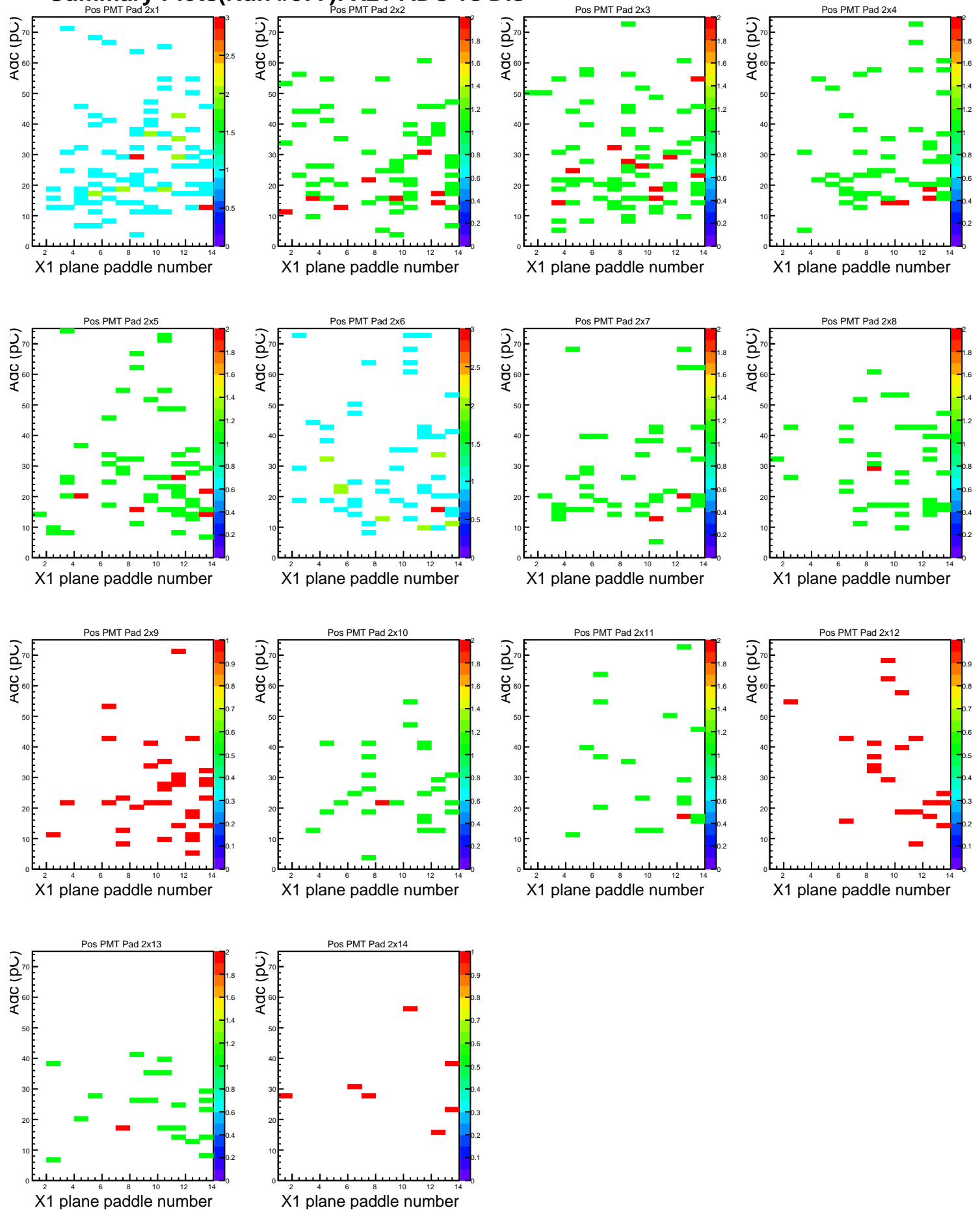
Summary Plots(Run #377): Y1+ ADC vs Dis



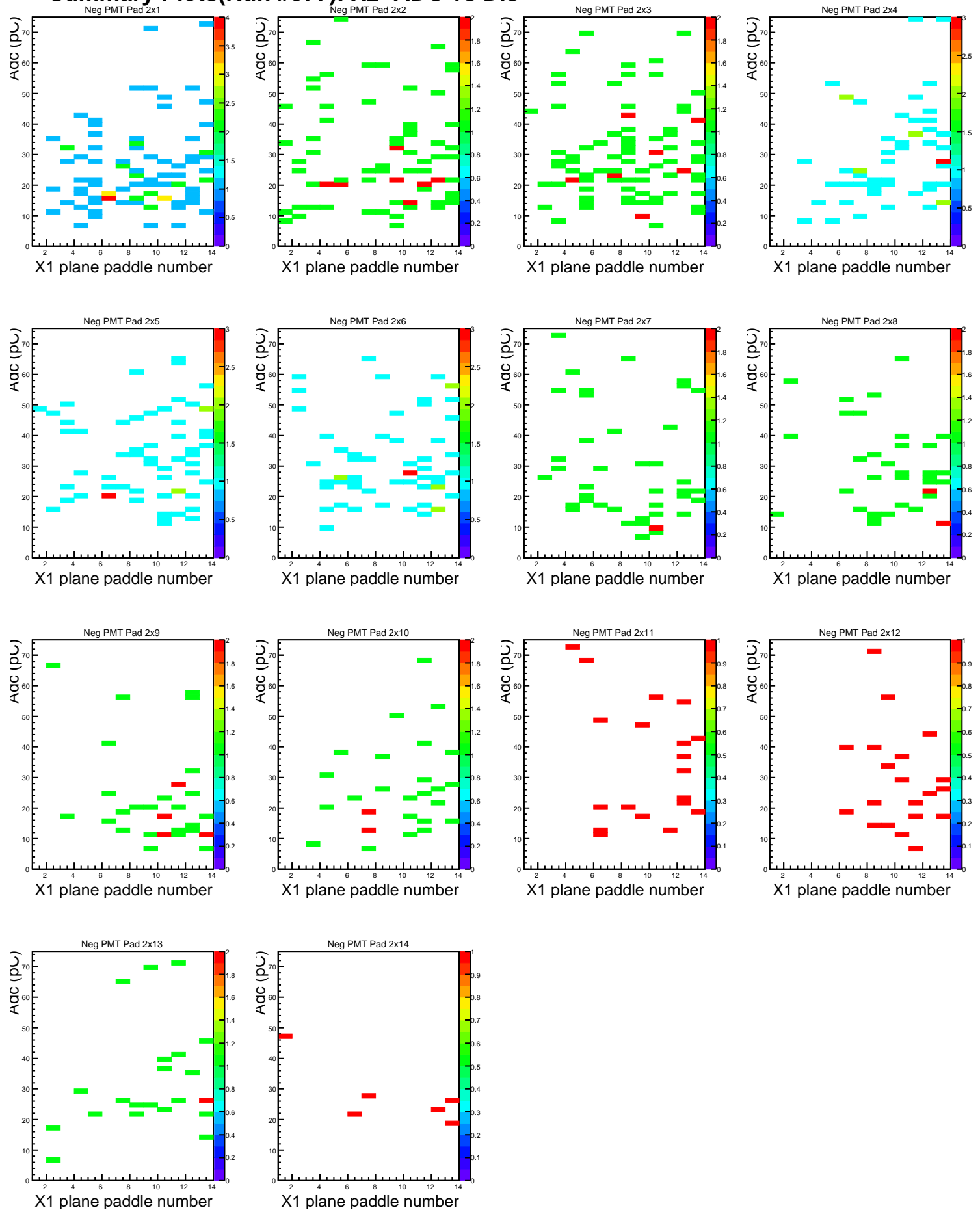
Summary Plots(Run #377): Y1- ADC vs Dis



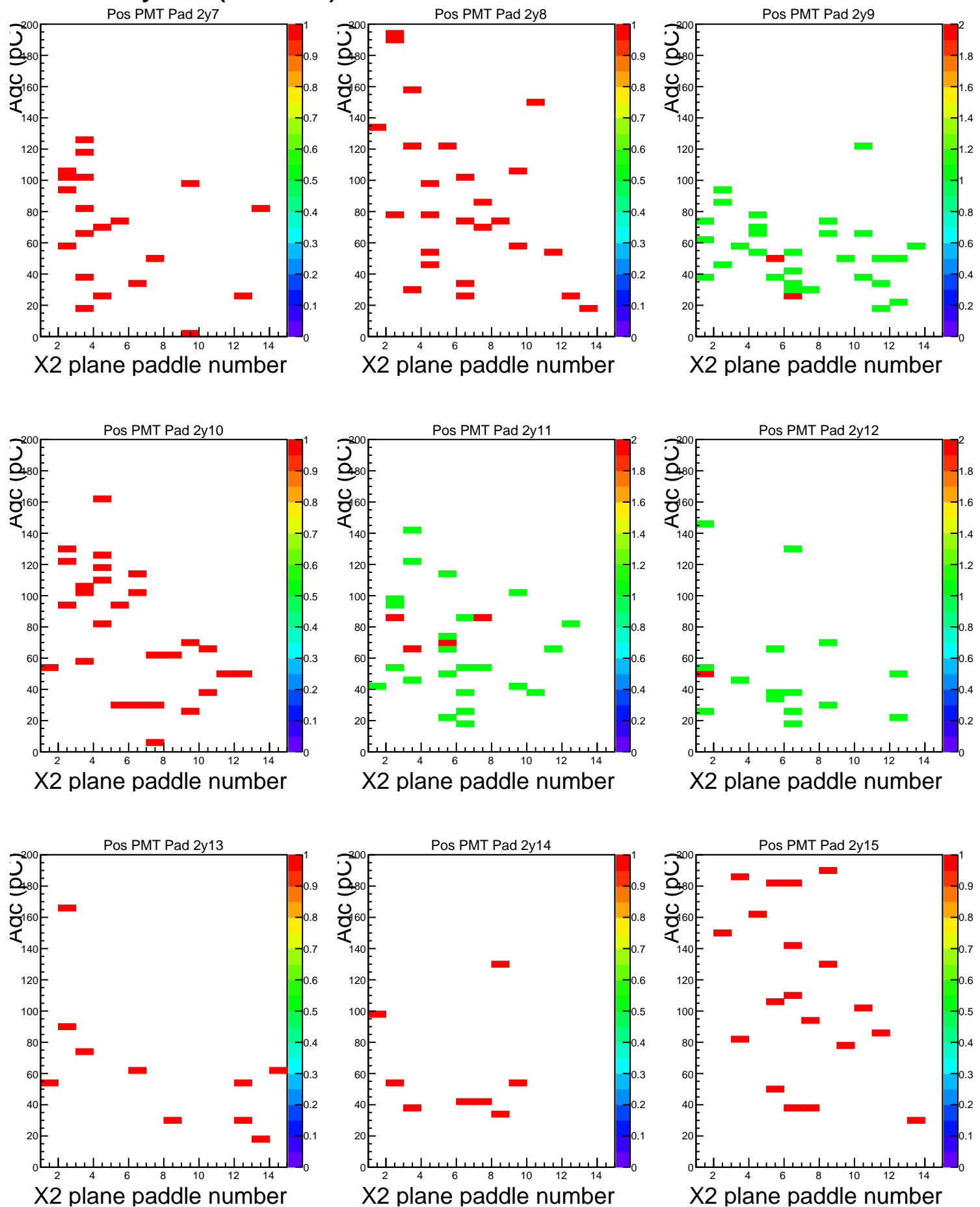
Summary Plots(Run #377): X2+ ADC vs Dis



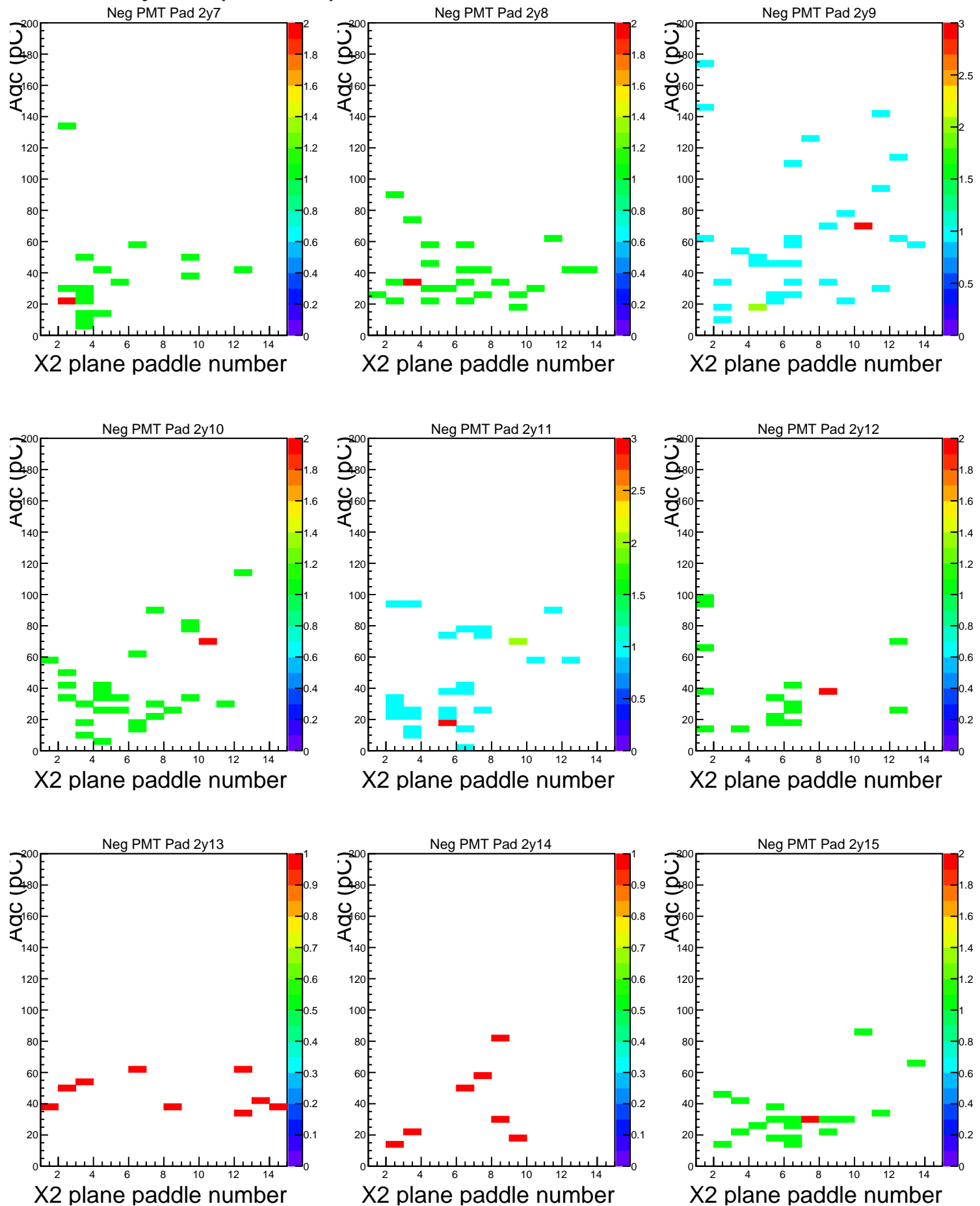
Summary Plots(Run #377): X2- ADC vs Dis



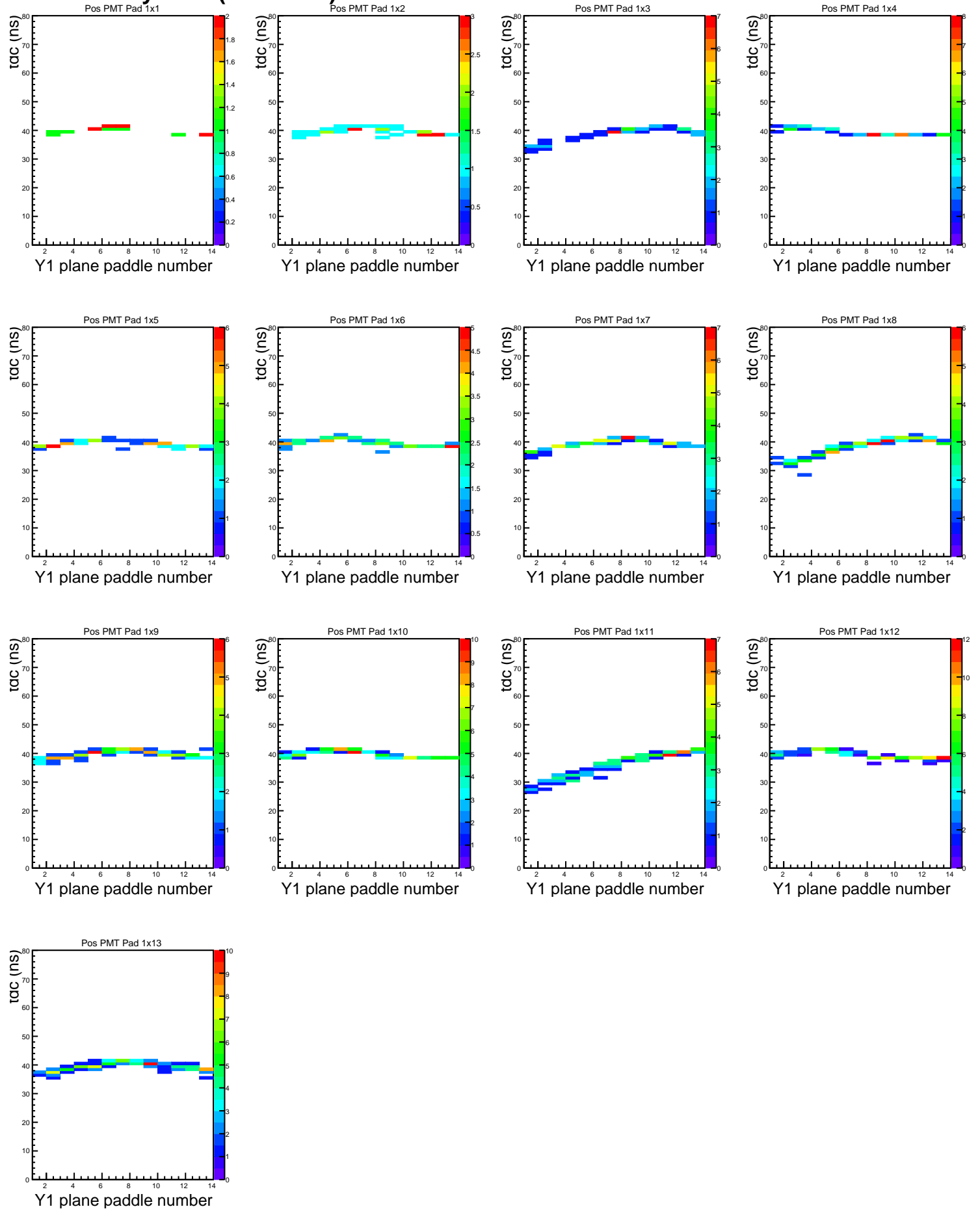
Summary Plots(Run #377): Y2+ ADC vs Dis



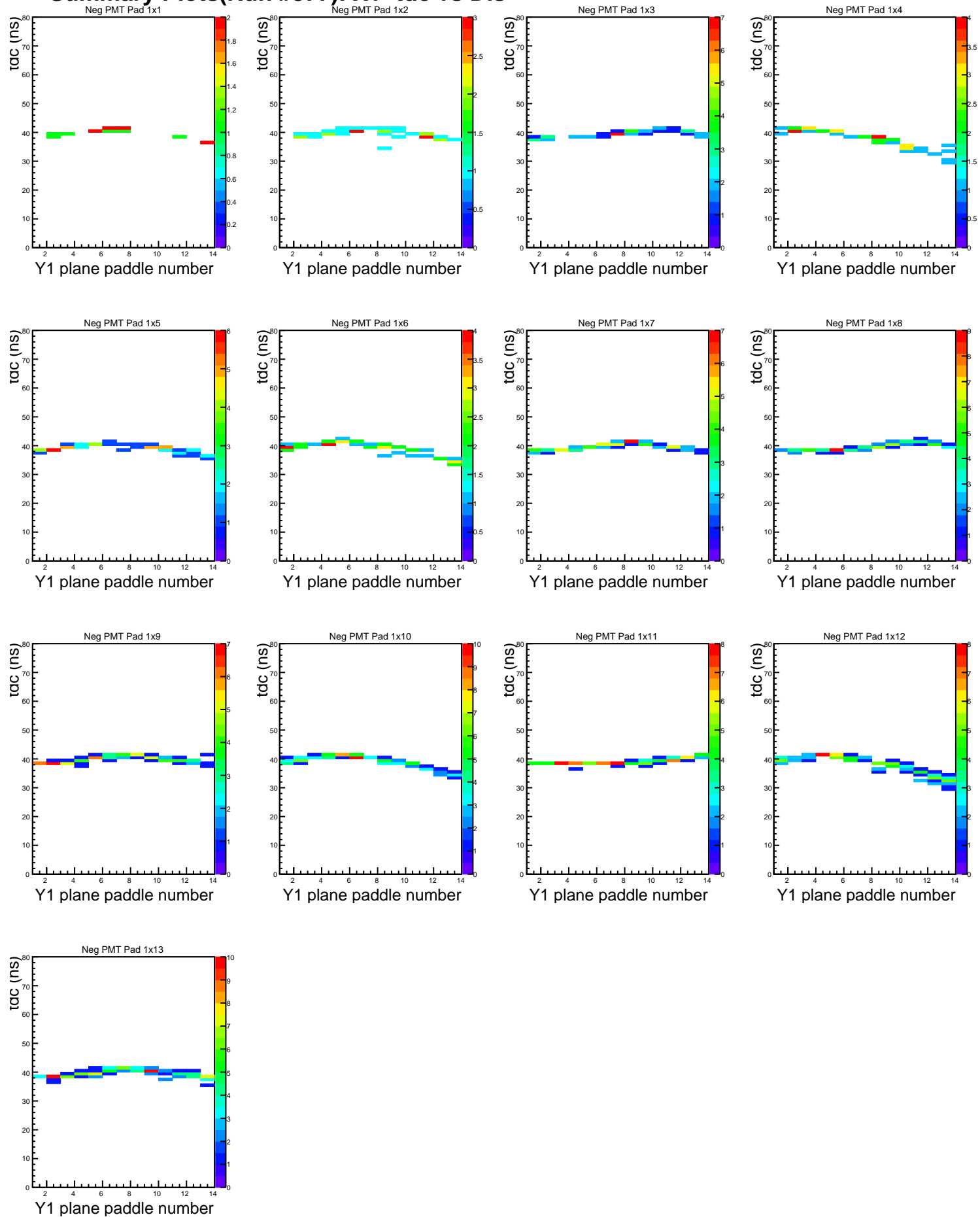
Summary Plots(Run #377): Y2- ADC vs Dis



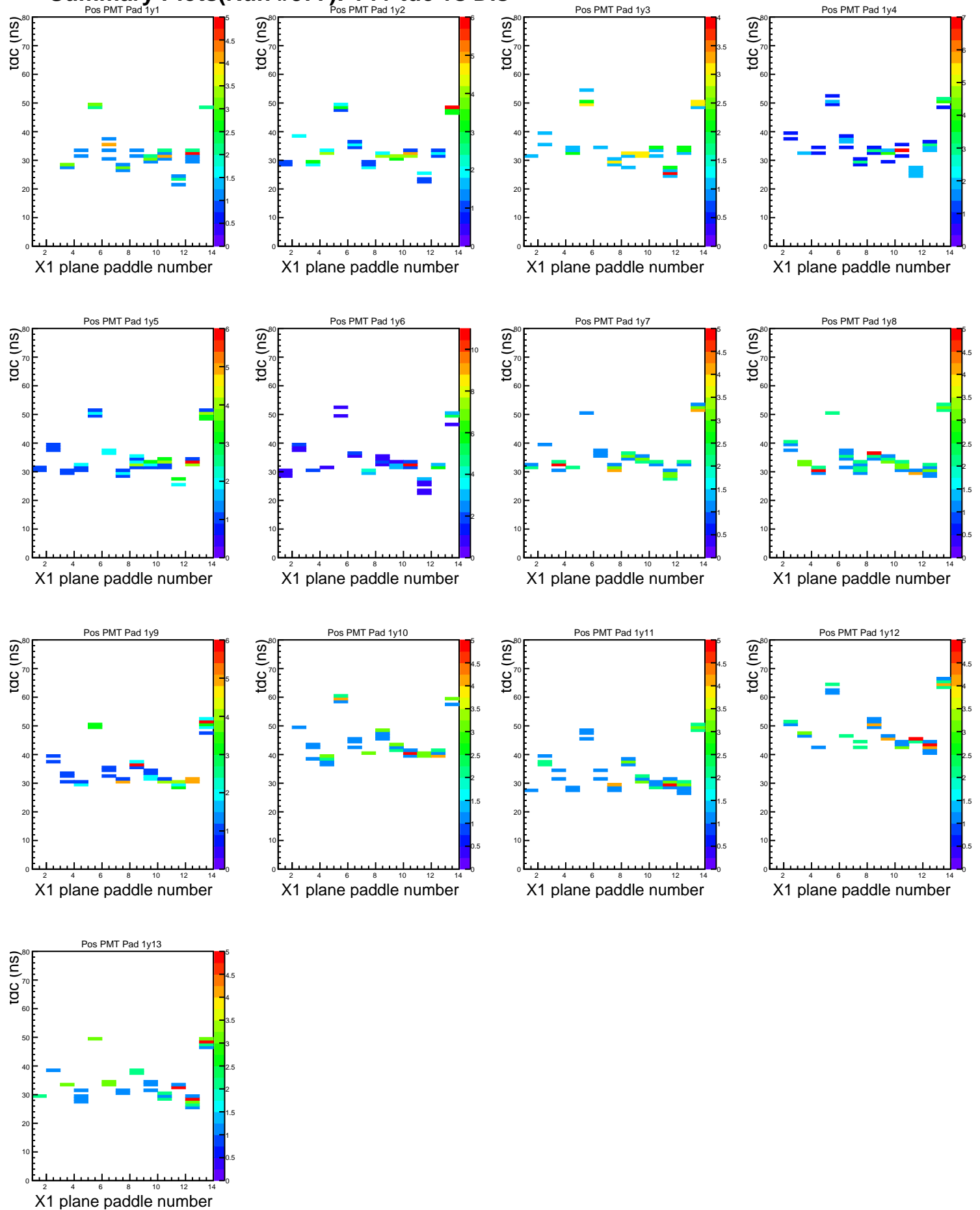
Summary Plots (Run #377): X1+ tdc vs Dis



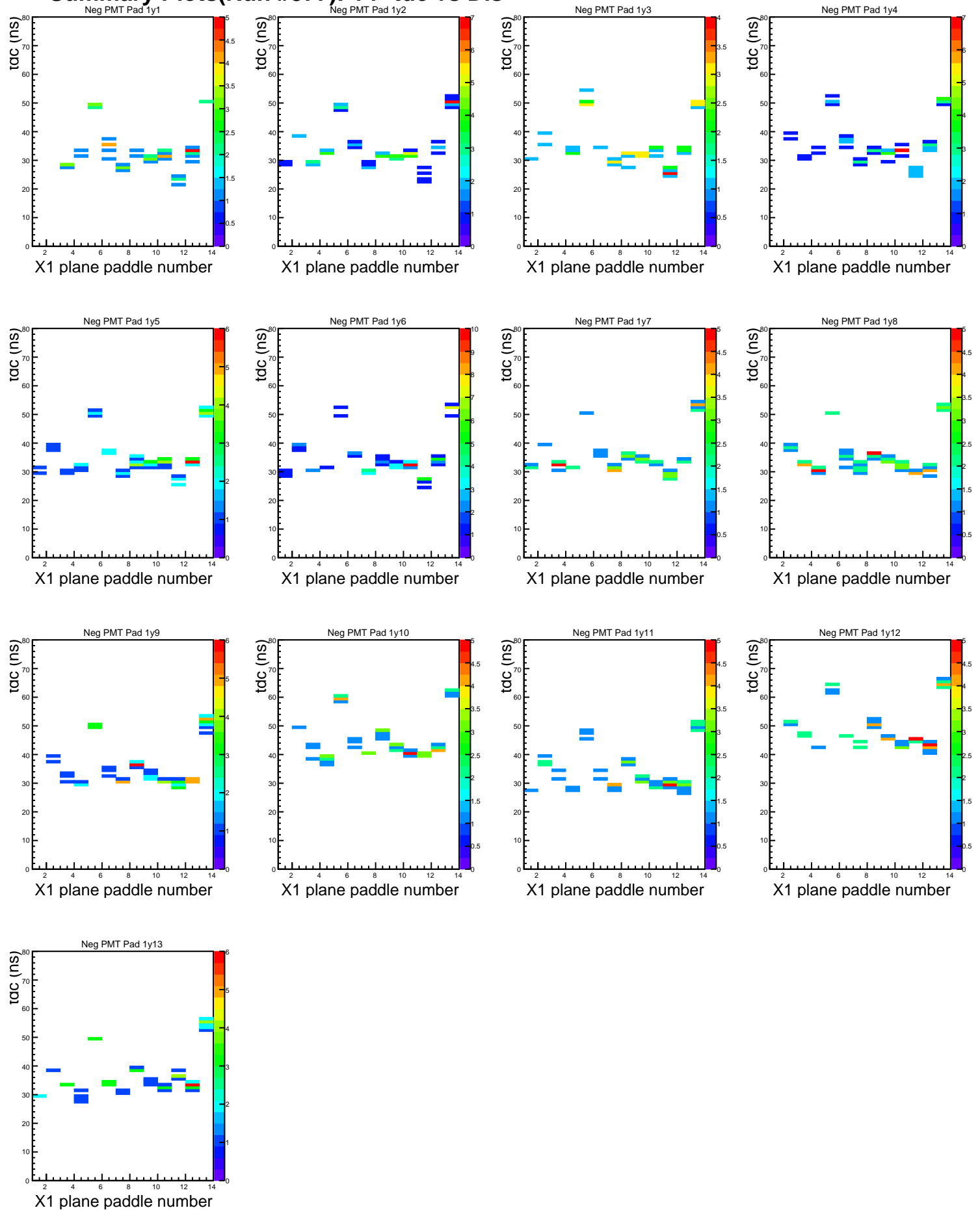
Summary Plots (Run #377): X1- tdc vs Dis



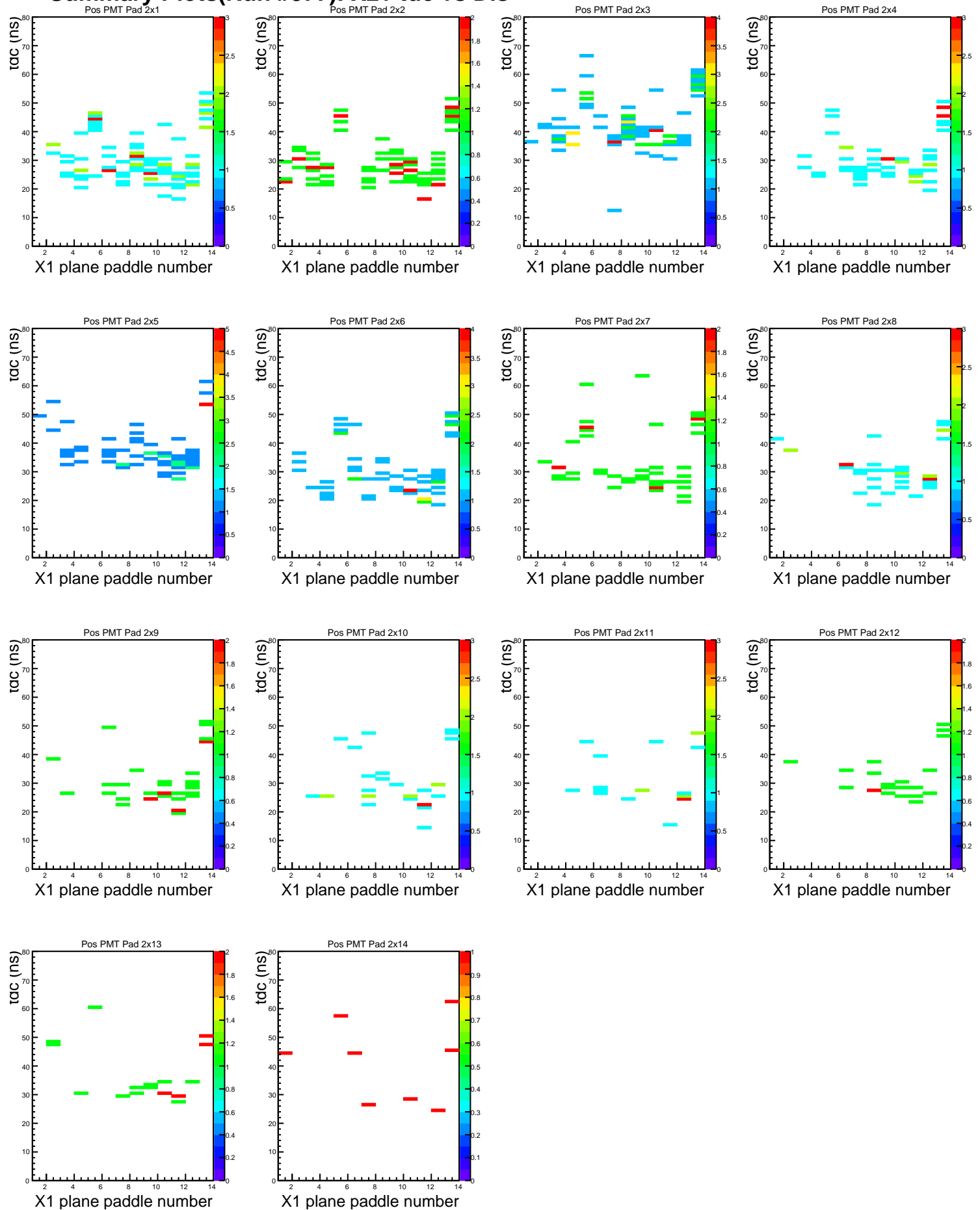
Summary Plots (Run #377): Y1+ tdc vs Dis



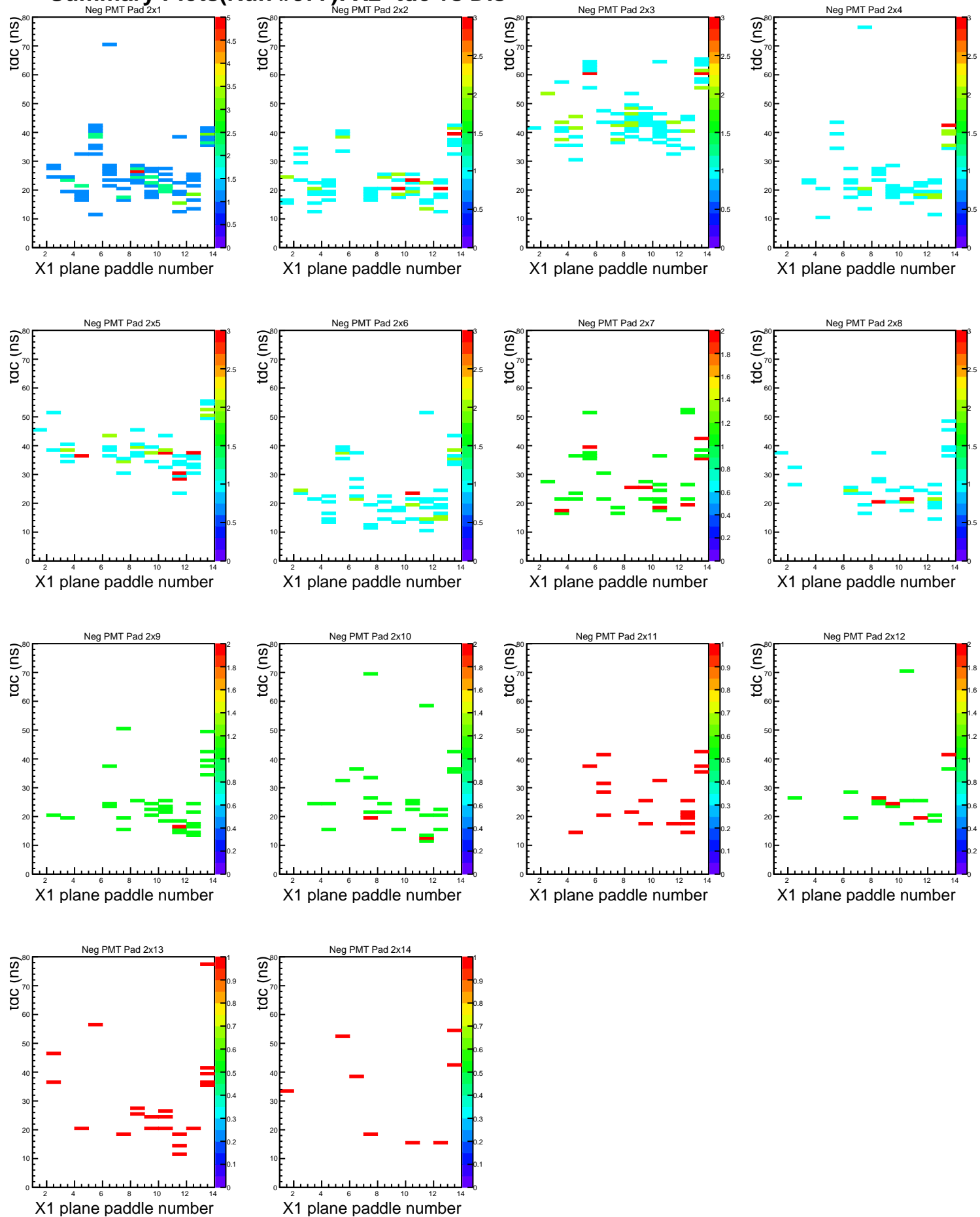
Summary Plots (Run #377): Y1- tdc vs Dis



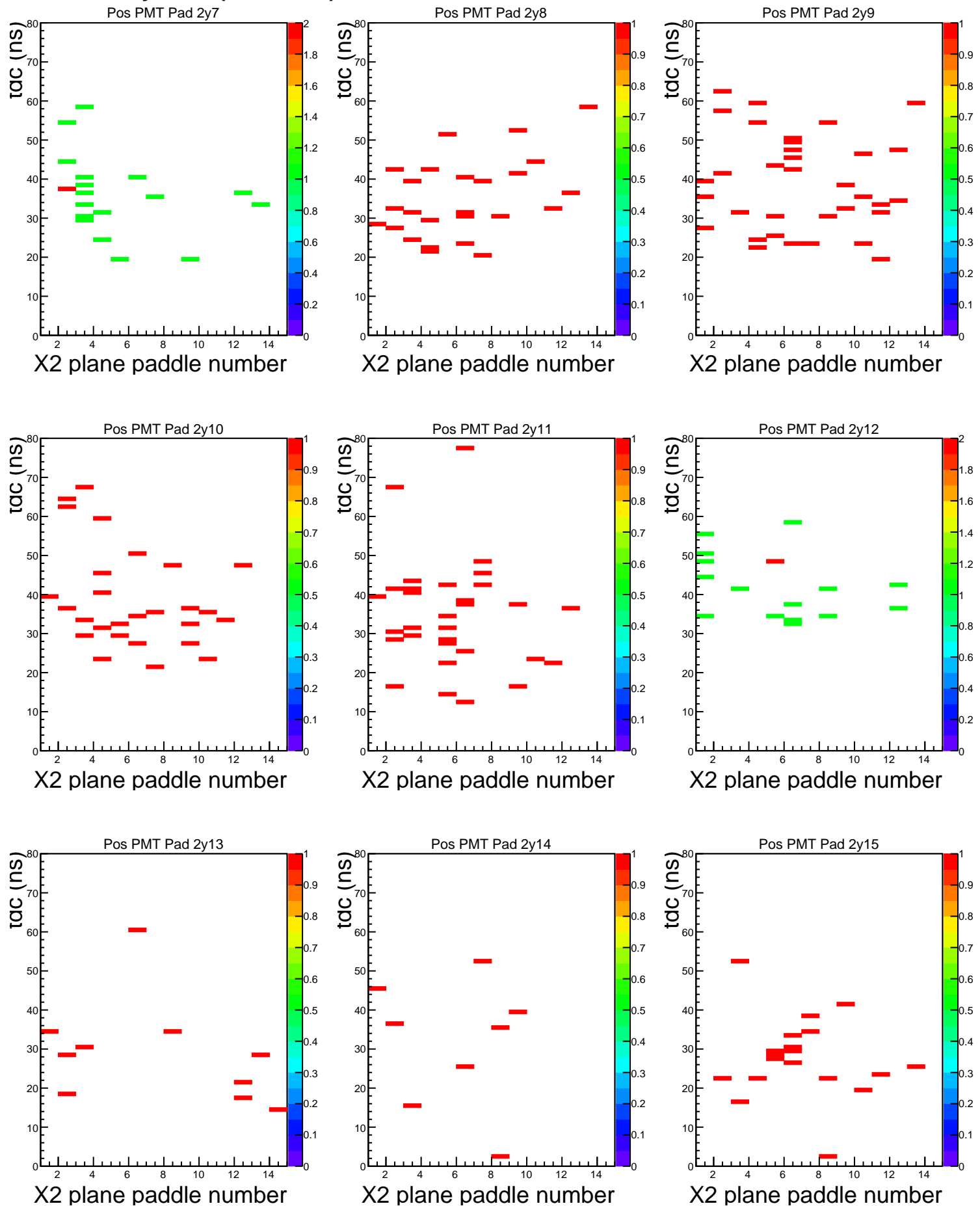
Summary Plots(Run #377): X2+ tdc vs Dis



Summary Plots (Run #377): X2- tdc vs Dis



Summary Plots(Run #377): Y2+ tdc vs Dis



Summary Plots(Run #377): Y2- tdc vs Dis

