

# EEES - UNIBO

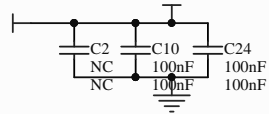
## Wolf-Man-Interface (WMI)

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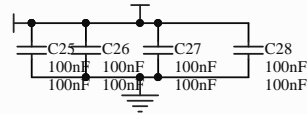
MRWOLF:	PULP STUFF
NRF52832:	nRF MCU
ANALOG:	AFE & ELECTRODE CHECK
POWER:	BATTERY MGMT & REGULATORS

Title		
Size	Number	Revision
A4		
Date:	3/21/2019	Sheet of
File:	C:\Users\...\00_Cover.SchDoc	Drawn By:

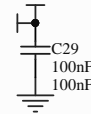
# BYPASS CAPS



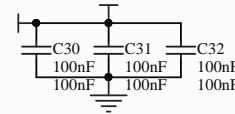
CAP as close as possible to device pins



CAP as close as possible to device pins



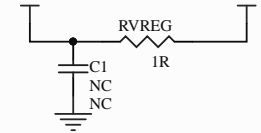
CAP as close as possible to device pins



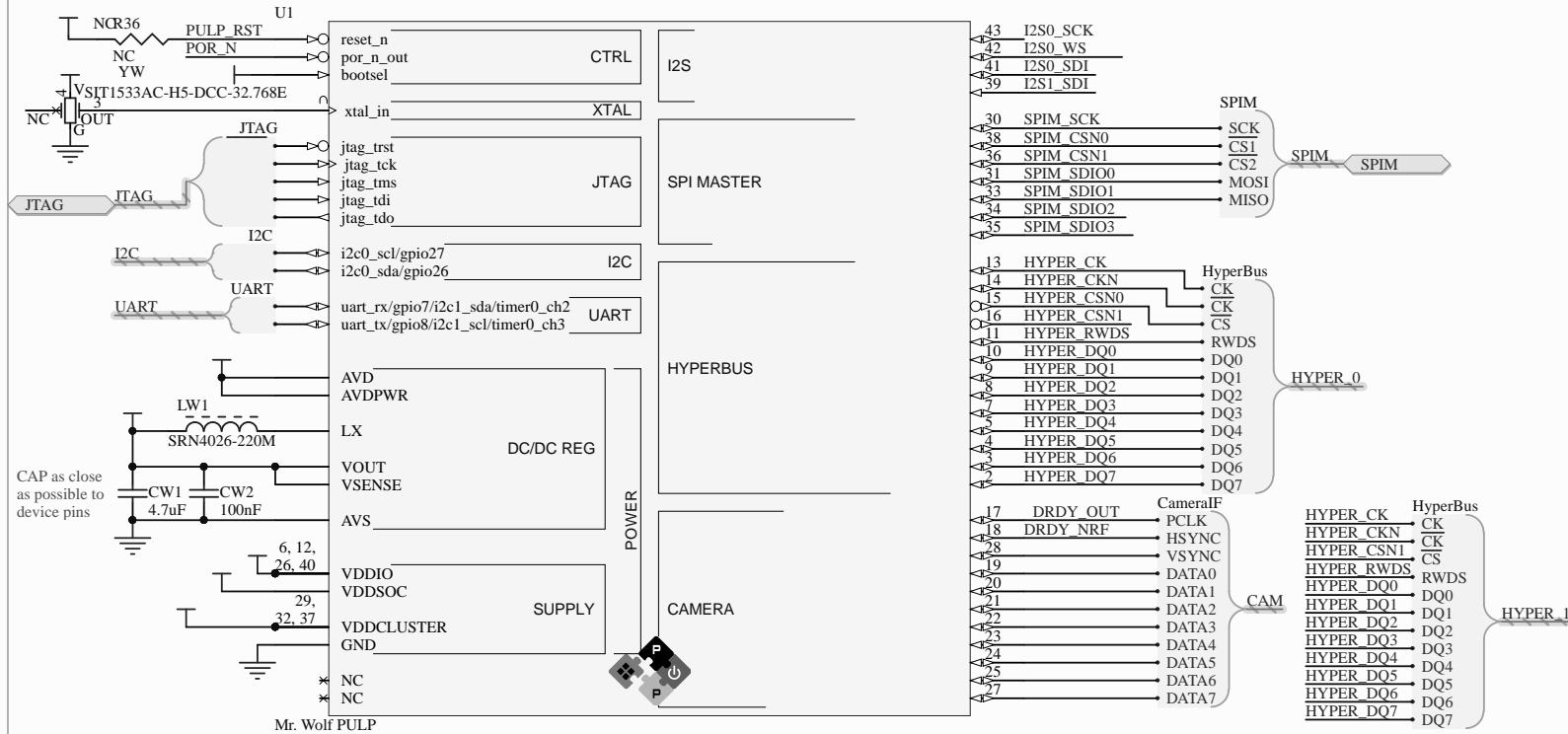
CAP as close as possible to device pins

Current measurement resistors

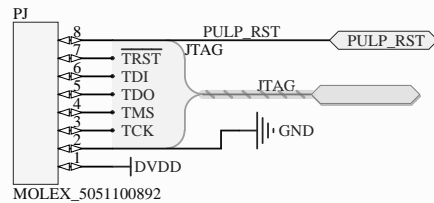
VREG



## Mr. Wolf PULP and Interfaces



## JTAG CONNECTOR



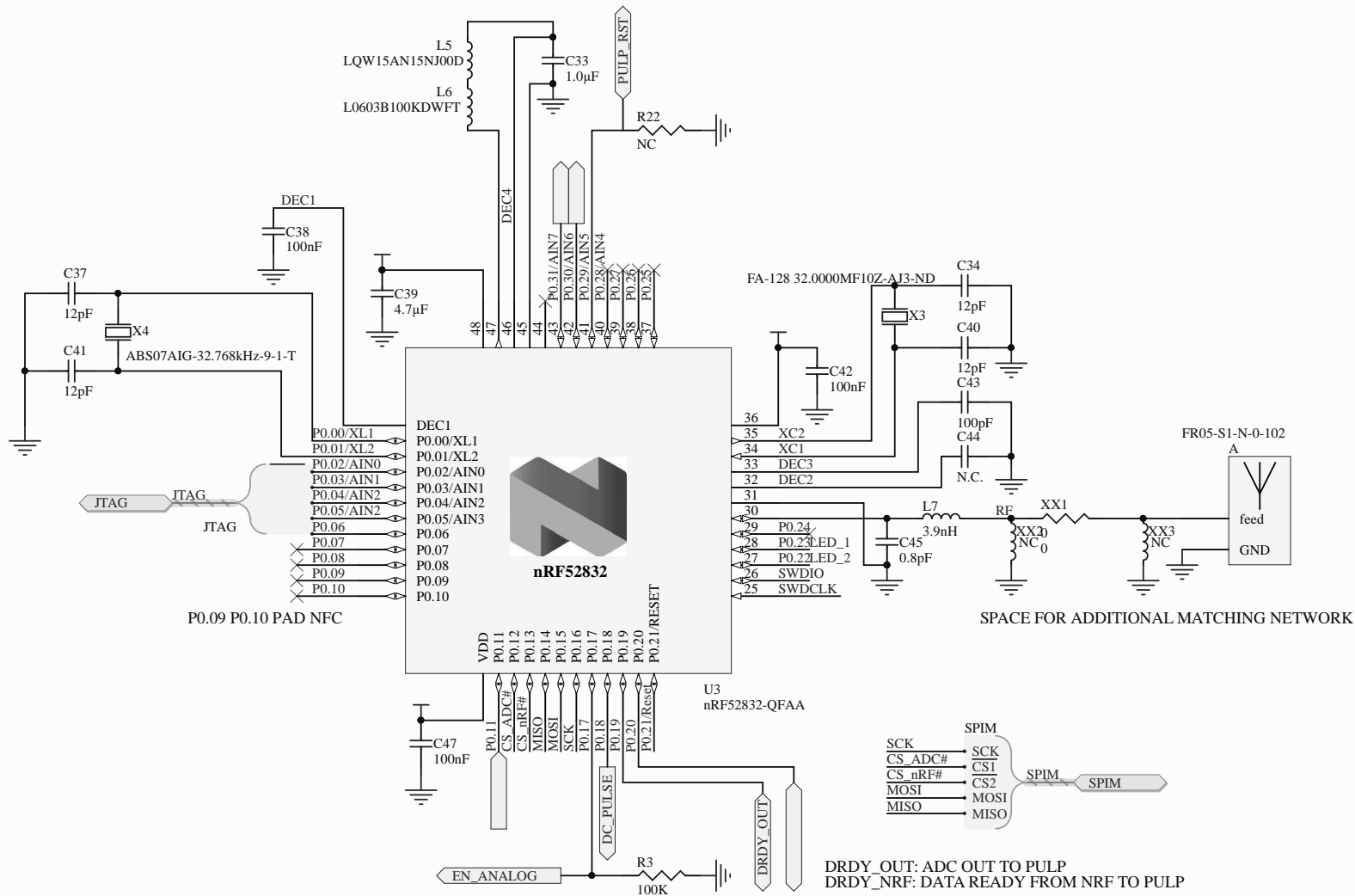
PROGRAMMABLE FROM:

- JTAG CONNECTOR
- JTAG FROM nRF

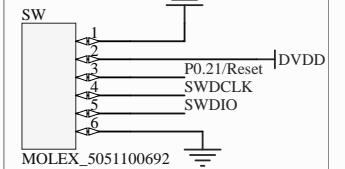
Title	WMI (MR. WOLF)		*
Size:	A4	Number:*	*
Date:	3/21/2019	Time: 1:08:11 PM	Sheet* of *
File:	C:\Users\javie\OneDrive\PhD Unibo\Projects\BioWolf and Connectors\BIOWOLF_xETH\01_MRWOLF.SchDoc		*

Altium

# NRF52832 WITH DC/DC AND CHIP ANTENNA

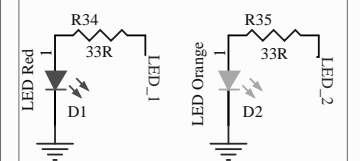


## SWD CONNECTOR



## Leds for debugging

### DEBUG LEDS



Title **WMI (BLE)**

Size: A4

Number:\*

Revision:\*

Date: 3/21/2019

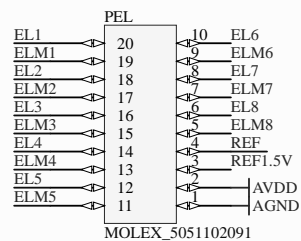
Time: 1:08:12 PM

Sheet\* of \*

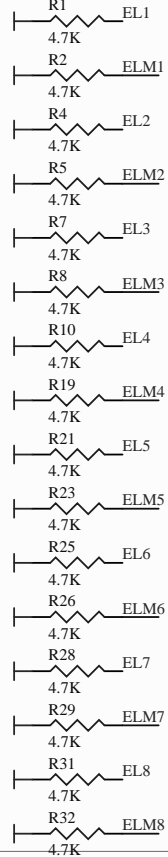
File: C:\Users\javie\OneDrive\PhD Unibo\Projects\BioWolf and Connectors\BIOWOLF\_xETH\02\_NRF52832.SchDoc



## ELECTRODE CONNECTOR

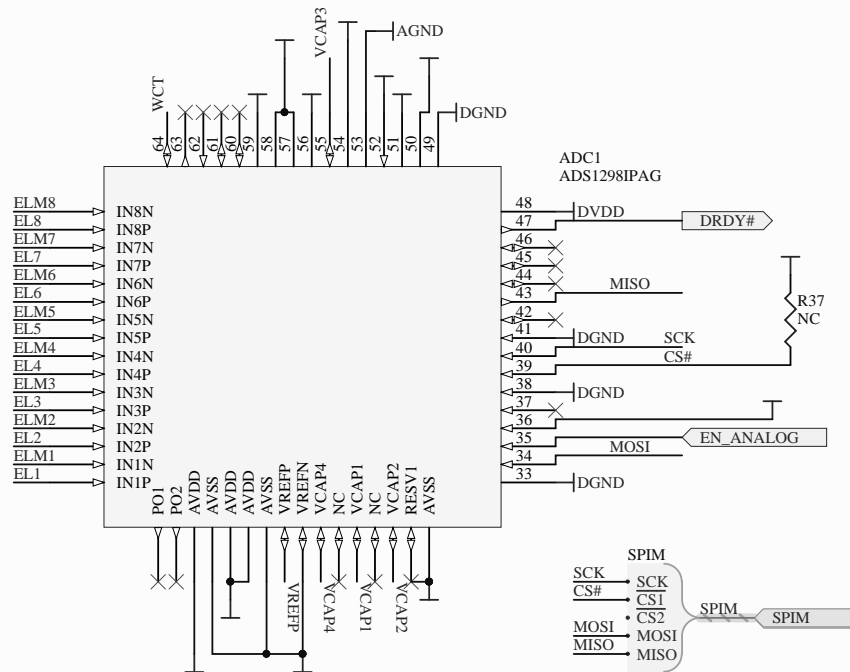


## ELECTRODE PULLUPS



CONNECT ONLY IF USING 2-WIRES ACTIVE ELECTRODES

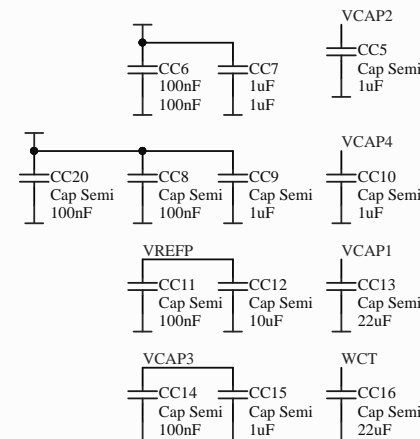
## ANALOG FRONT END



## COMPATIBLE DEVICES:

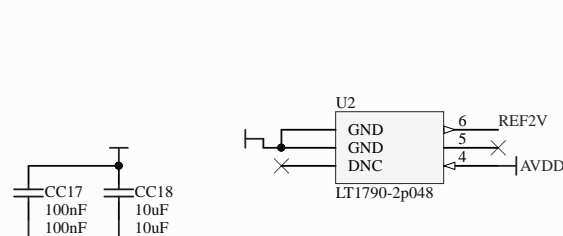
- ADS1294, ADS1296, ADS1298
- ADS1194, ADS1196, ADS1198
- ADS131E04, ADS131E06, ADS131E08
- ADS130E08

## AFE BYPASS

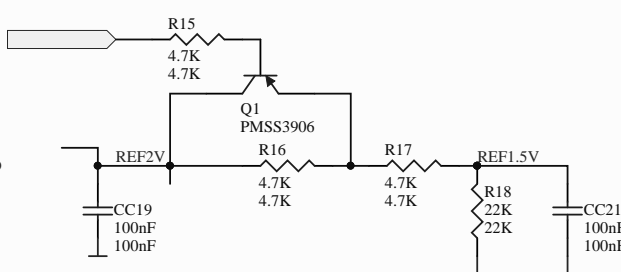


CLOSE TO PINS!

## REFERENCE VOLTAGE FOR GROUND ELECTRODE

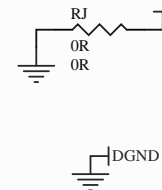


## SQUARE WAVE FOR ELECTRODE CONTACT CHECK



CHANGE RESISTOR VALUES FOR 2-WIRES ACTIVE ELECTRODES

CONNECT GROUND PLANES!!!  
(AGND - DGND - GND)



Title **WMI (AFE)**

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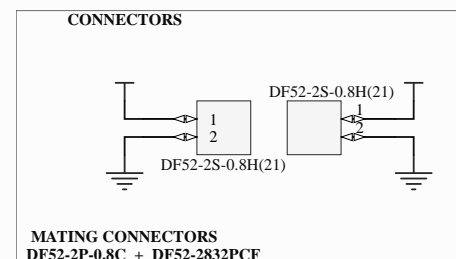
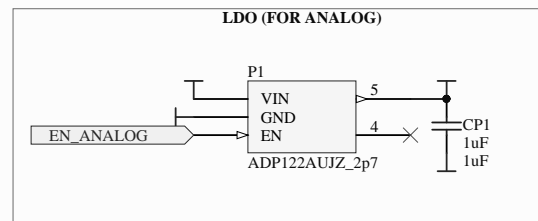
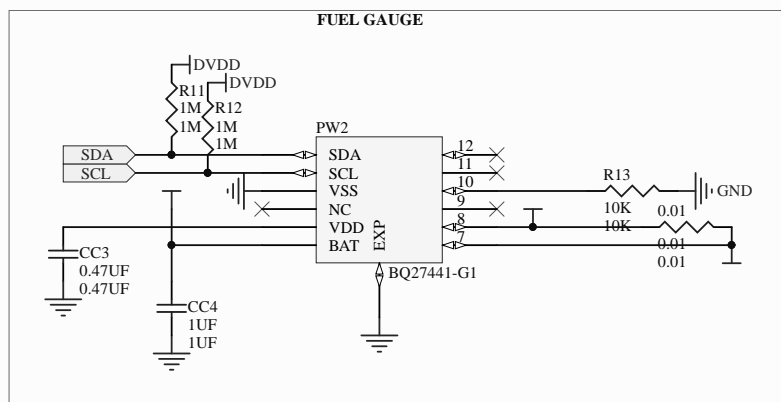
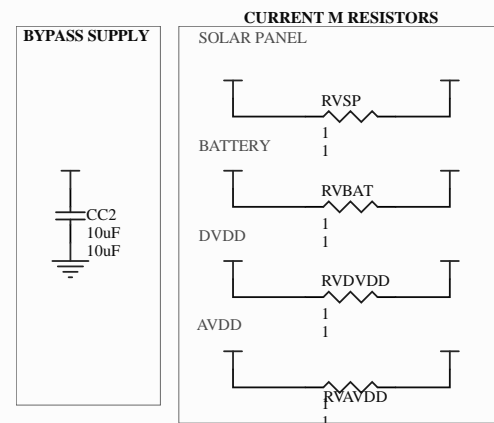
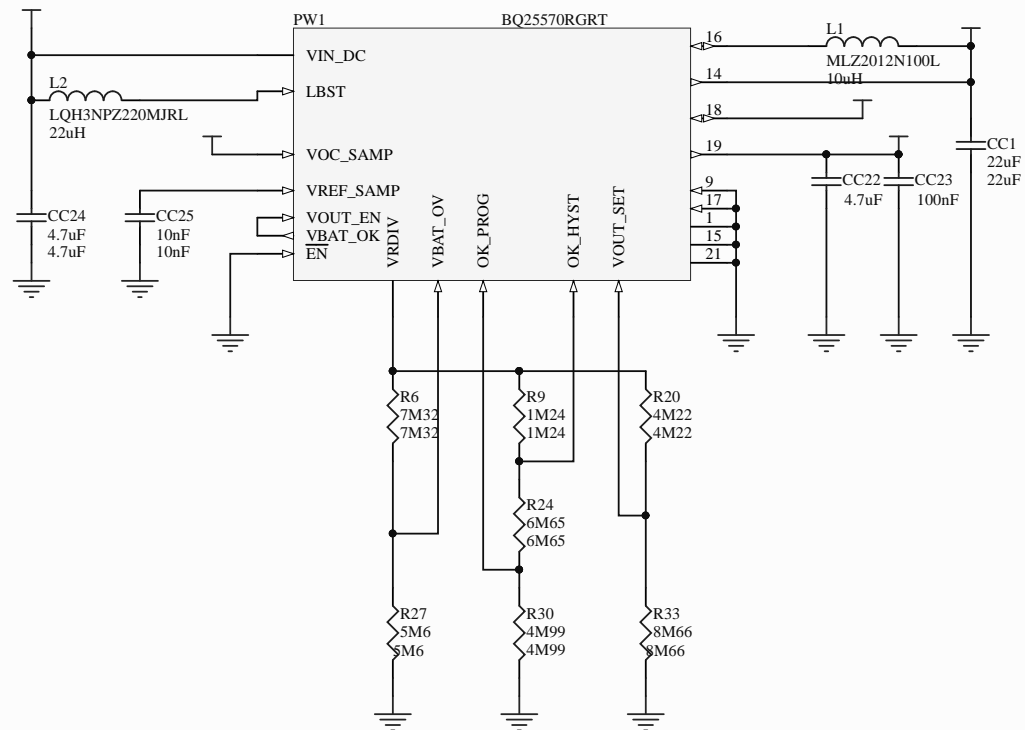
Date: 3/21/2019

Time: 1:08:13 PM

Sheet\* of \*

File: C:\Users\javie\OneDrive\PhD Unibo\Projects\BioWolf and Connectors\BIOWOLF\_xETH\03\_ANALOG.SchDoc

**Altium**



Title <b>WMI (POWER)</b>			*
Size: A4	Number:*	Revision:*	*
Date: 3/21/2019	Time: 1:08:14 PM	Sheet* of *	*
File: C:\Users\javie\OneDrive\PhD Unibo\Projects\BioWolf and Connectors\BIOWOLF_xETH\04_POWER.SchDoc			

