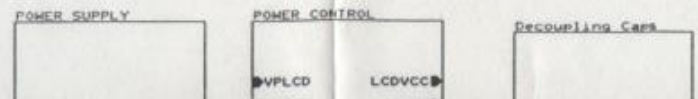
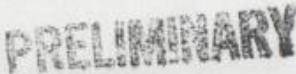


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Power Control FET's



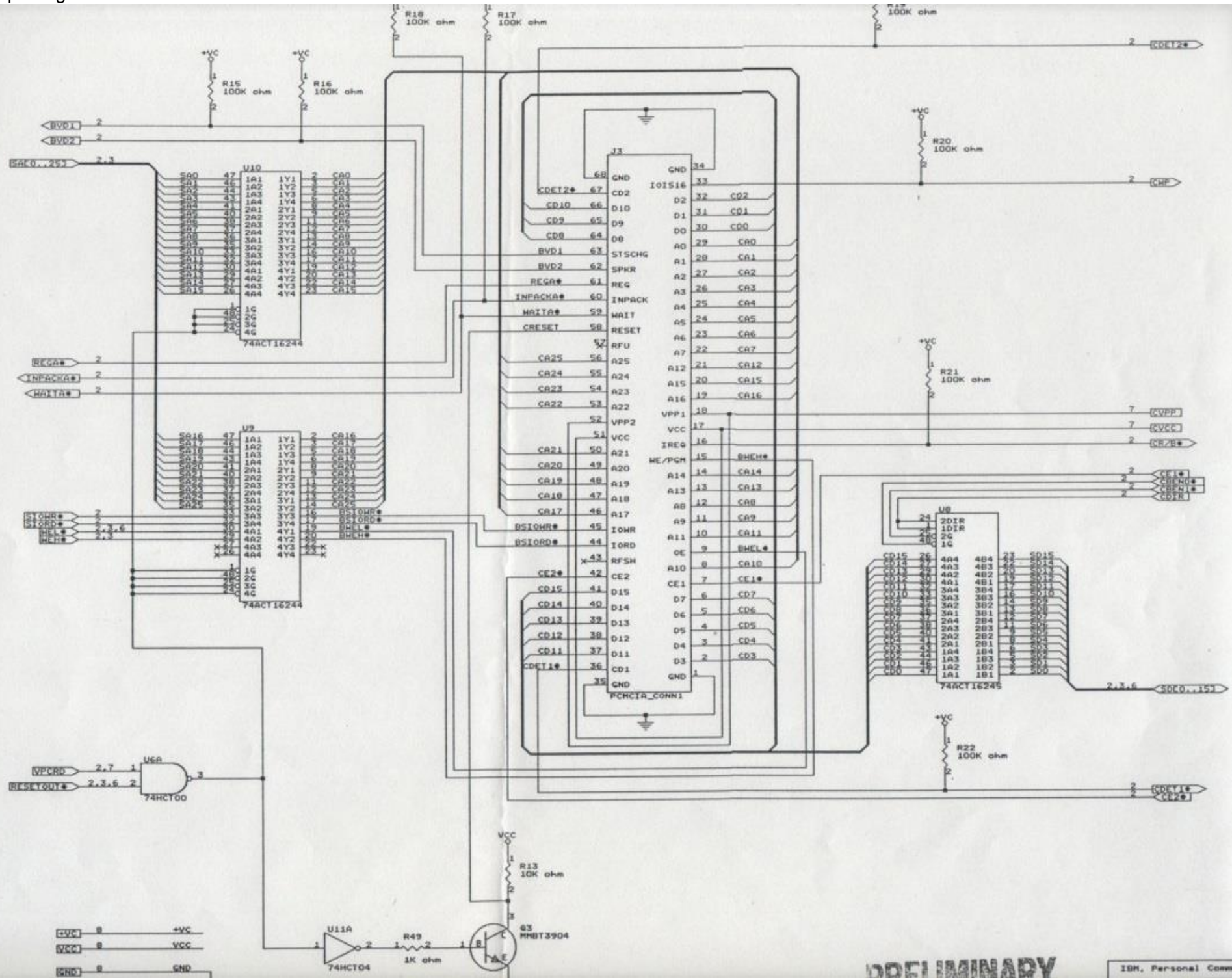
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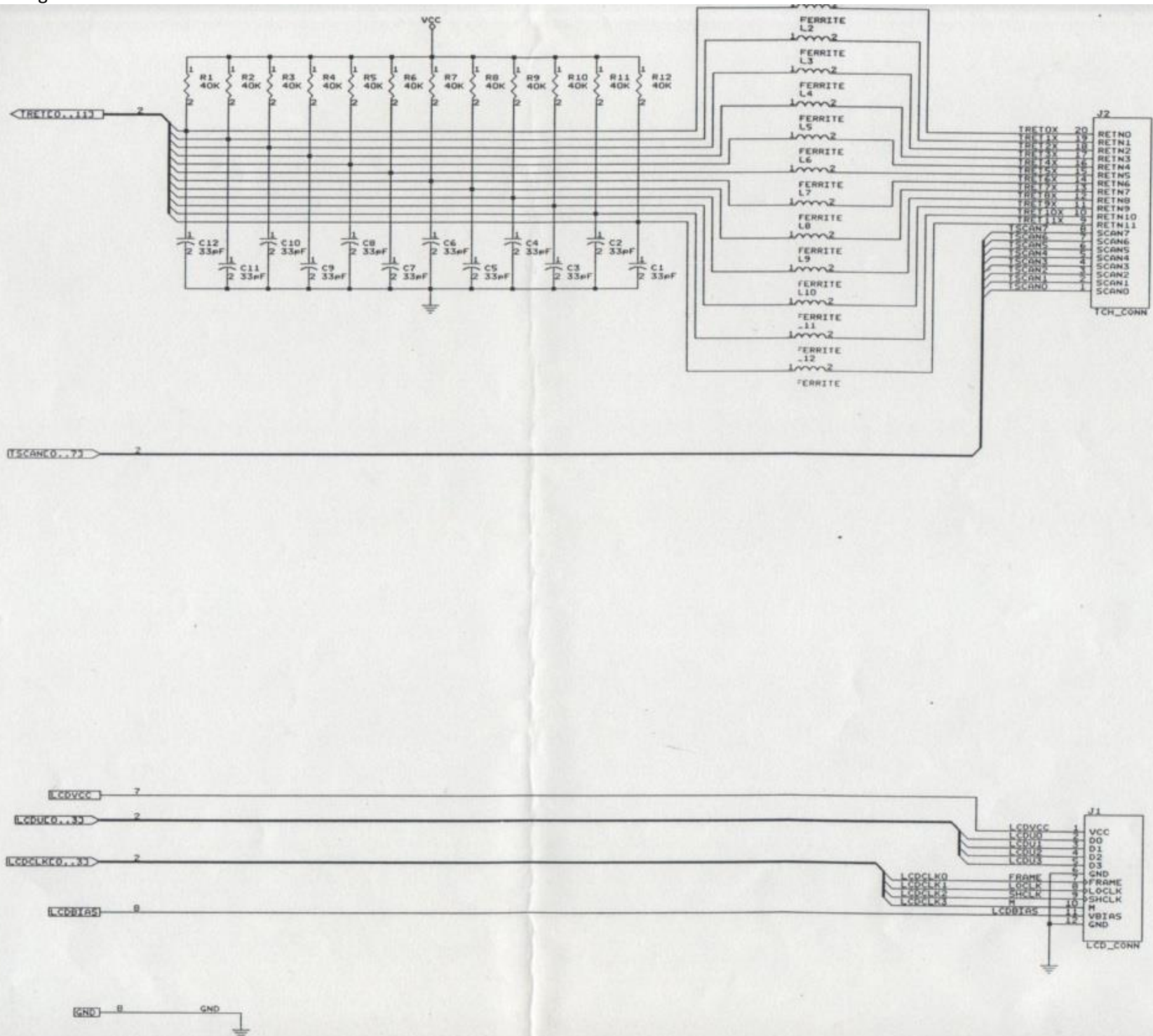


## SweetSpot Logics

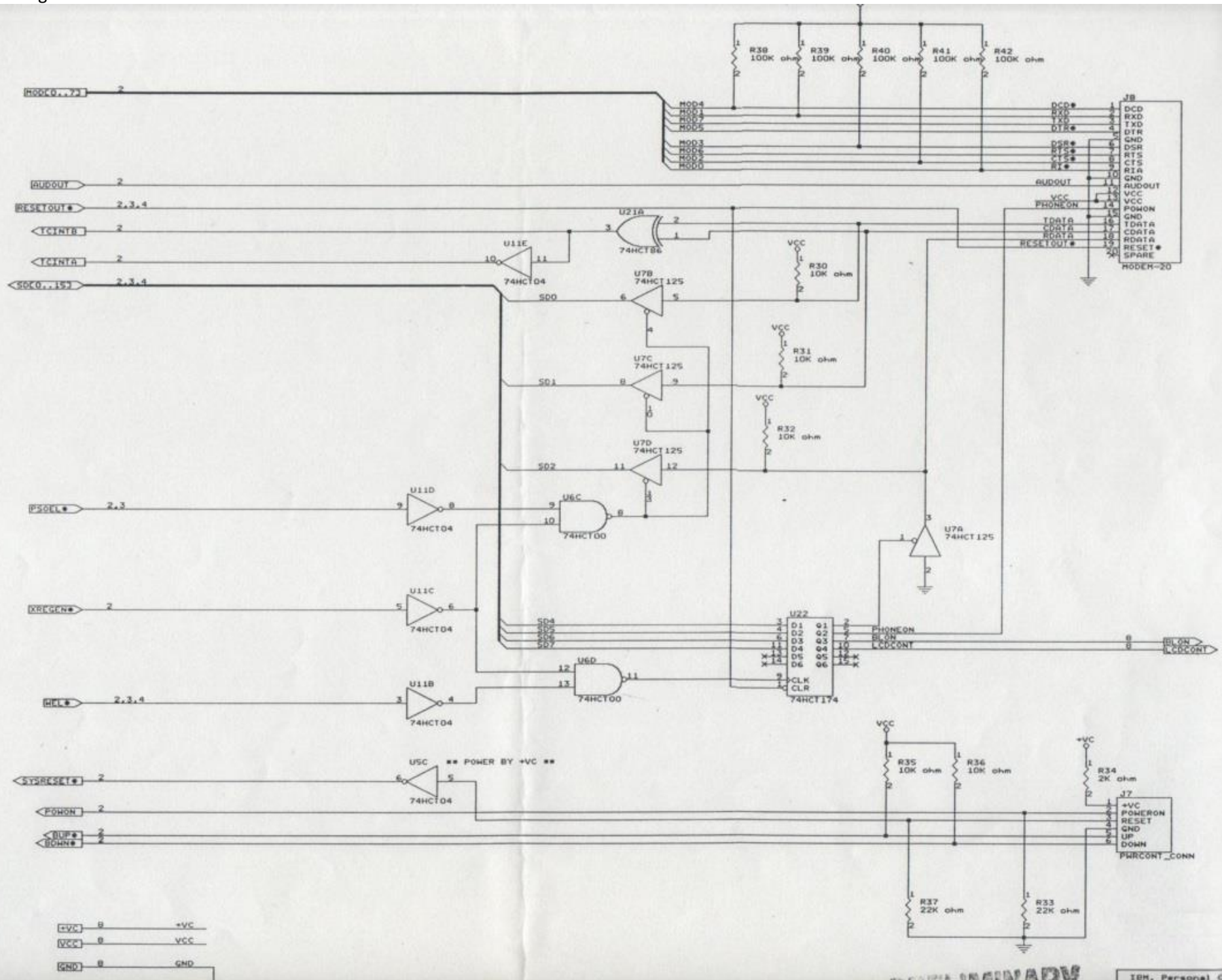


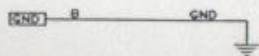
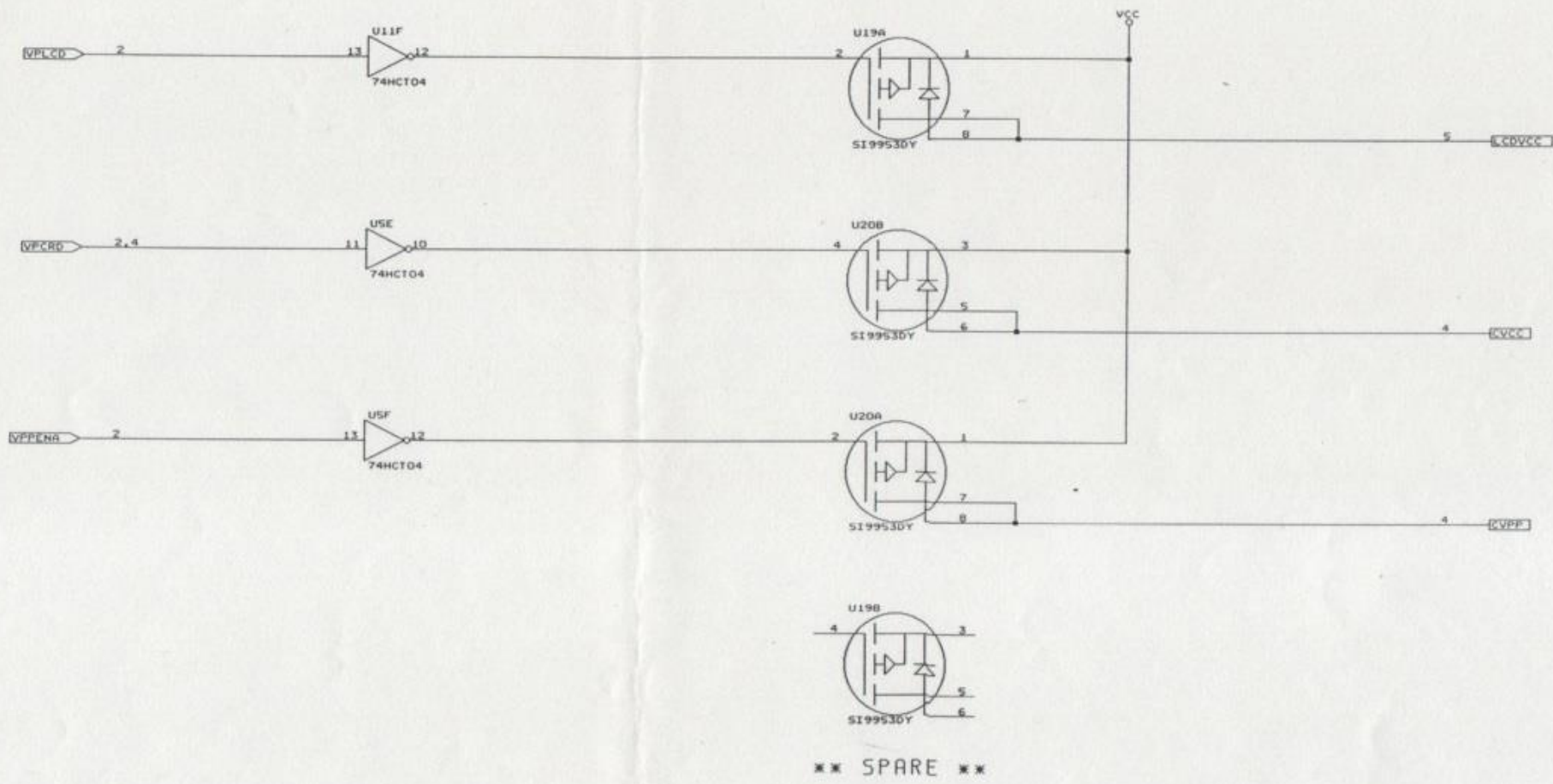






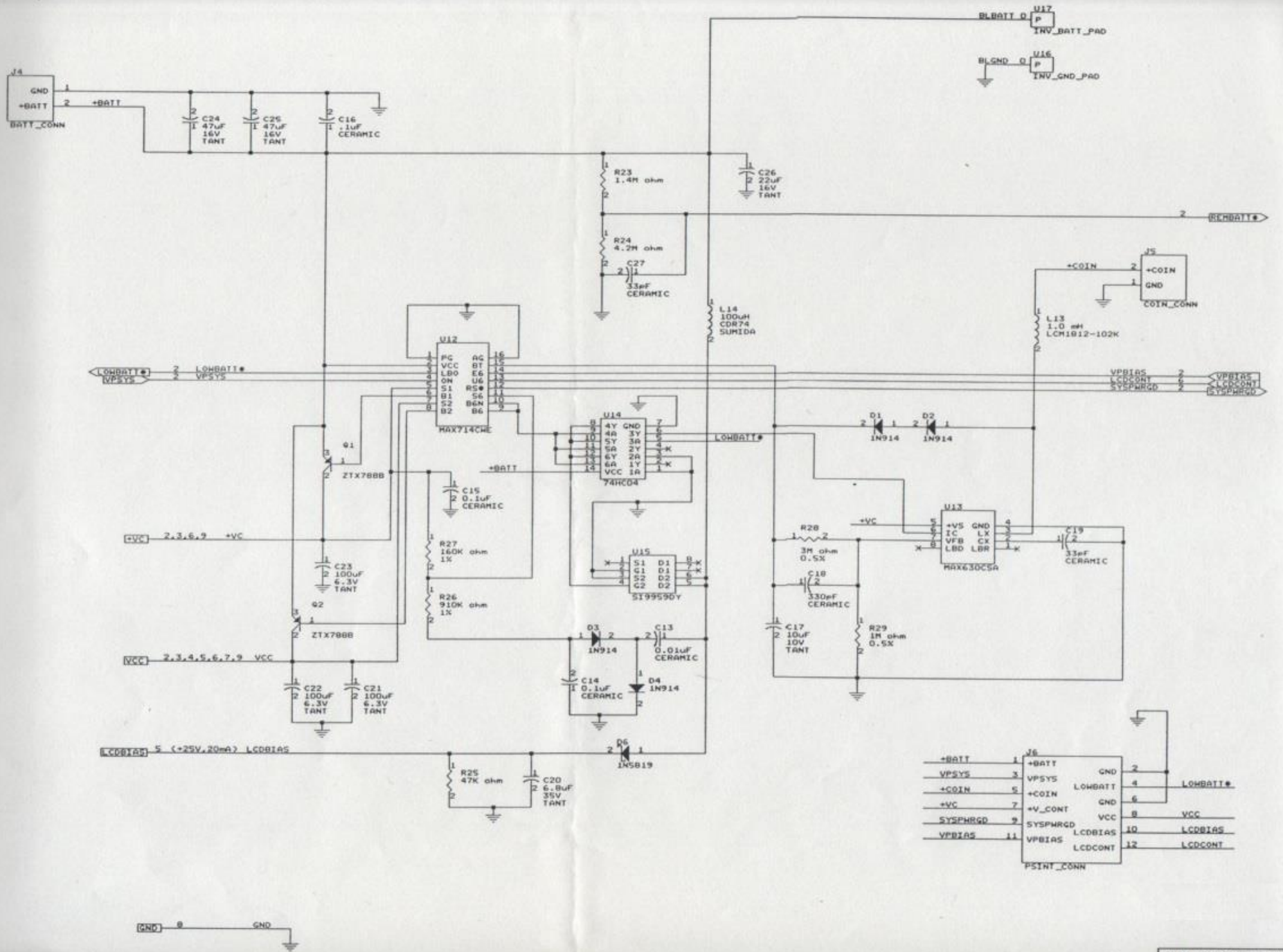




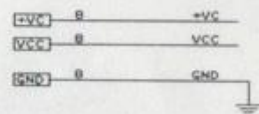
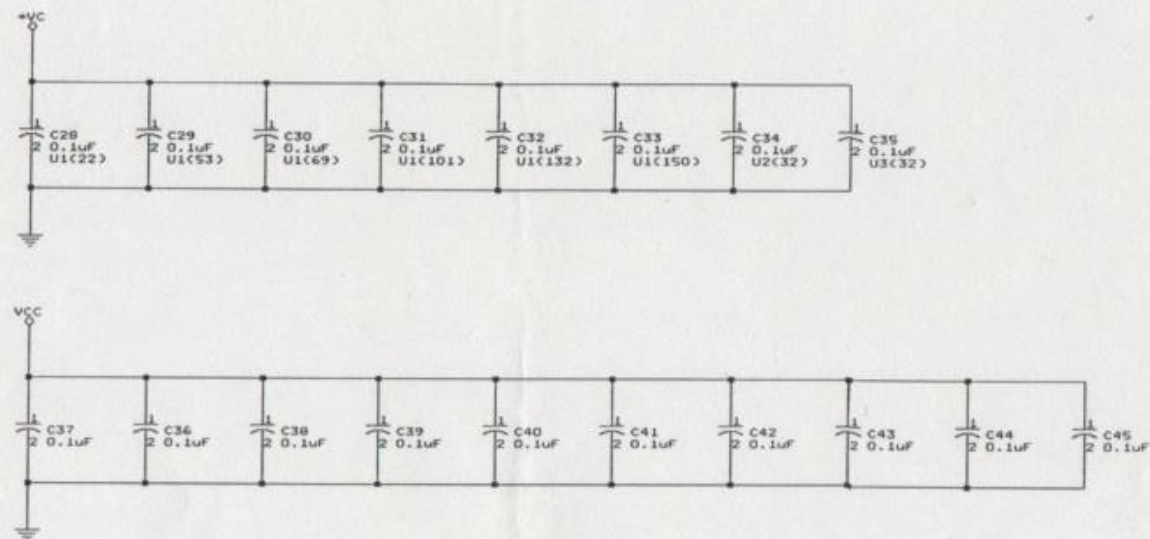


PRELIMINARY

# SweetSpot Logics







# SweetSpot Logics

