



Virtualization

Corso di Sistemi Distribuiti e Cloud Computing A.A. 2021/22

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Laurea Magistrale in Ingegneria Informatica

Virtualizzazione

- Livello alto di astrazione che nasconde i dettagli dell'implementazione sottostante
- **Astrazione** di risorse computazionali
 - Si presenta all'utilizzatore una vista logica diversa da quella fisica

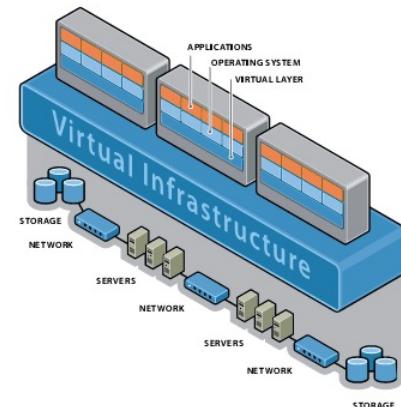


- Come? **Disaccoppiando** l'architettura ed il comportamento delle risorse hw e sw percepiti dall'utente dalla loro realizzazione fisica
- Obiettivi della virtualizzazione:
 - Affidabilità, prestazioni, sicurezza, ...

Virtualization of resources

- System (hw and sw) **resources** virtualization
 - Virtual machine, container, ...
- Storage virtualization
 - Storage Area Network (SAN), ...
- Network virtualization
 - Virtual LAN (VLAN), Virtual Private Network (VPN), ...
- Data center virtualization

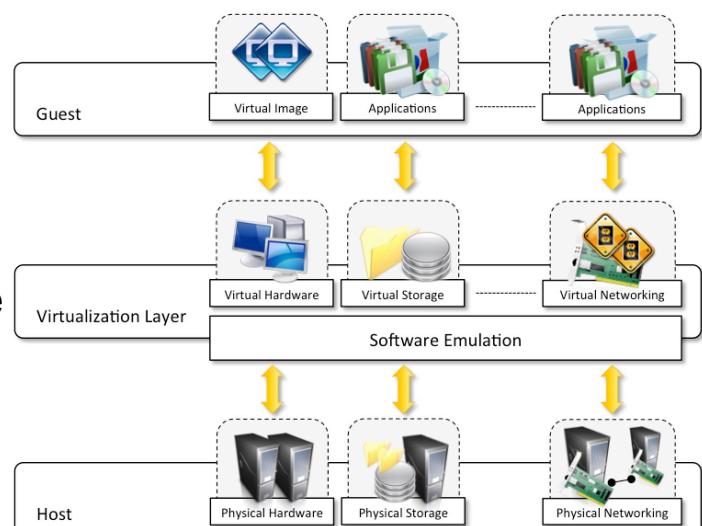
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Components of virtualized environment

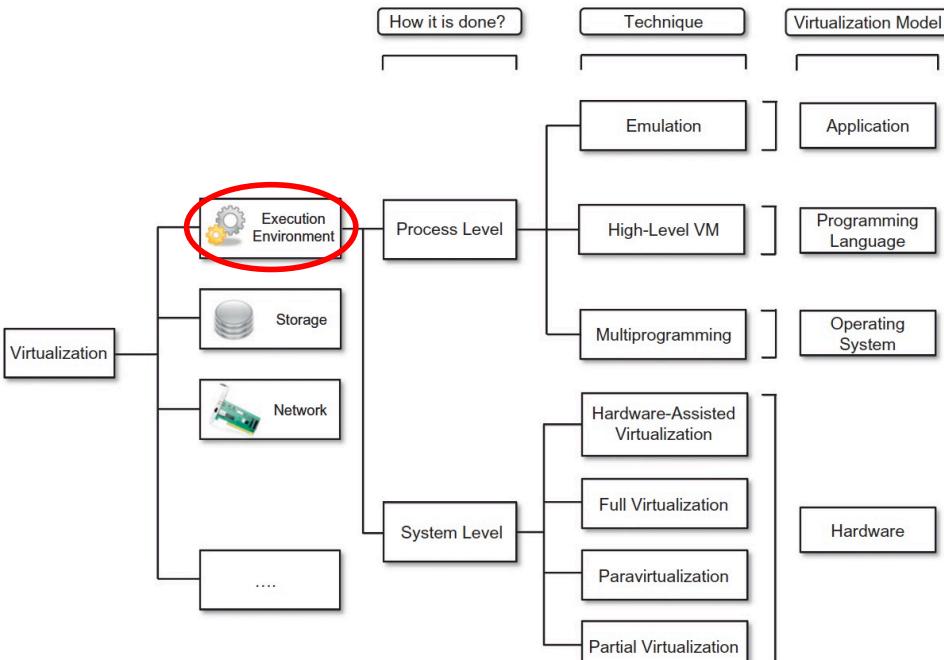
- Three major components:
 - Guest
 - Host
 - Virtualization layer
- **Guest:** system component that interacts with the virtualization layer rather than with the host
- **Host:** original environment where the guest is supposed to be managed
- **Virtualization layer:** responsible for recreating the same or a different environment where the guest will operate



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Taxonomy of virtualization techniques



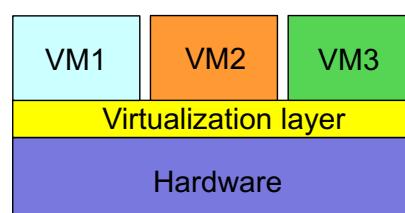
- **Execution environment virtualization** is the oldest, most popular and developed area ⇒ we will mostly investigate it

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Macchina virtuale

- Una **macchina virtuale (VM, Virtual Machine)** permette di rappresentare le risorse hw/sw di una macchina diversamente dalla loro realtà
 - Ad es.: risorse hw della VM (CPU, scheda di rete, ...) diverse dalle risorse fisiche della macchina reale
 - Ad es.: risorse sw (sistema operativo, ...) diverse dalle risorse sw della macchina reale
- Una singola macchina fisica può essere usata come differenti ambienti di elaborazione
 - Molteplici VM su una singola macchina fisica



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Virtualizzazione: cenni storici

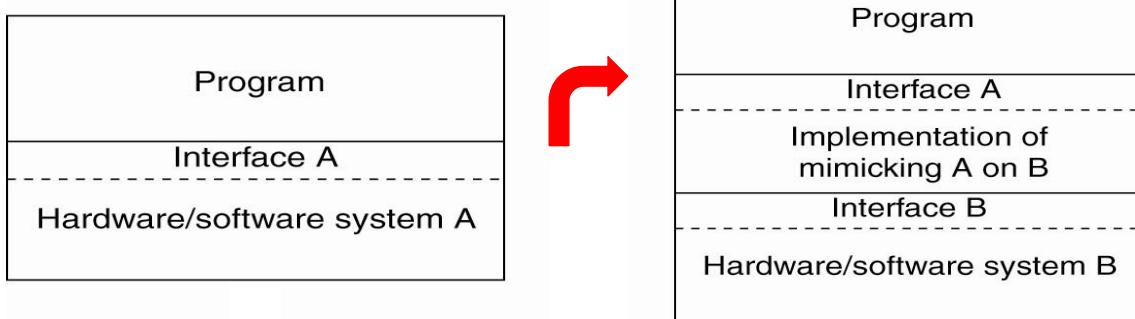
- Il concetto di VM è un'idea “vecchia”, essendo stato definito negli anni '60 in un contesto centralizzato
 - Ideato per consentire al software *legacy* (esistente) di essere eseguito su mainframe molto costosi e condividere in modo trasparente le (scarse) risorse fisiche
 - Ad es. il mainframe IBM System/360-67
- Prima monotasking, non multi, quindi era una idea per suddividere le risorse
- Negli anni '80, con il passaggio ai PC il problema della condivisione trasparente delle risorse di calcolo viene risolto dai SO multitasking
 - L'interesse per la virtualizzazione svanisce

Virtualizzazione: cenni storici

- Alla fine degli anni '90, l'interesse rinasce per rendere meno onerosa la programmazione hw special-purpose
 - VMware fondata nel 1998
- Si acuisce il problema dei costi di gestione e di sottoutilizzo di piattaforme hw e sw eterogenee
 - L'hw cambia più velocemente del sw (middleware e applicazioni)
 - Aumenta il costo di gestione e diminuisce la portabilità
- Diventa nuovamente importante la condivisione dell'hw e delle capacità di calcolo non usate per ridurre i costi dell'infrastruttura
- È una delle tecnologie abilitanti del Cloud computing

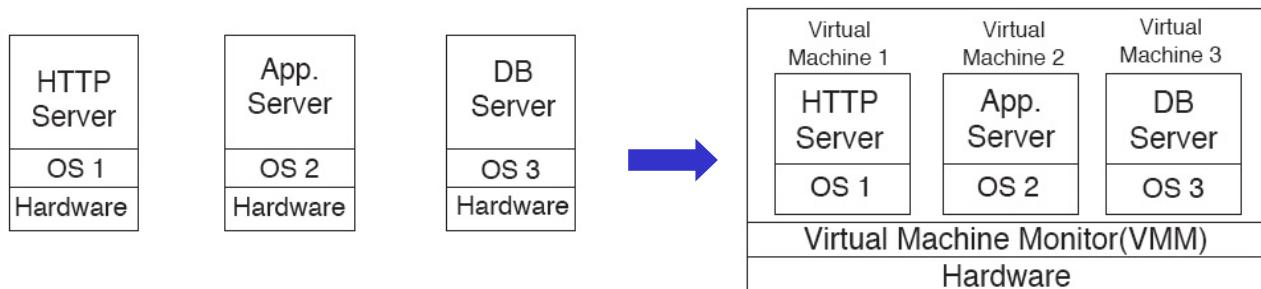
Virtualizzazione: vantaggi

- Facilita la **compatibilità, portabilità, interoperabilità e migrazione** di applicazioni ed ambienti
 - Indipendenza dall'hw
 - Create Once, Run Anywhere
 - VM legacy: eseguire vecchi SO o vecchie applicazioni su nuove piattaforme



Virtualizzazione: vantaggi

- Permette il **consolidamento dei server** in un data center, con vantaggi economici, gestionali ed energetici
 - Multiplexing di molteplici VM sullo stesso server
 - Obiettivo: ridurre il numero totale di server usati, utilizzandoli in modo più efficiente
 - Vantaggi:
 - Riduzione di costi, consumi energetici e spazio occupato
 - Semplificazione nella gestione, manutenzione ed upgrade dei server
 - Riduzione dei tempi di downtime, tramite **live migration** di VM



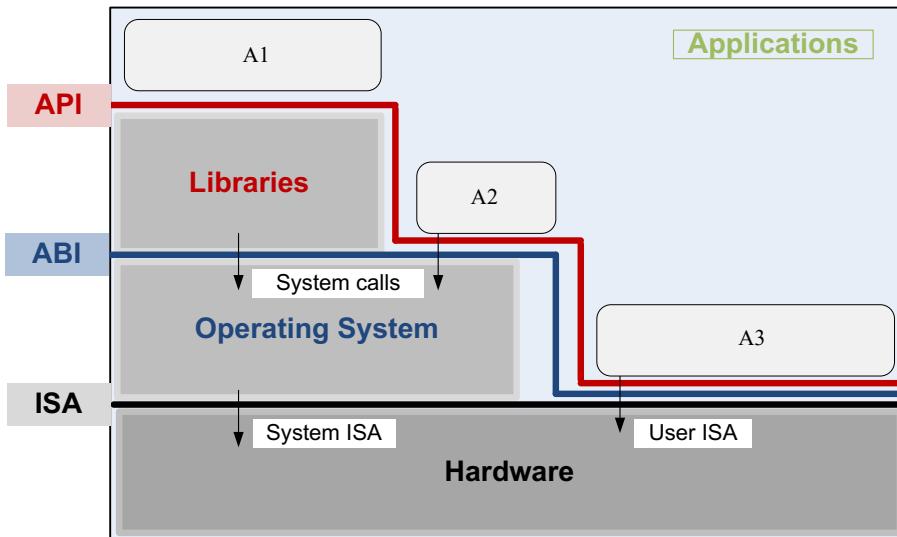
Virtualizzazione: vantaggi

- Permette di **isolare** componenti malfunzionanti o soggetti ad attacchi di sicurezza, incrementando **affidabilità** e **sicurezza** delle applicazioni
 - Macchine virtuali di differenti applicazioni non possono avere accesso alle rispettive risorse
 - Bug del software, crash, virus in una VM non possono danneggiare altre VM in esecuzione sulla stessa macchina fisica
- Permette di **isolare le prestazioni** di diverse VM
 - Tramite lo scheduling delle risorse fisiche che sono condivise tra molteplici VM in esecuzione sulla stessa macchina fisica
- Permette di **bilanciare il carico sui server**
 - Tramite la **migrazione** della VM da un server ad un altro

Uso di ambienti di esecuzione virtualizzati

- In ambito personale e didattico
 - Per eseguire simultaneamente diversi SO sulla stessa macchina
 - Per semplificare l'installazione di sw
- In ambito professionale
 - Per debugging, testing e sviluppo di applicazioni
- In ambito aziendale
 - Per consolidare l'infrastruttura del data center
 - Per garantire *business continuity*: incapsulando interi sistemi in singoli file (**system image**) che possono essere replicati, migrati o reinstallati su qualsiasi server

Interfaces in computer system



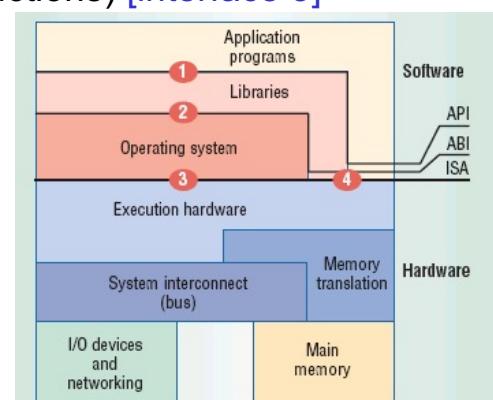
Applications:

- use library functions (A1)
- make system calls (A2)
- and execute machine instructions (A3)

Interfaces in computer system and virtualization

At which level can virtualization be implemented?

- Depends strongly on computer system interfaces
 - Hw/sw interface (**user-level ISA**: primarily for computation, unprivileged machine instructions called by every program) [interface 4]
 - Hw/sw interface (**system ISA**: primarily for system resource management, privileged machine instructions) [interface 3]
 - System calls [interface 2]
 - **ABI** (Application Binary Interface): interface 2 + interface 4
 - Library calls (**API**) [interface 1]
- Virtualization goal
 - Mimic the behavior of these interfaces



Smith and Nair, [The architecture of virtual machines](#), IEEE Computers, 2005

Levels of virtualization implementation

- Virtualization can be implemented at various operational levels:
 - ISA level
 - **Hardware level** (aka *system VMs*)  Our focus
 - **Operating system level** (aka *containers*)  Our focus
 - Library level
 - User application level (aka *process VMs*)

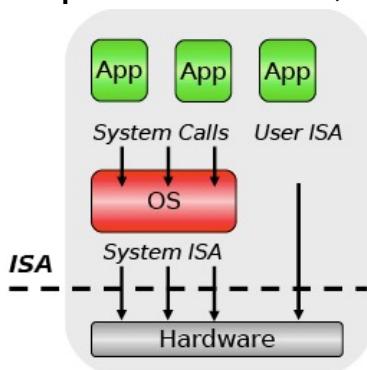
Levels of virtualization implementation

- **ISA level**
 - Goal: emulate a given ISA by ISA of host machine (e.g., MIPS binary code can run on x86-based host machine with the help of ISA emulation)
 - ISA emulation can be done through *code interpretation* or *dynamic binary translation*
 - Code interpretation: slow, because every source instruction is interpreted by the emulator for executing native ISA instructions
 - Dynamic binary translation: faster, because it converts in blocks rather than instruction by instruction
- **Hardware level** (aka *system VMs*)
 - Goal: virtualize computer's resources, such as its processors, memory, and I/O devices
 - Based on **Virtual Machine Monitor (VMM)**, aka **hypervisor**

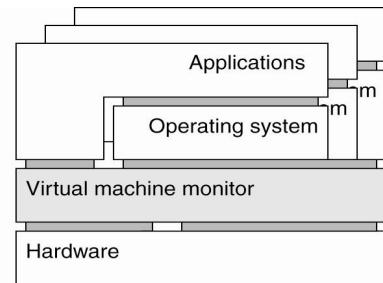
Hardware level (aka system VMs)

- Provides a complete environment in which an OS and many processes can coexist
 - VMM manages the set of hardware resources and shares them among multiple VMs and provide isolation and protection of VMs
 - When a VM performs a privileged instruction or operation that directly interacts with shared hw, VMM intercepts the instruction, checks it for correctness, and performs it

- Examples: VMware, KVM, Xen, Parallels, VirtualBox



Multiple instances of combinations
<application, operating system>

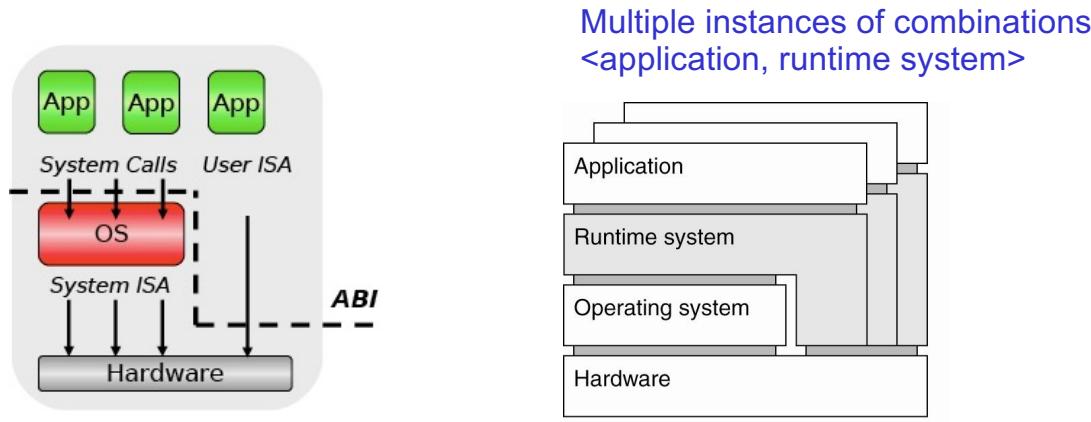


Levels of virtualization implementation (cont'd)

- Operating system level (aka containers)
 - Goal: create multiple isolated containers
 - Examples: Docker, Linux Containers
- Library level
 - Goal: create execution environment to run apps in a host environment that does not suit the native apps, rather than creating a VM to run the entire OS
 - Examples: Wine (to run Windows apps on top of POSIX-compliant OS), Cygwin, vCUDA (to run apps executing within VMs to leverage GPU acceleration)
- User application level (aka process VMs)

Process VM

- Abstract (virtual) computer for a process
 - Virtual platform that executes an **individual process**
 - Provides a **virtual ABI or API** environment for user apps
 - Application is compiled into an intermediary, portable code (e.g., Java bytecode), executed in the runtime environment provided by process VM
- Examples: JVM, .NET CLR



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Levels of virtualization implementation: summing up

- Relative merits of virtualization at different levels

Level of Virtualization	Functional Description	Example Packages	Merits, App Flexibility/Isolation, Implementation Complexity	
Instruction Set Architecture	Emulation of a guest ISA by host	Dynamo, Bird, Bochs, Crusoe	Low performance, high app flexibility, median complexity and isolation	
Hardware-Level Virtualization	Virtualization on top of bare-metal hardware	XEN, VMWare, Virtual PC	High performance and complexity, median app flexibility, and good app isolation	
Operating System Level	Isolated containers of user app with isolated resources	Docker Engine, Jail, FVM	Highest performance, low app flexibility and best isolation, and average complexity	
Run-Time Library Level	Creating VM via run-time library through API hooks	Wine, vCUDA, WABI, LxRun	Average performance, low app flexibility and isolation, and low complexity	
User Application Level	Deploy HLL VMs at user app level	JVM, .NET CLR, Panot	Low performance and app flexibility, very high complexity and app isolation	

Level of Implementation	Higher Performance	Application Flexibility	Implementation Complexity	Application Isolation
Instruction Set Architecture (ISA)	X	XXXXX	XXX	XXX
Hardware-Level Virtualization	XXXXX	XXX	XXXXX	XXXX
Operating System Level	XXXXX	XX	XXX	XX
Run-Time Library Support	XXX	XX	XX	XX
User Application Level	XX	XX	XXXXX	XXXXX

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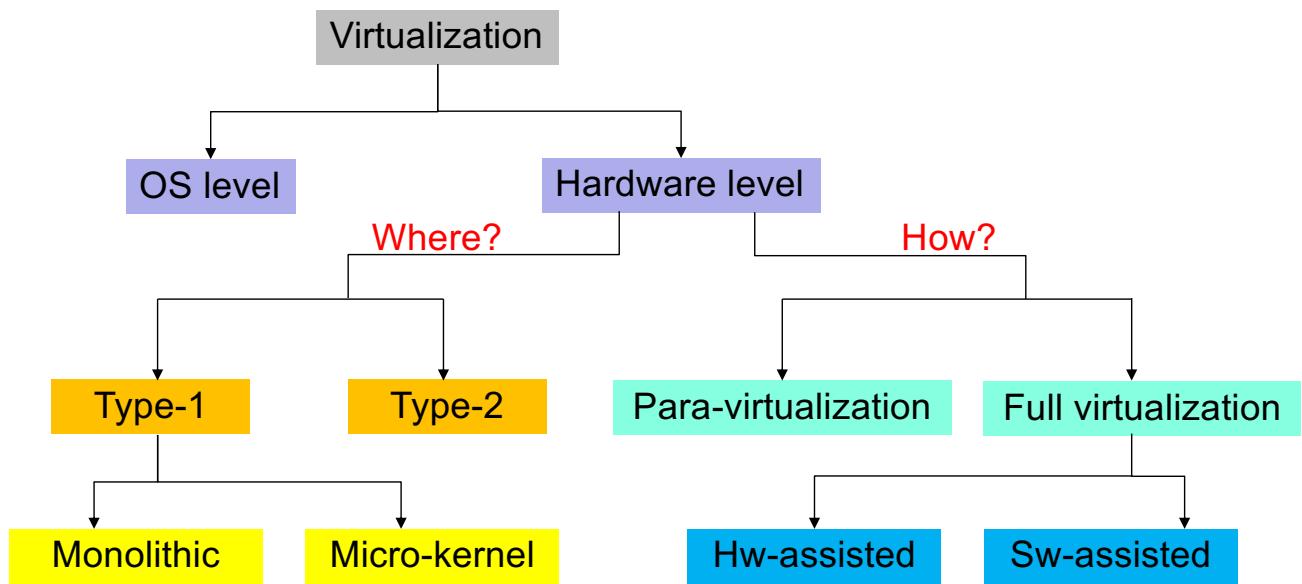
System-level virtualization: terminology

- Let's focus on **system-level virtualization** (achieved through a VMM or hypervisor)
- **Host**: base platform on top of which VMs are executed; made of
 - Physical machine
 - *Possible* host OS
 - VMM
- **Guest**: everything inside a single VM
 - Guest OS and applications executed inside the VM

System-level virtualization: classification

- We **distinguish** solutions according to:
 - Where to deploy the VMM
 - **System VMM** (also called type-1, native or bare-metal hypervisor)
 - **Hosted VMM** (also called type-2 hypervisor)
 - How to virtualize the execution of sensitive, non-virtualizable instructions
 - **Full virtualization**
 - *Software-assisted*
 - *Hardware-assisted*
 - **Para-virtualization**

System-level virtualization: classification



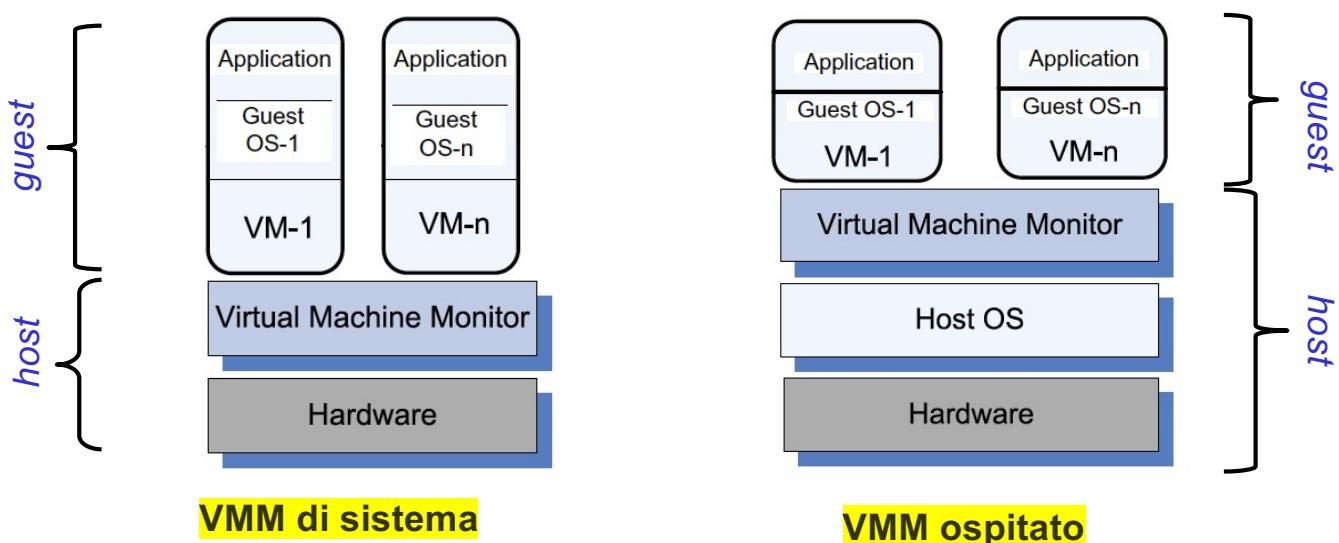
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VMM di sistema o VMM ospitato

In quale livello dell'architettura di sistema si colloca il VMM?

- Direttamente sull'hardware: **VMM di sistema**
- Sopra il SO host: **VMM ospitato**



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VMM di sistema o VMM ospitato

- **VMM di sistema (type-1)**: eseguito direttamente sull'hw, offre funzionalità di virtualizzazione integrate in un SO semplificato
 - L'hypervisor può avere un'architettura a *micro-kernel* (solo funzioni di base, no device driver) o *monolitica*
 - Esempi: Xen, KVM, VMware ESX, Hyper-V
- **VMM ospitato (type-2)**: eseguito sul SO host, accede alle risorse hw tramite le chiamate di sistema del SO host
 - Interagisce con il SO host tramite l'ABI ed emula l'ISA di hw virtuale per i SO guest
 - Vantaggio: può usare il SO host per gestire le periferiche ed utilizzare servizi di basso livello (es. scheduling delle risorse)
 - Vantaggio: non occorre modificare il SO guest
 - Svantaggio: degrado delle prestazioni rispetto a VMM di sistema
 - Esempi: Bochs, Parallels Desktop, VirtualBox

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Virtualizzazione completa o paravirtualizzazione

Quale modalità di dialogo tra la VM ed il VMM per l'accesso alle risorse fisiche, ovvero come gestire l'esecuzione di **istruzioni privilegiate**?

- **Virtualizzazione completa**
- **Paravirtualizzazione**

Confronto qualitativo di diverse soluzioni di virtualizzazione

https://en.wikipedia.org/wiki/Comparison_of_platform_virtualization_software

Virtualizzazione completa o paravirtualizzazione

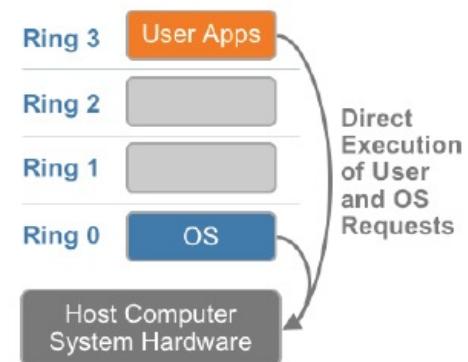
- **Virtualizzazione completa (full)**
 - Il VMM espone ad ogni VM interfacce hw simulate **funzionalmente identiche** a quelle della sottostante macchina fisica
 - Il VMM *intercetta* le richieste di accesso privilegiato all'hw (ad es. istruzioni di I/O) e ne *emula* il comportamento atteso
 - Esempi: KVM, VMware ESXi, Microsoft Hyper-V
- **Paravirtualizzazione**
 - Il VMM espone ad ogni VM interfacce hw simulate **funzionalmente simili** (**ma non identiche**) a quelle della sottostante macchina fisica
 - Non viene emulato l'hw, ma viene creato uno strato minimale di sw (**Virtual Hardware API**) per assicurare la gestione delle VM ed il loro isolamento
 - Esempi: Xen, Oracle VM (basato su Xen), PikeOS

Virtualizzazione completa: pro e contro

- **Vantaggi**
 - Non occorre modificare il SO guest
 - Isolamento completo tra le istanze di VM: sicurezza, facilità di emulare diverse architetture
- **Svantaggi**
 - **VMM più complesso**
 - Necessaria la collaborazione del processore per implementazione efficace: **perché?**

Problemi per realizzare la virtualizzazione di sistema

- L'architettura del processore opera secondo **almeno 2 livelli** (ring) di protezione: *supervisor* e *user*
 - Ring 0: privilegi massimi
 - Ring 3: privilegi minimi



- Con la virtualizzazione: *Architettura x86 senza virtualizzazione*
 - **VMM** opera in supervisor mode (ring 0)
 - **SO guest** e applicazioni (quindi la VM) operano in user mode (ring 3 o ring 1 per SO guest)
 - Problema del **ring deprivilegging**: il SO guest opera in un ring che non gli è proprio ⇒ **non può eseguire istruzioni privilegiate** (e.g., lidt in x86, load interrupt descriptor table)
 - Problema del **ring compression**: poiché applicazioni e SO guest eseguono allo stesso livello, occorre proteggere lo spazio del SO

Virtualizzazione completa: soluzioni

- Come risolvere il ring deprivilegging?
 - **Trap-and-emulate**: quando il SO guest tenta di eseguire un'**istruzione privilegiata**, occorre notificare un'eccezione (**trap**) al VMM e trasferirgli il controllo; il VMM controlla la correttezza dell'operazione richiesta e ne **emula** il comportamento
 - Le **istruzioni non privilegiate** eseguite dal SO guest sono invece **eseguite direttamente**

Popek and Goldberg virtualization requirements

- Popek and Goldberg (1974) defined a set of conditions sufficient for a computer architecture to support system virtualization efficiently
- Definitions
 - *Privileged instruction*: traps when executed in user mode
 - Privileged state: determines resource allocation
 - Includes privilege mode, addressing context, exception vectors, ...
 - *Sensitive instruction*: control- or behavior-sensitive
 - control sensitive: changes privileged state
 - behavior sensitive: exposes privileged state
 - Innocuous instruction: not sensitive

Popek and Goldberg, [Formal requirements for virtualizable third generation architectures](#), *Commun. ACM* 17, 1974

Popek and Goldberg virtualization requirements

- **Theorem 1:** “For any conventional third-generation computer, an effective VMM may be constructed if the set of sensitive instructions for that computer is a subset of the set of privileged instructions.”
 - In other words... trap-and-emulate: it is sufficient that all the instructions that could affect the correct functioning of VMM (sensitive instructions) always trap and pass control to VMM

Popek and Goldberg virtualization requirements

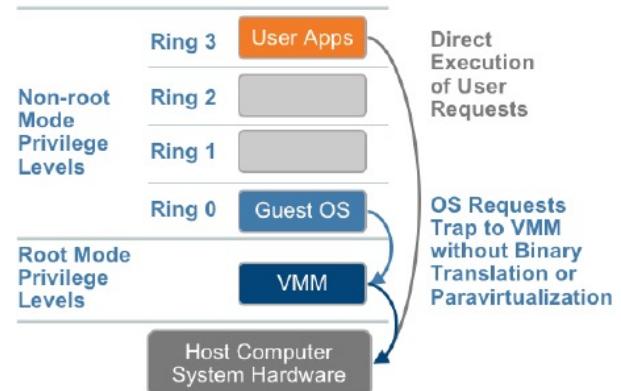
- However, implementation of trap-and-emulate is challenging: common architectures are non-virtualizable according to Popek and Goldberg's theorem
 - x86: many instructions are non-virtualizable, because are *sensitive but unprivileged*
 - E.g., PUSHF (push flags) is not privileged
 - MIPS: mostly virtualizable, but...
 - Kernel registers \$k0, \$k1 (needed to save/restore state) are user-accessible
 - ARM: mostly virtualizable, but
 - Some instructions are undefined in user-mode

Virtualizzazione completa: soluzioni

- Come realizzare il meccanismo di trap?
 - A **livello hardware** se il processore fornisce **supporto** alla virtualizzazione ⇒ **hardware-assisted CPU virtualization**
 - A **livello software** se il processore non fornisce supporto alla virtualizzazione ⇒ **fast binary translation**

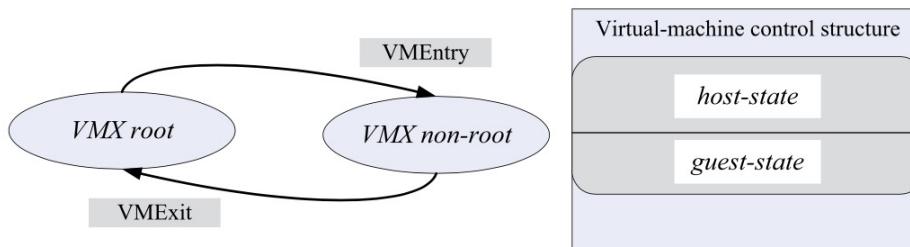
Hardware-assisted CPU virtualization

- Hardware-assisted CPU virtualization (Intel VT-x and AMD-V) provides two new forms of CPU operating modes, called **root mode** and **non-root mode**, each supporting all four x86 protection rings
 - VMM runs in root mode (Root-Ring 0), while guest OSes run in guest mode in their original privilege levels (Non-Root Ring 0): no longer ring deprivileging and ring compression problems
 - VMM can control guest execution through VM control data structures in memory



x86 architecture with full virtualization and *hardware-assisted CPU virtualization*

Hardware-assisted CPU virtualization: VT-x

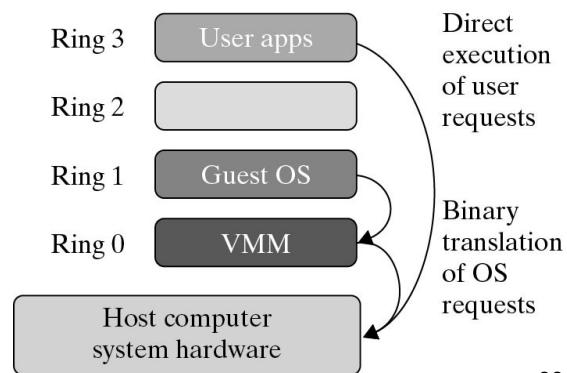


- **VMX root:** intended for hypervisor operations, (like x86 without VT-x)
- **VMX non-root:** intended to support a VM
- When executing **VMEntry** operation, processor state is loaded from *guest-state* of VM scheduled to run; control is then transferred from hypervisor to VM
- **VMExit** saves processor state in *guest-state* area of running VM; it loads the processor state from *host-state*, then transfers control to hypervisor

Fast binary translation

- Il meccanismo di trap al VMM per le istruzioni privilegiate è offerto solo dai processori con supporto hardware per la virtualizzazione (Intel VT-x e AMD-V)
 - IA-32 non lo è: come realizzare la virtualizzazione completa in mancanza del supporto hw?
 - Fast binary translation:** il VMM scansiona il codice prima della sua esecuzione per sostituire blocchi contenenti istruzioni privilegiate con blocchi funzionalmente equivalenti e contenenti istruzioni per la notifica di eccezioni al VMM
 - I blocchi tradotti sono eseguiti direttamente sull'hw e conservati in una cache per eventuali riusi futuri
 - Maggiore complessità del VMM e minori prestazioni
- Architettura x86 con virtualizzazione completa e *fast binary translation*

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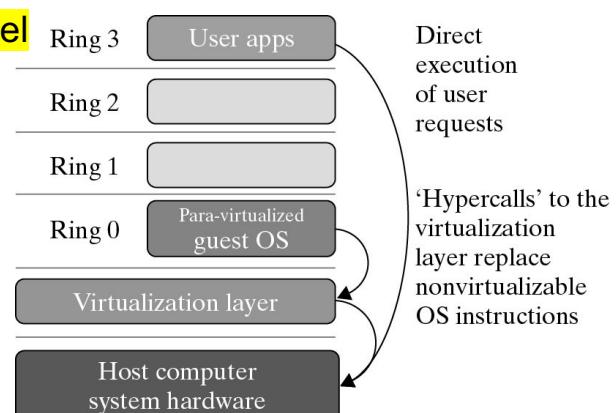
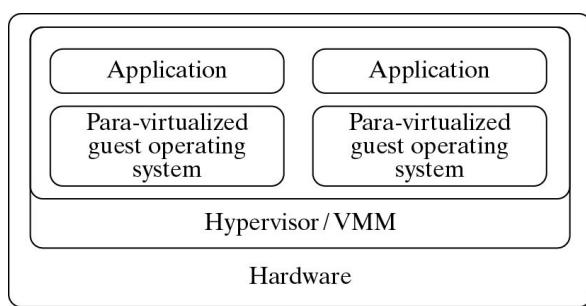


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Paravirtualization

- Non-transparent virtualization solution
 - Guest OS kernel must be modified to let it invoke the virtual API exposed by hypervisor
- Nonvirtualizable instructions are replaced by *hypercalls* that communicate directly with hypervisor
 - Hypercall: software trap from guest OS to hypervisor, just as syscall is a software trap from app to kernel

hypercall : hypervisor = syscall : kernel

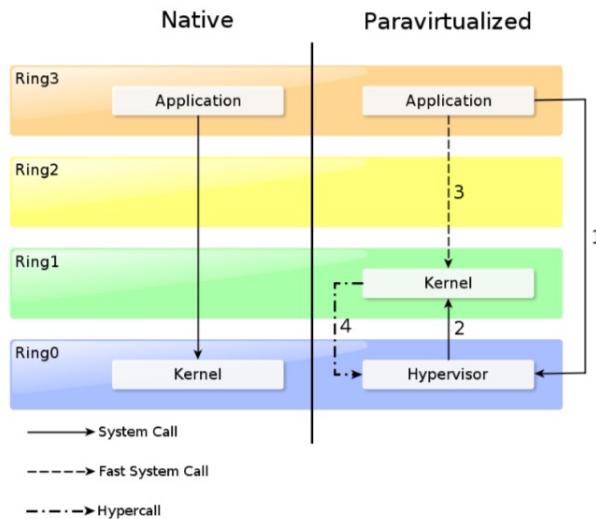


x86 architecture with *paravirtualization*

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Paravirtualization: hypercall execution



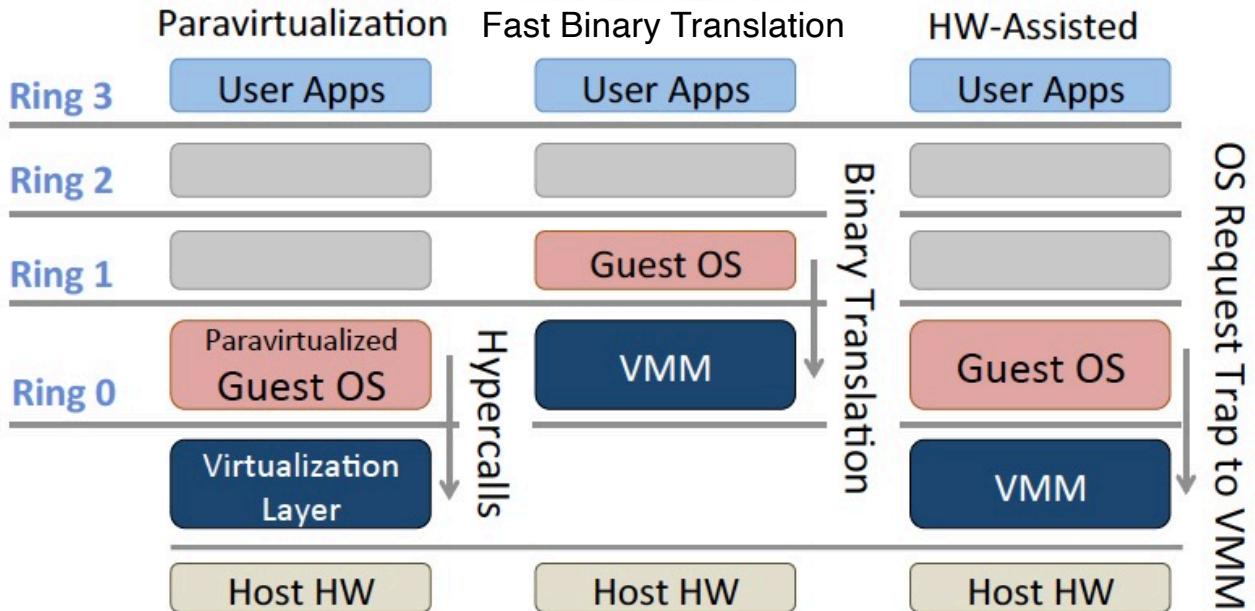
- When an application running in the VM issues a guest OS system call, through the hypercall the control flow jumps to hypervisor, which then passes control back to the guest OS

Source: "The Definitive Guide to XEN hypervisor"

Paravirtualization: pros & cons

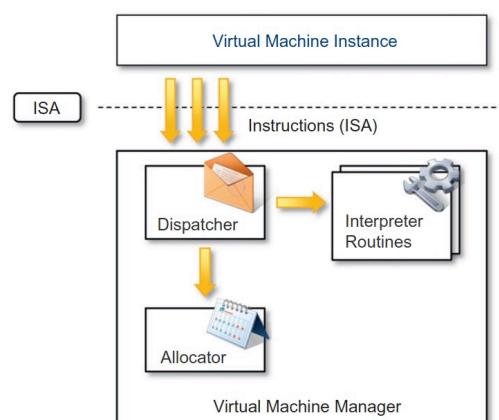
- Pros (vs full virtualization):
 - Relatively easier and more practical implementation
 - Reduced overhead with respect to fast binary translation
 - Does not require virtualization extensions from the host CPU as hw-assisted virtualization does
- Cons:
 - Requires source code of OSes to be available
 - OSes that cannot be ported (e.g., Windows) can use ad-hoc device drivers that remap the execution of critical instructions to the virtual API exposed by the VMM
 - Cost of maintaining paravirtualized OSes
 - Paravirtualized OS cannot longer run on hardware directly

Summing up the different approaches



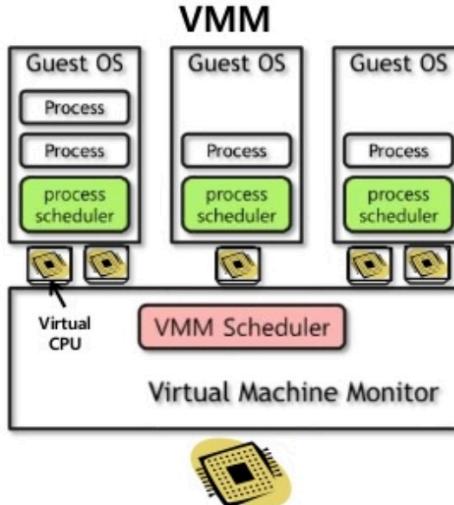
VMM reference architecture

- Three main modules
 - **Dispatcher**: VMM entry point that reroutes the privileged instruction issued by the VM to one of the other two modules
 - **Allocator** (or **scheduler**): decides about the system resources to be provided to the VM
 - **Interpreter**: executes a proper routine when the VM executes a privileged instruction



VMM reference architecture: scheduler

- VMM scheduler: additional scheduling layer with respect to traditional CPU scheduling

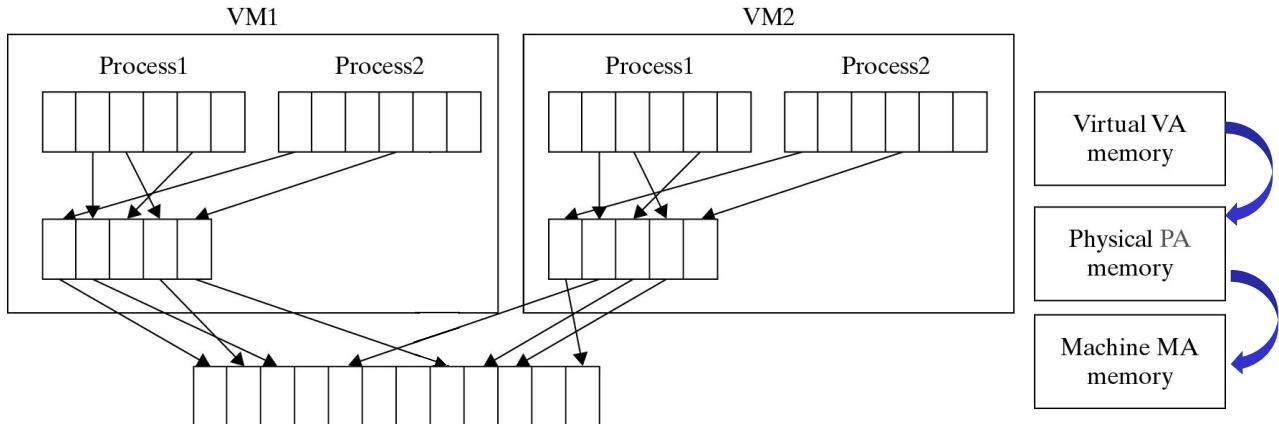


- How to efficiently schedule virtual CPUs?
 - We will study how virtual CPUs scheduling is managed by Xen hypervisor

Memory virtualization

- In a *non-virtualized* environment
 - One-level memory mapping: from virtual memory to physical memory provided by page tables
 - MMU and TLB hardware components to optimize virtual memory performance
- In a *virtualized* environment
 - All VMs share the same machine memory and VMM needs to partition it among VMs
 - **Two-level memory mapping**: from guest virtual memory to guest physical memory to host physical memory
- Some terms
 - *Guest virtual memory*: memory visible to apps; continuous virtual address space presented by guest OS to apps
 - *Guest physical memory*: memory visible to guest OS
 - *Host/machine physical memory*: actual hw memory visible to VMM

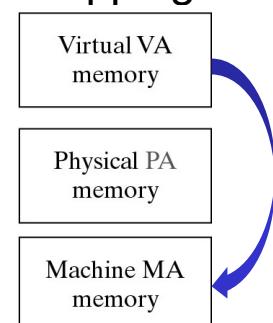
Two-level memory mapping



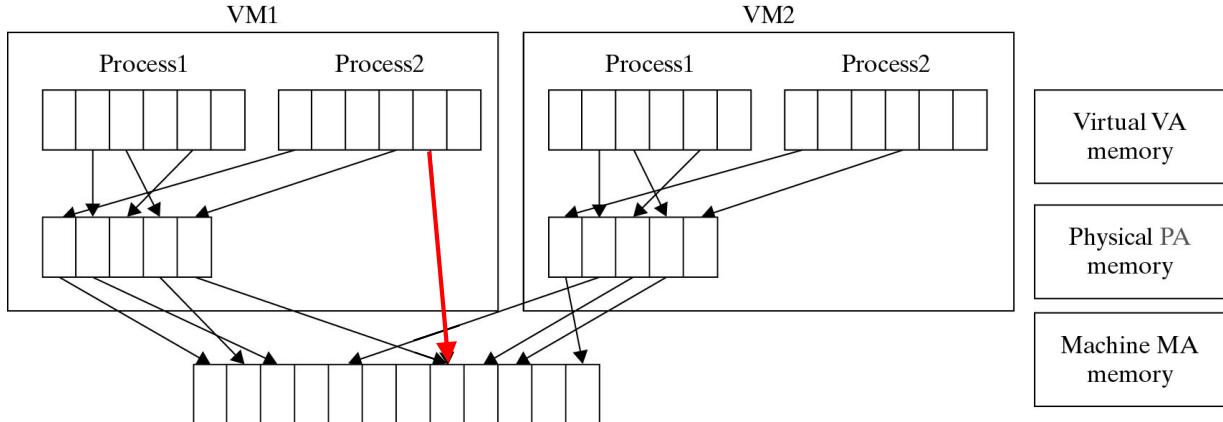
- Going from guest virtual memory to host physical memory requires two-level memory mapping
GVA (guest virtual address) → GPA (guest physical address) → HMA (host machine address)
- Guest physical address ≠ host machine address: why?
 - Hints: many VMs; what does guest OS expect about its memory?

Shadow page tables

- To avoid an unbearable performance drop due to the extra memory mapping, VMM maintains **shadow page tables (SPTs)** and uses them to accelerate the mapping
 - So to achieve direct mapping from GVA to HPA
- SPT directly maps GVA to HPA
 - Guest OS creates and manages page tables (PTs) for its virtual address space without modification
 - But these PTs are not used by MMU hardware
 - VMM creates and manages PTs that map virtual pages directly to machine pages
 - These VMM PTs are the **shadow page tables** and are loaded into MMU
 - VMM needs to keep the SPTs consistent with changes made by each guest OS to its PTs



Memory mapping with SPTs



- VMM uses TLB hardware to map virtual memory directly to machine memory to avoid the two levels of translation on every access (red arrow)

Shadow page tables consistency

- When guest OS changes its PTs, VMM needs to update the SPTs to enable a direct lookup
- How?
 - VMM maps guest OS PTs as read only
 - When guest OS writes to PTs, trap to VMM
 - VMM applies write to SPT and guest OS PT, then returns
 - Also known as [memory tracing](#)
 - Adds overhead

Challenges in memory virtualization with SPT

- Address translation

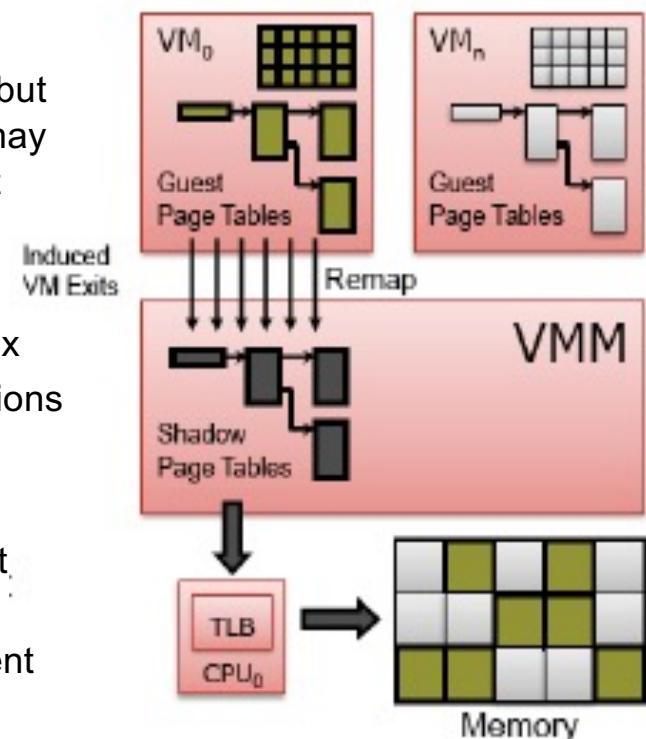
- Guest OS expects contiguous, zero-based physical memory, but underlying machine memory may be non contiguous: VMM must preserve this illusion

- Page table shadowing

- SPT implementation is complex
- VMM intercepts paging operations and constructs copy of PTs

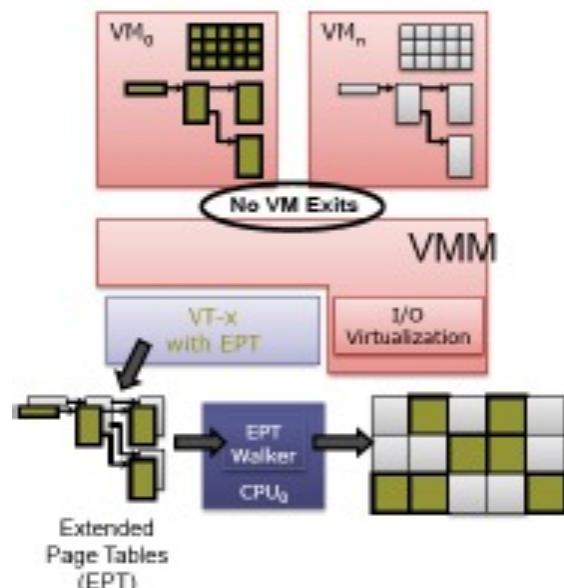
- Overheads

- SPTs consume significant host memory
- SPTs need to be kept consistent with guest PTs
- VM exits add to execution time



Hw support for memory virtualization

- SPT is a software-managed solution; let's consider a more efficient hardware solution
- **Second Level Address Translation (SLAT)** is the hardware-assisted solution for memory virtualization (Intel EPT and AMD RVI) to translate GVA into HPA
- Using SLAT significant performance gain with respect to SPT: around 50% for MMU intensive benchmarks



Case study: Xen



- The most notable example of **paravirtualization**
 - www.xenproject.org (developed at University of Cambridge)
 - Open-source **type-1 (system VMM) hypervisor** with microkernel design
 - Offers to guest OS a virtual interface (**hypercall API**) to whom guest OS must refer to access machine physical resources
 - Supports both **paravirtualization (PV)** and hardware-assisted virtualization (**HVM**)
 - With paravirtualization Xen requires PV-enabled guest OSs and PV drivers (part of Linux kernel and other OSs)
 - OSs ported to Xen: Linux, NetBSD, FreeBSD and OpenSolaris
 - With HVM also unmodified guest OSs (e.g., Windows)
 - Foundation for commercial virtualization products (e.g., Oracle VM and Qubes OS)
 - Powers IaaS providers (Alibaba, Amazon, IBM, Rackspace, ...)
 - In 2017 Amazon began a shift to KVM for new EC2 instance types

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Xen: pros and cons

- Pros
 - Thin hypervisor model
 - 300K lines of code on x86, 65K on Arm
 - Small footprint and interface (around 1MB in size)
 - Scalable: up to 4,095 host CPUs with 16Tb of RAM
 - More robust and secure than other hypervisors, see <https://youtu.be/sjQnAIJji4k>
 - But still vulnerable to attacks <https://xenbits.xen.org/xsa/>
 - Continuously improved
 - Flexibility in management
 - Tuning for performance
 - Low overhead (within 2%) with respect to bare metal machine without virtualization
 - Supports VM live migration
- Cons
 - I/O performance still remains challenging

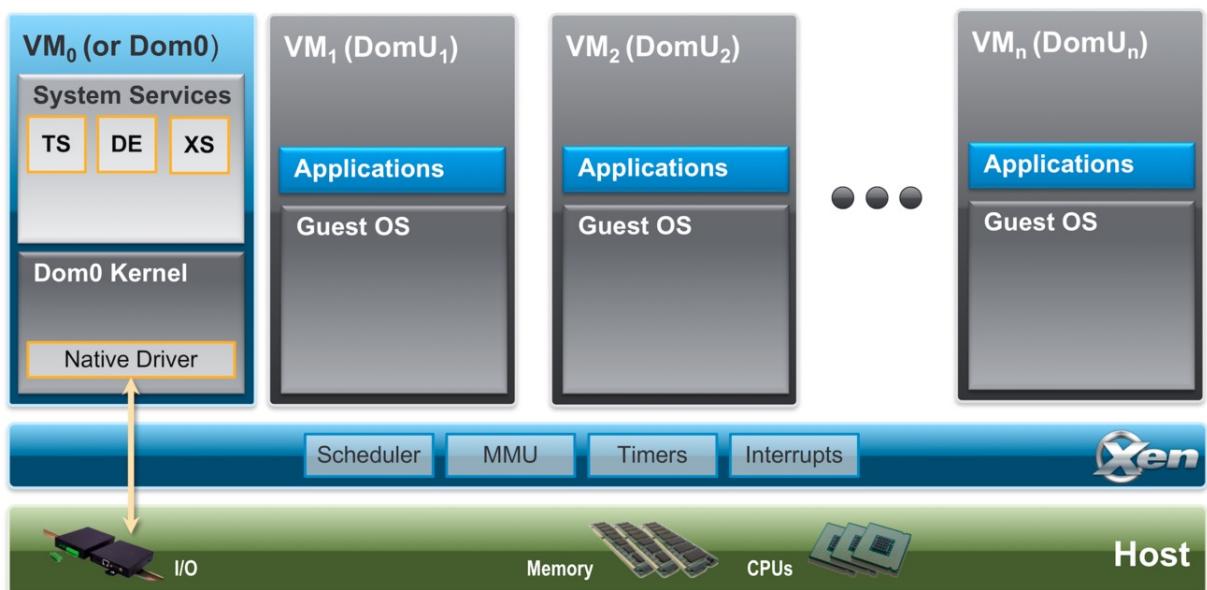
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Xen architecture

- Goal of Cambridge group who designed Xen (late 1990s)
 - Design a VMM capable of scaling to about 100 VMs running applications and services without any modifications to ABI
 - First public release in 2003
- Microkernel design
- What can be paravirtualized?
 - Disk and network devices
 - Interrupts and timers
 - Emulated motherboard and legacy boot
 - Privileged instructions and page tables (memory access)
 - Privileged instructions issued by guest OS are replaced with hypercalls

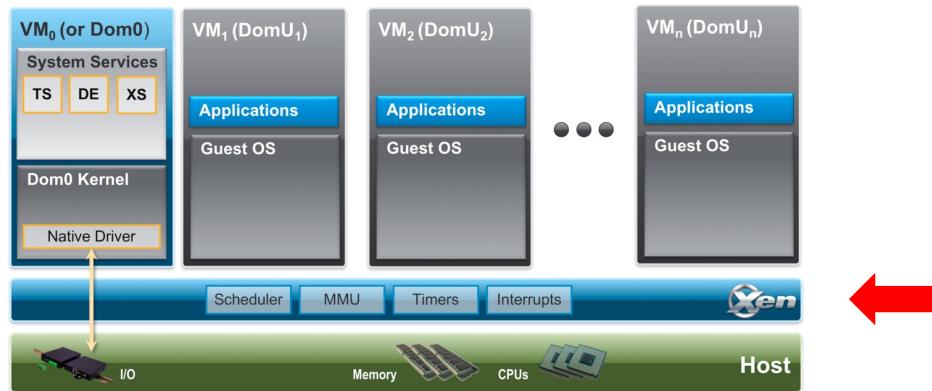
Xen architecture



https://wiki.xen.org/wiki/Xen_Project_Software_Overview

Xen architecture: hypervisor

- In charge of scheduling, memory management, interrupt and device control
- Per-domain and per-vCPU info management



Xen architecture: domains

- Domains
 - Represent VM instances, each running their own OS and apps
 - Run on virtual CPUs (vCPUs)
- DomU (unprivileged domain): guest VMs
 - Totally isolated from hw (i.e., no privilege to access hw or I/O functionality)
- Dom0 (control domain): special domain devoted to execution of Xen control functions and privileged instructions
 - Mandatory, initial domain started by Xen on boot
 - Contains drivers for all devices and some services: Device Emulation (DS), XenStore/XenBus (XS), and Toolstack (TS)
 - Special privileges: can access hw directly and system's I/O functions, can interact with other VMs

Dom0 components: XenStore and Toolstack

- **XenStore**: information storage space shared between domains managed by *xenstored* daemon
 - Stores configuration and status information
 - Implemented as hierarchical key-value storage
 - When values are changed in the store, a watch function notifies listeners (e.g., drivers) of changes of the key they have subscribed to
 - Communicates with guest VMs via shared memory using Dom0 privileges
- **Toolstack**: allows a user to manage VM lifecycle (create, shutdown, pause, migrate) and configuration
 - To create a new VM, a user provides a configuration file describing memory and CPU allocations and device configurations
 - Toolstack parses this file and writes this information in XenStore
 - Takes advantage of Dom0 privileges to map guest memory, to load kernel and virtual BIOS and to set up initial communication channels with XenStore and with virtual console when a new VM is created

CPU schedulers in Xen

- Hypervisor scheduler decides, among all the **virtual CPUs** (vCPUs) of the various VMs, which ones should execute on the physical CPUs (pCPUs)
 - Further scheduling level with respect to those provided by OS (scheduling of processes and scheduling of user-level threads within processes)
- Xen allows to choose among different CPU schedulers
 - We consider the **Credit** scheduler (default scheduler in Xen)
- **Scheduling algorithm goals:**
 - Make sure that domains get **fair** share of CPU
 - **Proportional share** algorithm: allocates pCPU in proportion to the number of shares (weights) assigned to vCPUs
 - Keep the CPU busy
 - **Work-conserving** algorithm: does not allow the CPU to be idle when there is work to be done
 - Schedule with low latency

Credit scheduler

- Proportional fair share and work-conserving scheduler
- Each domain is assigned a *weight* and optionally a *cap* (tunable parameters)
 - Weight: relative CPU allocation per domain (default 256)
 - Cap: maximum amount of CPU a domain can use
 - cap = 0 (default): vCPU can receive any extra CPU (i.e., work-conserving)
 - cap ≠ 0: limits amount of CPU that vCPU receives (e.g., 100 = 1 pCPU, 50 = 0.5 pCPU)
 - The scheduler transforms the weight into a *credit* allocation for each vCPU; as a vCPU runs, it consumes credits
- For each pCPU, the scheduler maintains a queue of vCPUs, with all the under-credit vCPUs first, followed by over-credit vCPUs; the scheduler picks the first vCPU in the queue
- Automatically load balances vCPUs across pCPUs on SMP host
 - Before a pCPU goes idle, it will consider other pCPUs in order to find any runnable vCPU; this approach guarantees that no pCPU idles when there is runnable work in the system wiki.xen.org/wiki/Credit_Scheduler

Performance comparison of hypervisors

- Developments in virtualization techniques and CPU architectures have reduced the performance cost of virtualization but still some overheads
 - Especially when multiple VMs compete for hw resources
- We consider two performance comparison studies
 - Papers on the course site
 - “Old” studies but overall message still valid
- Take-home message
 - No one-size-fits-all solution exists
 - Different hypervisors show different performance characteristics for varying workloads

Performance comparison of hypervisors

A component-based performance comparison of four hypervisors (IM 2013)

- Microsoft Hyper-V, KVM, VMware vSphere and Xen, all with *hardware-assisted virtualization* settings
- Analyzed components: CPU, memory, disk I/O and network I/O
- Results
 - Performance depends on type of virtualized hw resource, but no single hypervisor always outperforms the others
 - vSphere performs the best, but the others perform respectably
 - CPU and memory: lowest levels of overhead
 - I/O and network: Xen overhead for small disk operations
 - Takeaway: consider application type because **different hypervisors** may be best suited for **different workloads**

Performance comparison of hypervisors

Performance overhead among three hypervisors: an experimental study using Hadoop benchmarks (BigData 2013)

- Use Hadoop MapReduce apps to evaluate and compare the performance impact of three hypervisors
 - Commercial one (undisclosed), Xen, and KVM
- Results
 - For **CPU-intensive benchmarks**, negligible performance difference among hypervisors
 - For **I/O-intensive benchmarks** significant performance variations
 - Commercial hypervisor best at disk writing, KVM best for disk reading
 - Xen best when combination of disk reading and writing with CPU-intensive computations

VM portability

- **VM image**: copy of the VM containing a bootable OS, data files, and applications

- How to import and export VM images and avoid vendor lock-in?
- Open Virtualization Format (OVF)
 - Open industry standard (ISO 17203) for packaging and distributing VMs
 - Virtual-platform agnostic
 - Image stored in .ova file (Open Virtual Appliance)
 - VM configuration specified in XML format within a .ovx file
 - Supported by many virtualization products (Citrix, Hyper-V, VMware, VirtualBox, ...)
 - Not all: e.g., OpenStack does not support OVF packages

VM resizing and migration

- Useful techniques to deploy and manage large-scale virtualized environments
 - **Dynamic resizing** for **vertical scaling** (scale up, scale down)
 - **Live migration**
 - Move VM between different physical machines (or data centers) without stopping it

VM dynamic resizing

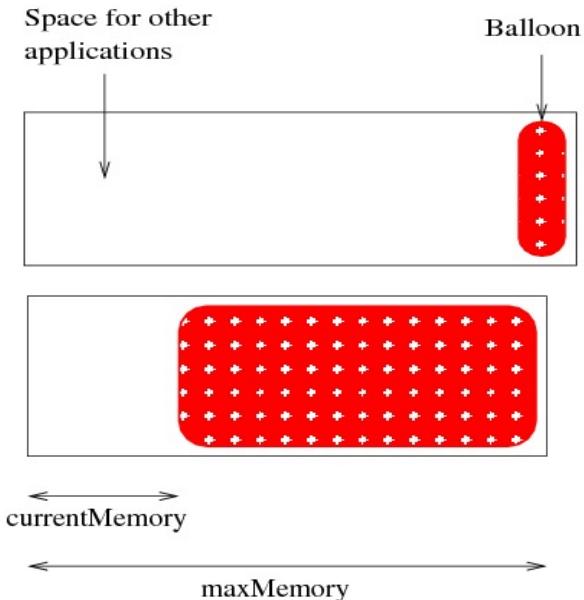
- Fine-grain mechanism with respect to VM migrating or rebooting
 - Example: app running on a VM starts consuming a lot of resources and VM starts running out of RAM and CPU
 - Solution: **dynamically resize the VM** (aka warm resize)
- Pros: more cost-effective and faster than VM reboot
- Cons: not supported by all virtualization products and guest OSs
- What can be resized *without* stopping and rebooting the VM?
 - Number of virtual CPUs
 - Memory

VM dynamic resizing: CPU

- To add or remove virtual CPUs (without VM turning off)
- Linux-based systems support **CPU hot-plug/hot-unplug**
https://www.kernel.org/doc/html/latest/core-api/cpu_hotplug.html
 - Uses information in virtual file system sysfs (processor info is in /sys/devices/system/cpu)
 - /sys/devices/system/cpu/cpuX for cpuX (X = 0, 1, 2, ...)
 - To turn on cpu #5:
echo 1 > /sys/devices/system/cpu/cpu5/online
 - To turn off cpu #5:
echo 0 > /sys/devices/system/cpu/cpu5/online
- Can be controlled using **virsh**
 - Command line tool to configure and manage virtual machines, available with some hypervisors (KVM, Xen)
 - To set the number of vCPUs while VM is running (cannot exceed the maximum number of vCPUs)
`virsh setvcpus <vm_name> <vcpu_count> --current`

VM dynamic resizing: memory

- Based on *memory ballooning*
 - Mechanism used by many hypervisors (e.g., KVM, Xen and VMware) to pass memory back and forth between hypervisor and guest OSs
 - In KVM: *virtio_balloon* driver
- When balloon deflates:
more memory for the VM
 - Anyway, VM memory size cannot exceed maxMemory
- When balloon inflates
 - Swap memory pages to disk



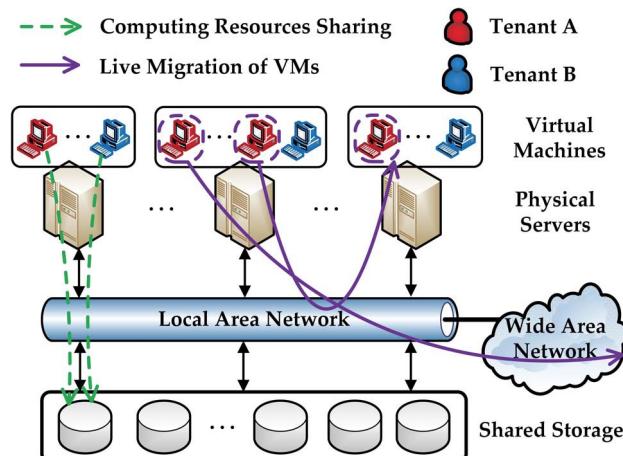
Migrazione di VM

- Vantaggi della migrazione
 - Utile in **cluster e data center virtuali** per:
 - Consolidare l'infrastruttura
 - Avere flessibilità nel **failover**
 - Bilanciare il carico
- Svantaggi e problemi
 - Supporto da parte del VMM
 - Overhead di migrazione non trascurabile
 - Migrazione in ambito WAN non banale

Migrazione di VM

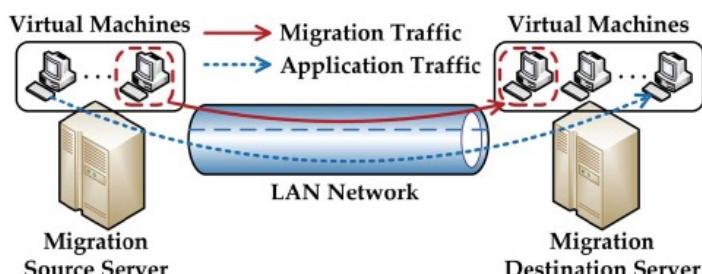
- Approcci per migrare istanze di macchine virtuali tra macchine fisiche:
 - Stop and copy:** si spegne la VM sorgente e si trasferisce l'immagine della VM sull'host di destinazione, ma il downtime può essere troppo lungo
 - L'immagine della VM può essere grande e la banda di rete limitata
 - Live migration:** la VM sorgente è in funzione **durante** la migrazione

Live migration largamente usata da Google: più di 1M di migrazioni al mese



Migrazione live di VM

- Prima di avviare la migrazione live
 - Fase di **setup**: si seleziona l'host di destinazione (ad es. con obiettivo di load balancing, energy efficiency, oppure server consolidation)
- Cosa migrare? **Memoria, storage e connessioni di rete**
- Come? In modo **trasparente** alle applicazioni in esecuzione sulla VM
 - Costo della migrazione live: vi è comunque un **downtime** dell'applicazione



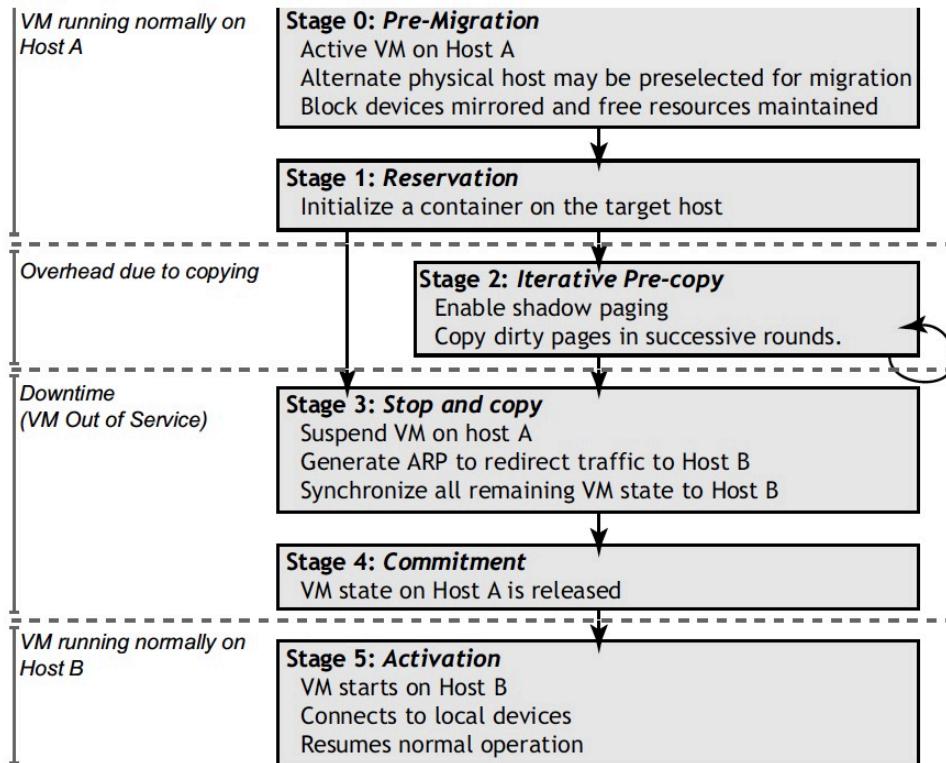
Migrazione live di VM: storage e rete

- Per migrare lo storage:
 - Usare storage condiviso da **host sorgente** e **destinazione**
 - SAN (Storage Area Network) o più economico NAS (Network Attached Server) o file system distribuito (e.g., NFS, GlusterFS o CEPH)
 - In assenza di storage condiviso: il VMM sorgente salva tutti i dati della VM sorgente in un file di immagine, che viene trasferito sull'host di destinazione
- Per migrare le connessioni di rete:
 - La VM sorgente ha un indirizzo IP virtuale (eventualmente anche indirizzo MAC virtuale)
 - Il VMM conosce il mapping tra IP virtuale e VM
 - Se sorgente e destinazione sono sulla stessa sottorete IP, non occorre fare forwarding sulla sorgente
 - Invio di risposta ARP non richiesta da parte della destinazione per avvisare che l'indirizzo IP è stato spostato in una nuova locazione ed aggiornare quindi le tabelle ARP

Migrazione live di VM: memoria

- Per migrare la memoria (inclusi registri della CPU e stato dei device driver):
 1. Fase di **pre-copy**: il VMM copia in modo **iterativo** le pagine da VM sorgente a VM di destinazione **mentre** la VM sorgente è in esecuzione
 - All'iterazione n copiate le pagine modificate durante iterazione $n-1$
 2. Fase di **stop-and-copy**: la VM sorgente viene fermata e vengono copiate pagine **dirty**, stato della CPU e dei device
 - Tempo di **downtime**: da qualche msec a qualche sec, in funzione di dimensione della memoria, tipo di app e banda di rete
 3. Fasi di **commitment** e **reactivation**: la VM di destinazione carica lo stato e riprende l'esecuzione; la VM sorgente viene rimossa (ed eventualmente spento l'host sorgente)
- Noto come approccio **pre-copy**
 - La memoria è copiata prima che l'esecuzione della VM riprenda a destinazione
 - Soluzione comune (es. KVM, VMWare, Xen, Google CE)

VM live migration: overall process



Source: C. Clark et al., "Live Migration of Virtual Machines", NSDI'05.
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VM live migration: alternatives for memory

- Pre-copy cannot migrate in a transparent manner memory-intensive apps
 - E.g., for write-intensive memory app, pre-copy is unable to transfer memory faster than memory is dirtied by running app
- Two alternative approaches
 - Post-copy
 - Hybrid
- **Post-copy**
 - CPU and device state are transferred immediately to destination host followed by transfer of execution control to destination host
 - Memory is fetched on-demand if needed by the running VM on the destination host (*pull* approach)
 - Reduces downtime and total migration time ✓
 - Incurs app degradation due to page faults which must be resolved over the network X

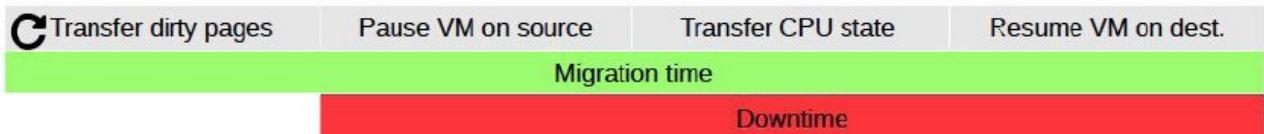
VM live migration: alternatives for memory

• Hybrid

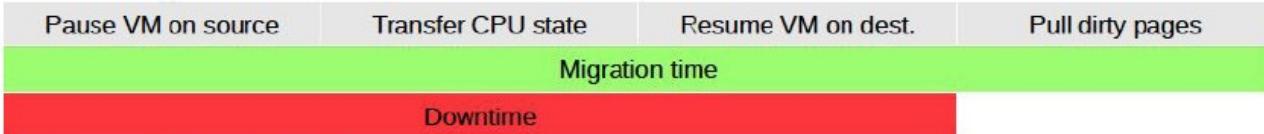
- Special case of post-copy migration: post-copy preceded by a bounded pre-copy stage
- Idea: transfer a subset of the most frequently accessed memory pages before VM execution is switched to the destination, so to reduce app performance degradation after the VM is resumed
- Pre-copy stage reduces the number of future network-bound page faults as a large portion of VM memory is already pre-copied ✓
- No standard implementation of post-copy and hybrid approaches in current hypervisors

Approaches for migrating memory

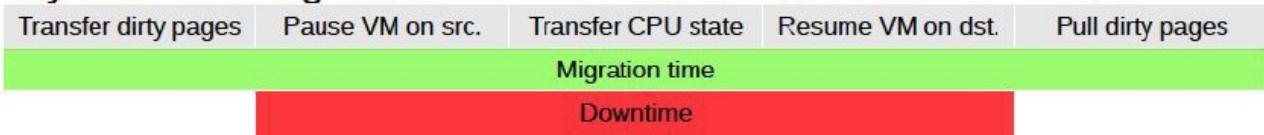
• Pre-copy Live Migration



• Post-copy Live Migration



• Hybrid Live Migration



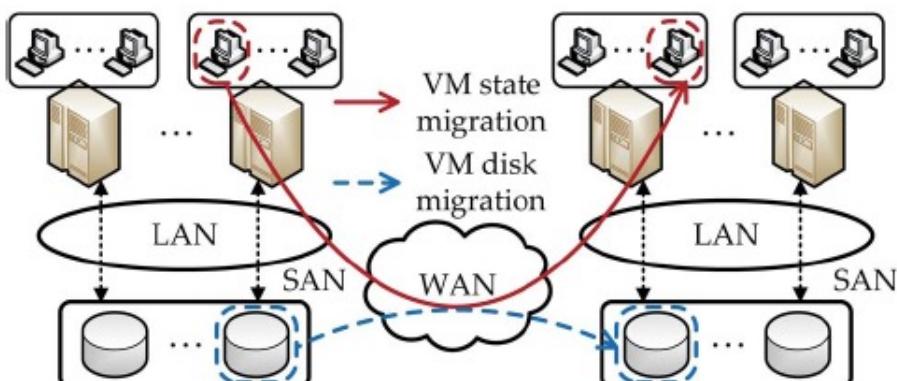
Courtesy of C.Vojtech, <http://bit.ly/2h7wSWB>

Live VM migration and hypervisors

- Live VM migration is supported by open-source and commercial hypervisors
 - E.g., KVM, Hyper-V, Xen, VirtualBox
- Can be controlled using `virsh` with different options
 - Virsh is a command line tool to configure and manage virtual machines, available with some hypervisors (KVM, Xen)
`$> virsh migrate --live [--undefinesource] [--copy-storage-all] [--copy-storage-inc] domain desturi`
`$> virsh migrate-setmaxdowntime domain downtime`
`$> virsh migrate-setspeed domain bandwidth`
`$> virsh migrate-getspeed domain`

VM migration in WAN environments

- How to achieve live migration of VMs across multiple geo-distributed data centers?
 - Key challenge: maintain network connectivity and preserve open connections during and after migration
 - Limited support in open-source and commercial hypervisors



VM migration in WAN environments: storage

- Approaches to migrate storage in WAN
 - **Shared storage**
 - Cons: storage access time can be too slow
 - **On-demand fetching**
 - Transfer only some blocks to destination and then fetch remaining blocks from source only when requested
 - Cons: does not work if source crashes
 - **Pre-copy/write throttling**
 - Pre-copy VM disk image to destination whilst VM continues to run, keep track of write operations on source (delta) and then apply delta on destination
 - If the write rate at the source is too fast, use write throttling to slow down the VM so that migration can proceed

VM migration in WAN environments: network

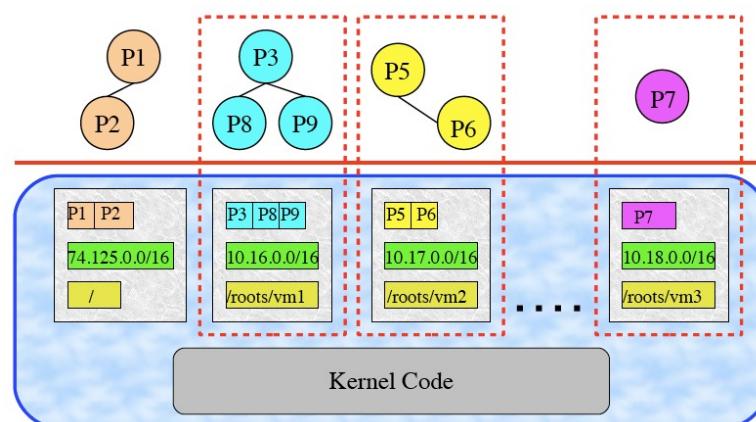
- Approaches to migrate network connections in WAN
 - **IP tunneling**
 - Set up an IP tunnel between the old IP address at source VM and the new IP address at destination VM
 - Use tunnel to forward all packets that arrive at source VM for the old IP address
 - Once migration has completed and the VM can respond at its new location, update the DNS entry with the new IP address
 - Tear down the tunnel when no connections remain that use the old IP address
 - Cons: does not work if source VM crashes
 - **Virtual Private Network (VPN)**
 - Use MPLS-based VPN to create the abstraction of a private network and address space shared by multiple data centers
 - **Software-Defined Networking**
 - Change the control plane, no need to change IP address!

Virtualizzazione a livello di SO

- Finora abbiamo considerato la virtualizzazione a livello di sistema
- Analizziamo ora la **virtualizzazione a livello di sistema operativo** (o **container-based virtualization**)
- Permette di eseguire molteplici ambienti di esecuzione tra di loro isolati all'interno di un **singolo SO**
 - Tali ambienti sono chiamati:
 - **container**
 - **jail**
 - **zone**
 - **virtual execution environment (VE)**

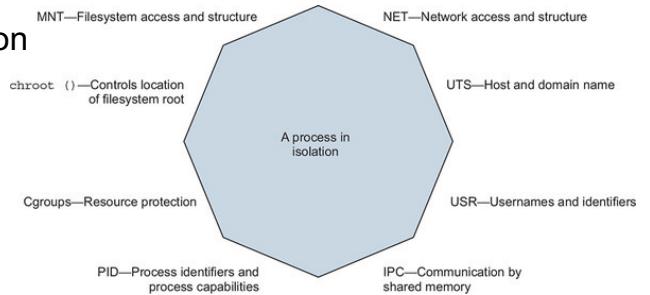
OS-level virtualization

- OS kernel allows the existence of multiple isolated user space instances, called **containers**
- Each container has:
 - Its own set of processes, file systems, users, network interfaces with IP addresses, routing tables, firewall rules, ...
- Containers share the same OS kernel (e.g., Linux)



OS-level virtualization: mechanisms

- Which kernel mechanisms to manage containers?
 - Need to isolate processes from each other in terms of sw and hw (CPU, memory, ...) resources
- Main mechanisms offered by Unix-like OS kernel
 - `chroot` (change root directory)
 - Allows to change the apparent root folder for the current running process and its children
 - `cgroups` (Linux-specific)
 - Manage resources for groups of processes
 - `namespaces` (Linux-specific)
 - Per-process resource isolation



Mechanisms: namespaces

- Feature of Linux kernel that allows to isolate what a set of processes can see in the operating environment (processes, ports, files, ...)
- Kernel resources are partitioned so that one set of processes sees one set of resources, while another set of processes sees a different set of resources
- Different types of namespaces

Mechanisms: namespaces

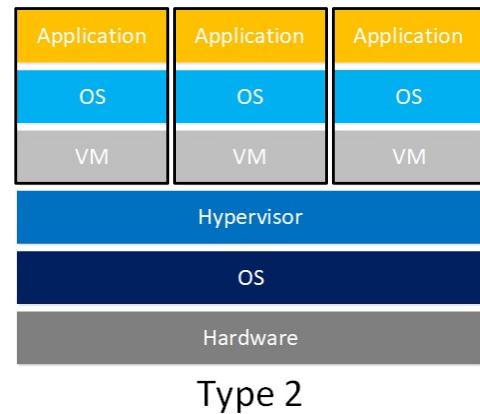
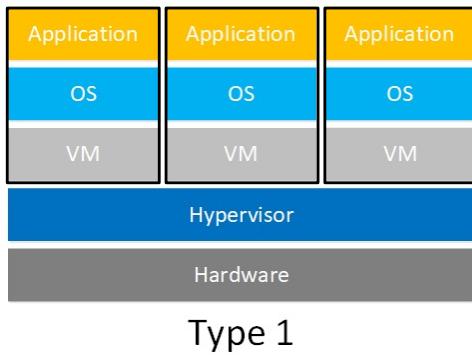
- **mnt**: isolates mount points
- **pid**: isolates the PID space, so that each process only sees itself and its children (PID 1, 2, 3, ...)
- **network**: allows each container to have its dedicated network stack: its own private routing table, set of IP addresses, socket listing, firewall, and other network-related resources
- **user**: isolates user and group IDs, e.g., allowing a non-root user on the host to be mapped with the root user within the container
- **uts** (Unix timesharing): provides dedicated host and domain names
- **ipc**: provides dedicated shared memory for IPC, e.g., different Posix message queues

Mechanisms: cgroups

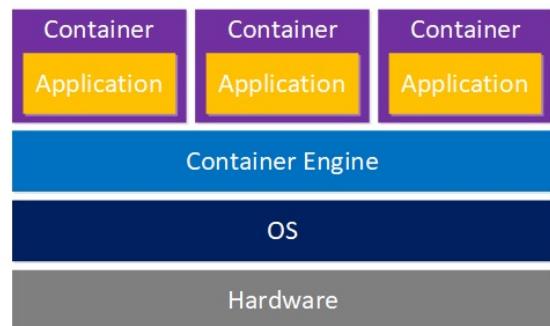
- Allows to limit, measure and isolate the use of resources (CPU, memory, block I/O, network) of a set of processes
- Low-level filesystem interface similar to sysfs and procfs
 - Default location in /sys/fs/cgroup

OS-level virtualization: pros

- VMM-based (type 1) vs container-based virtualization



In a nutshell: lightweight
vs. heavyweight



OS-level virtualization: pros

With respect to VMM-based virtualization

- Minimal performance degradation
 - Apps invoke system calls directly, without VMM indirection
- Minimum startup and shutdown times
 - Seconds per container, minutes per VM
- High density
 - Hundreds of instances on a single physical machine (PM)
- Smaller image (footprint)
 - Does not include the OS kernel
- Ability to share memory pages among multiple containers running on same PM
- Increased portability and interoperability
- Containerized apps independent of execution environment

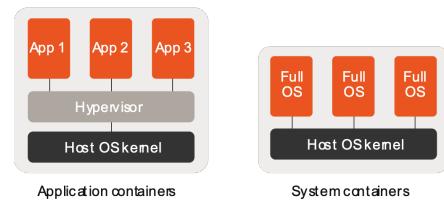
OS-level virtualization: cons

With respect to VMM-based virtualization

- Less flexibility
 - You cannot run different OS kernels simultaneously on same PM
- Only native applications for supported OS (e.g., native applications for Linux)
- Less isolation
- Greater risk of vulnerability
 - Single vulnerability in OS kernel can compromise entire system

OS-level virtualization: products

- Docker
 - The most popular application container engine
- FreeBSD Jail
- Solaris Zones/Containers
- LXC (LinuX Containers)
 - Supported by mainline Linux kernel
 - Provides *system containers* (full OS image), while Docker provides *application containers*
 - LXD
 - System container manager built on top of LXC
- Virtuozzo Containers
- OpenVZ (for Linux)
- rkt
 - Application container engine

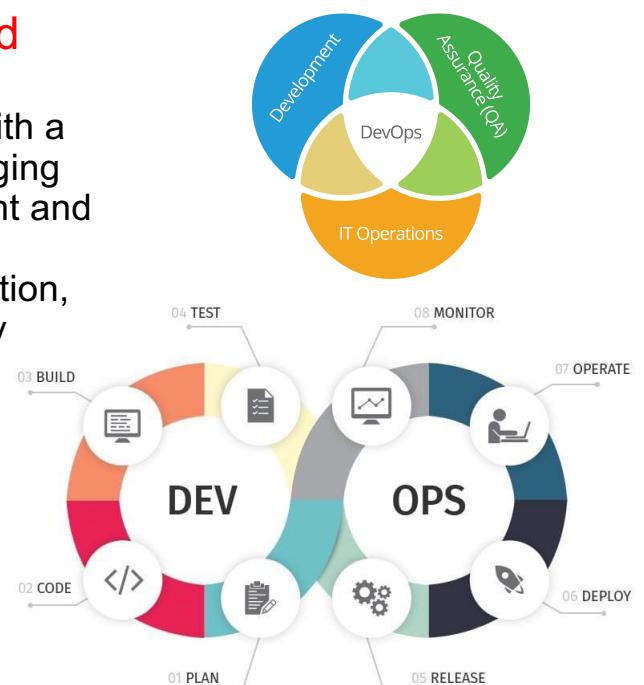


OS-level virtualization: only Linux?

- Windows and OS X support container-based virtualization
 - See Docker Desktop
- Alternative: install a VM with Linux as guest OS and use a container-based virtualization product inside the VM
 - Cons: performance loss
 - Cons: containerized apps must run on Linux (no OS X or Windows native applications)

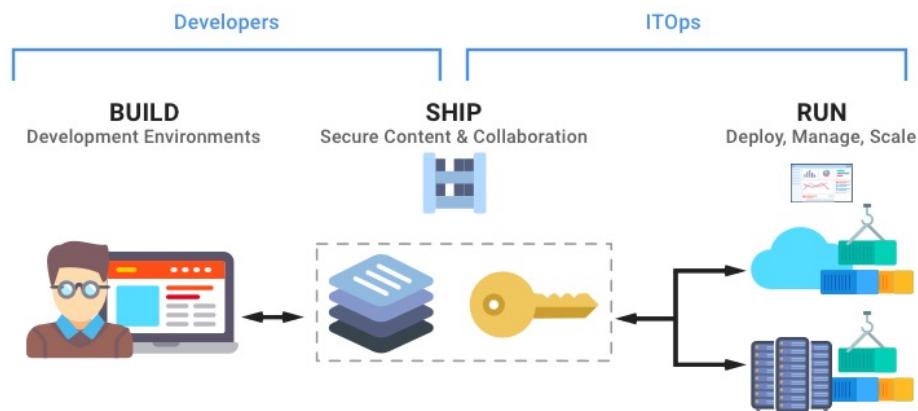
DevOps

- Containers help in the shift to **DevOps** and **CI/CD** (Continuous Integration and Continuous Deployment)
- **DevOps = Development and Operations**
 - Development methodology with a set of practices aimed at bridging the gap between Development and Operations, emphasizing communication and collaboration, continuous integration, quality assurance and delivery with automated deployment



Containers and DevOps

- Containers provide a new way to **build, package, share, and deploy apps**
 - Containers (more than VMs) allow developers to build code collaboratively by sharing images while simplifying deployment to different infrastructures

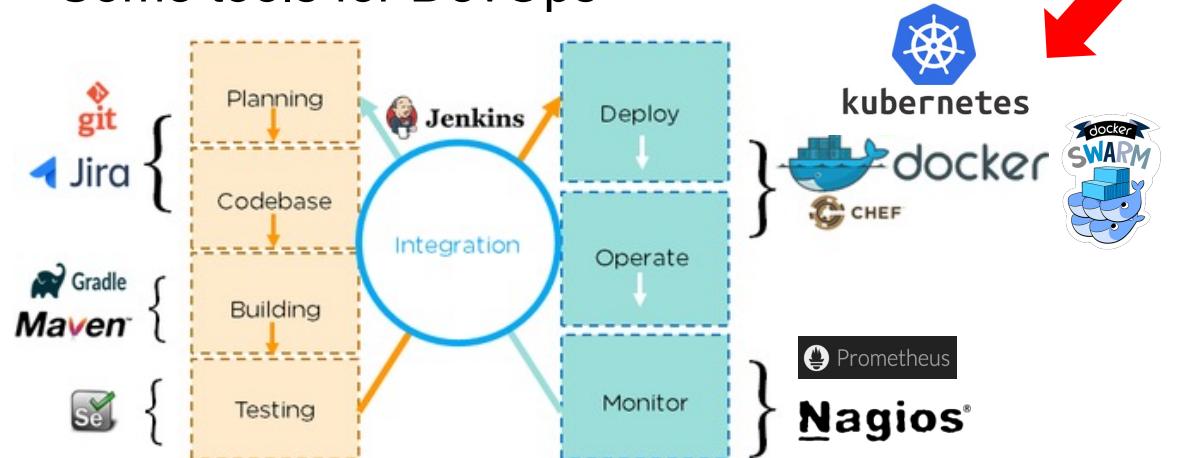


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Containers and DevOps

- Some tools for DevOps



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Containers, microservices, and serverless

- Using containers
 - Application and all its dependencies into a single package that can run almost anywhere
 - Use fewer resources than traditional VMs
- Containers are also a key enabling technology for **microservices** and **serverless computing**
 - Microservices and functions can be wrapped as containers

Docker

- Let's go into the details of Docker

<http://www.ce.uniroma2.it/courses/sdcc2122/slides/Docker.pdf>

Salta alle slide su Docker, poi ritorna qui.

Container dynamic resizing

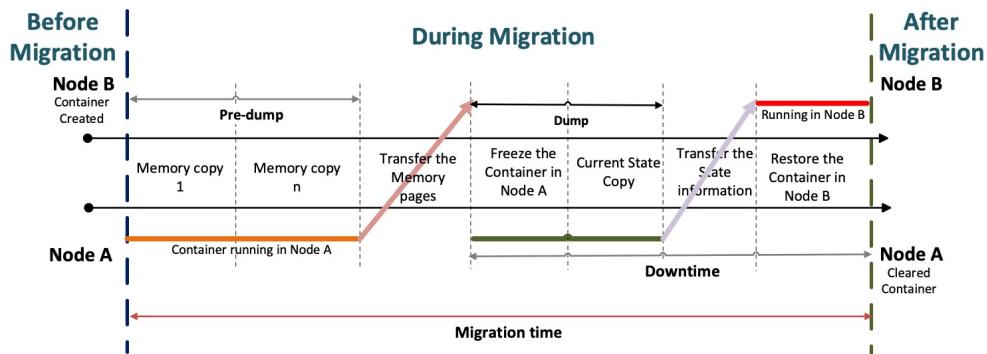
- As for VMs, we can resize and migrate containers
- Dynamic resizing (CPU, memory, I/O) of container limits
 - Possibly without restarting the container, but depends on the container engine
 - Low-level solution: cgroups limits can be changed on the fly

Live migration of containers

- As for VM migration, we need to:
 - Save state
 - Transfer state
 - Restore from state
- State saving, transferring and restoring happen with apps frozen: migration downtime
 - Use memory pre-copy or memory post-copy
- No native support in container engines, requires additional tool CRIU
- We also need to migrate container image (and volumes) and network connections

Live migration of containers

- Use **CRIU** tool to support live migration (in Docker and other container engines) through checkpointing and restoration technique
 - During checkpoint, CRIU freezes the running container at source and collects information about its CPU state, memory content, and process tree
 - Collected information is passed on to destination, and the container is resumed
 - How to with Docker (experimental)



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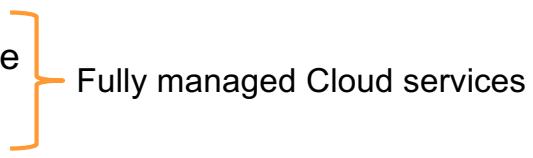
Containers in the Cloud

- Containers and container development platforms as first-class Cloud services
- **Container-as-a-Service (CaaS)**
 - Amazon Elastic Container Service (ECS)
 - Two launch modes: EC2 and Fargate
 - Azure Container
 - Google Container Engine
 - Alauda (Container-as-a-Service solution) <https://www.alauda.io>
 - Docker Cloud

Hypervisors and containers in the Cloud

- Which virtualization technology for IaaS providers?
 - Hypervisor-based virtualization: greater security, isolation, and flexibility (different OSs on same PM)
 - Container-based virtualization: smaller-size deployment, reduced startup and shutdown times
- Some questions
 - Containers inside VMs or on top of bare metal?
 - Will containers replace VMs in Cloud offering or the other way?
 - New trend: combine security and isolation properties provided by hypervisors with speed and flexibility of containers
 - Firecracker: open source virtualization technology by Amazon built for creating and managing secure, multi-tenant container and function-based services. Based on KVM but with minimalist design. Runs workloads in lightweight VMs, called micro VMs
<https://firecracker-microvm.github.io>

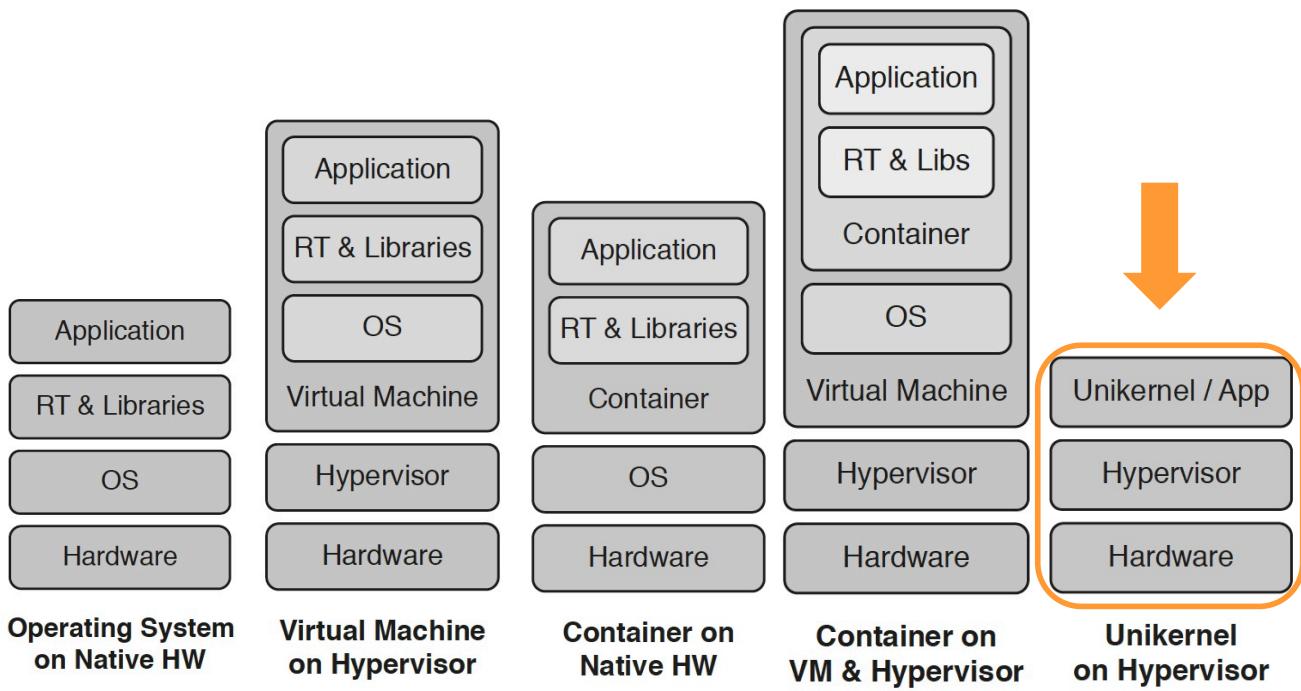
Container orchestration

- Platforms for managing the deployment of multi-container packaged applications in large-scale clusters
- Allow to configure, provision, deploy, monitor, and dynamically control containerized apps
 - Used to integrate and manage containers at scale
- Examples
 - **Docker Swarm** (see Docker slides)
 - **Kubernetes** (next lesson)
 - Amazon Elastic Container Service
 - Google Kubernetes Engine
 - Marathon

Fully managed Cloud services

New lightweight approaches to virtualization

- Deployment strategies examined so far



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New lightweight approaches to virtualization

- Microservices, serverless computing, IoT, fog/edge computing, and Network Function Virtualization (NFV) increasingly demand for **low-overhead** (or **lightweight**) virtualization techniques
 - OS-level virtualization is not enough
 - Most apps do not require many of the services and tools coming with common OSs (shells, editors, core utils, and package managers)
 - How to have tiny, one-shot VMs that run on hypervisors with great density and self-scale their resource needs?
 - How to improve security?
- Lightweight OSs and unikernels**
 - Basic idea: avoid OS overhead and reduce attack surface

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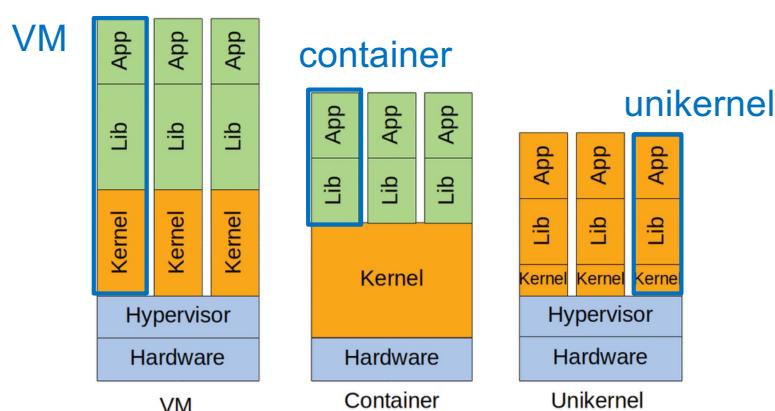
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Lightweight operating systems

- Minimal, container-focused OSes, typically with a monolithic kernel architecture
 - E.g.,: Fedora CoreOS, Rancher OS
- **Fedora CoreOS** <https://getfedora.org/it/coreos/>
 - Minimal, monolithic and compact Linux distribution
 - Only minimal functionalities required for deploying apps inside containers, together with built-in mechanisms for service discovery, container management and configuration sharing
 - Designed for large-scale deployments, mostly targeting enterprises, with focus on automation, ease of application deployment, security, and scalability
 - Can be also installed on bare metal

Unikernels

- **Unikernel** (or library OS) <http://unikernel.org>
 - *Single-purpose, single-language* virtual machine hosted on a minimal environment
 - Specialized OS with minimal set of libraries which correspond to OS constructs required for app to run, all in a *single address space*



Unikernels: pros and cons

- Pros:
 - Lightweight and small (minimal memory footprint)
 - Fast (no context switching)
 - Secure (reduced attack surface)
 - Fast boot (measured in ms)
- Cons:
 - Only work in hypervisor-based virtual environments
 - Poor debugging
 - Single language runtime

See <https://www.youtube.com/watch?v=oHcHTFleNtg>

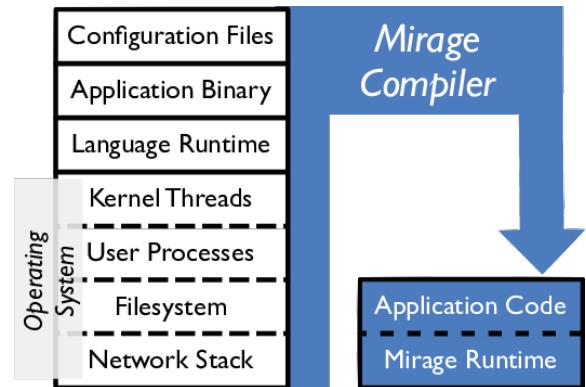
Unikernels: products

- Some unikernel products (and supported programming language):
 - OS^V (C++, Go, Python and Java, ...)
 - IncludeOS (C++)
 - MirageOS (OCaml)
- OS^V
 - Unikernel designed to run single unmodified Linux application securely as microVM on top of a hypervisor (e.g., KVM, Xen, Firecracker)
 - Goal: isolation benefits of hypervisors without overhead of guest OS
 - To run app on OS^V, you need to build an image by fusing OSv kernel and app files together

Unikernels: products

- MirageOS

- Code can be developed on a normal OS (e.g., Linux) and then compiled into a fully-standalone, specialised unikernel that runs on top of Xen or KVM hypervisor
- Code can be developed in OCaml, a high-level functional programming language



Performance of virtualization approaches

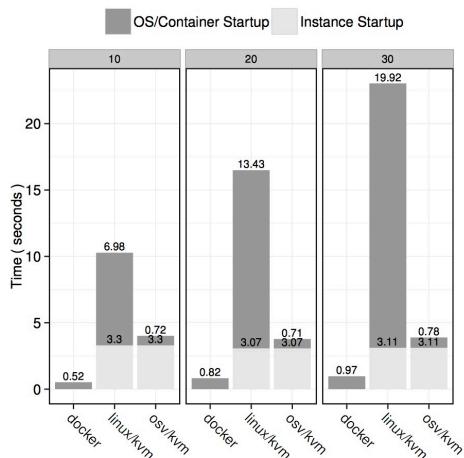
- Some performance studies compare hypervisor vs. lightweight virtualization
- Overall result: overhead introduced by containers is almost negligible
 - Fast instantiation time
 - Small per-instance memory footprint
 - High density
- ... but paid in terms of security

Virtualization	Instantiation time	Image size	Memory footprint	Programming language dependance	Live migration support
VM	~5/10 sec	~1 GB	~100 MB	No	Yes
Container	~0.8/1 sec	~50 MB	~5 MB	No	Non-native
Unikernel	<50 msec	<50 MB	~8 MB	Yes	No

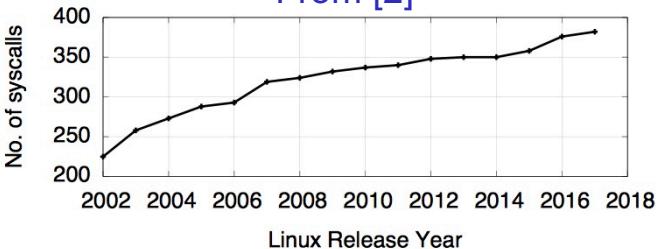
Source: [Consolidate IoT edge computing with lightweight virtualization](#)

Performance of virtualization approaches

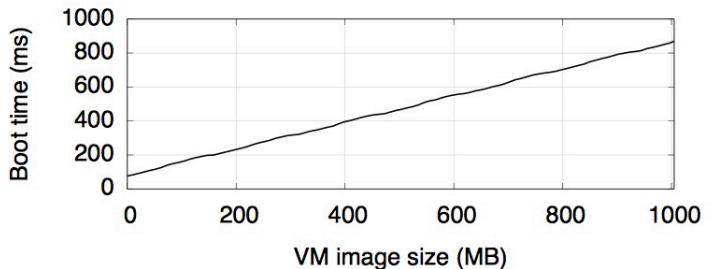
From [1]



From [2]



- Difficulties in securing containers due to growth of Linux syscall API



- VM boot times grow linearly with VM size

[1] [Time provisioning evaluation of KVM, Docker and Unikernels in a cloud platform](#),
CCGrid'16

[2] [My VM is lighter \(and safer\) than your container](#), SOSP'17