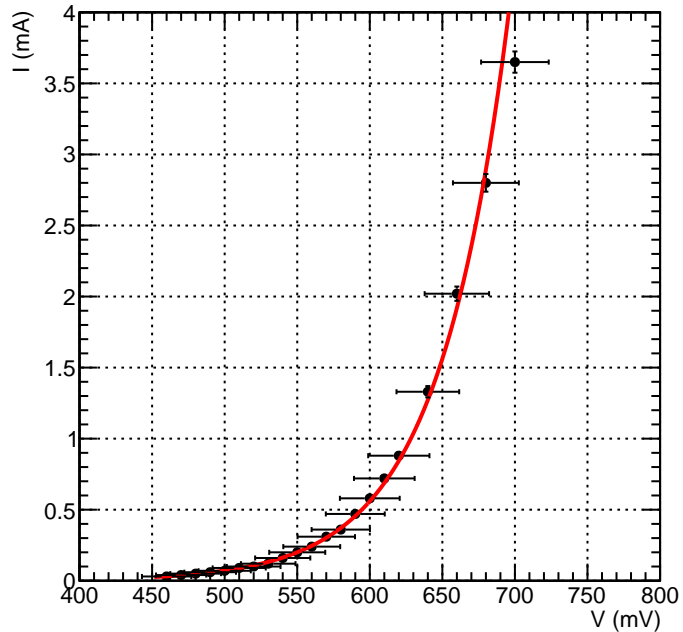


I vs V



Silicio\_log

