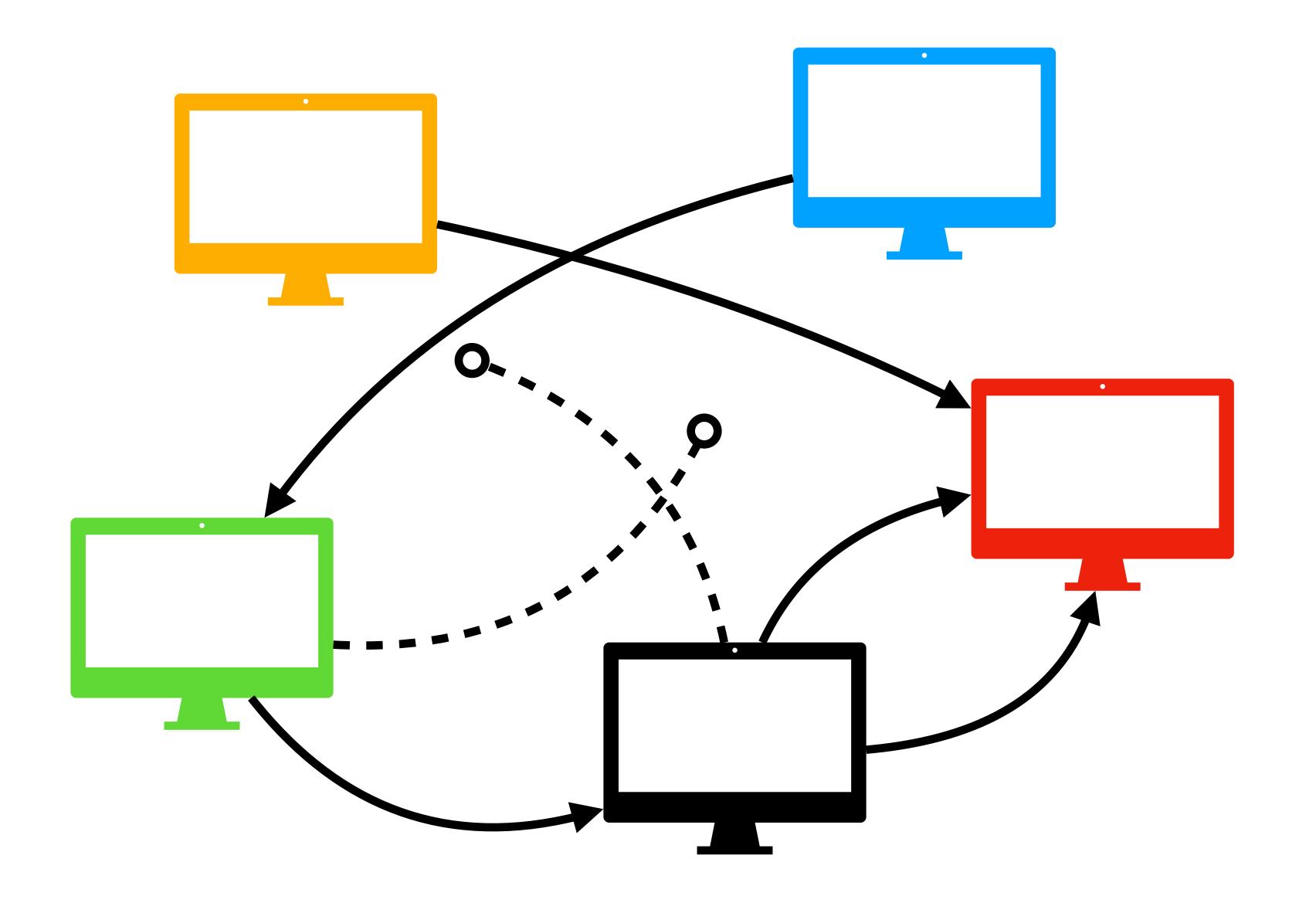


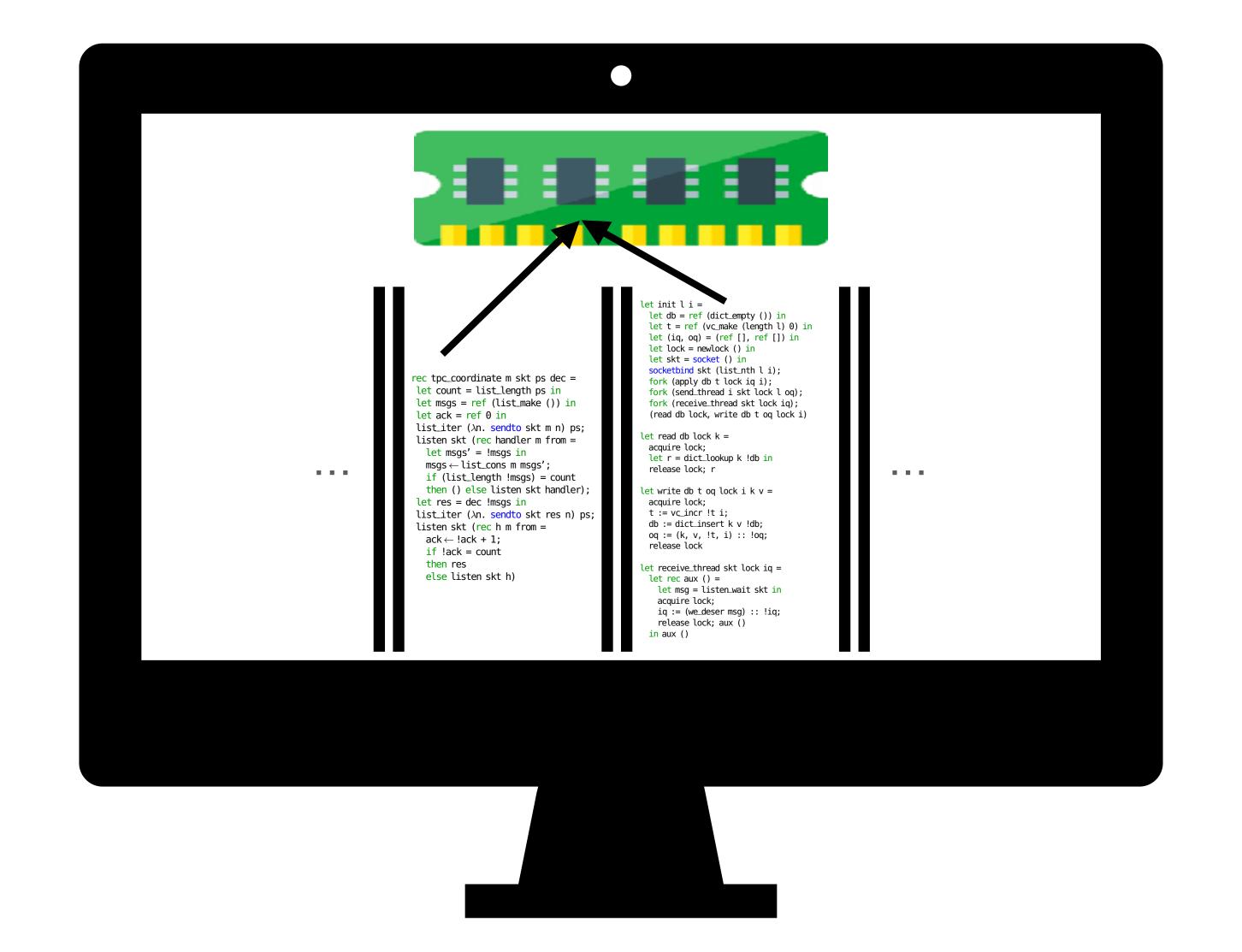
Aneris

A Mechanised Logic for Modular Reasoning about Distributed Systems March 29, 2021 @ ESOP'20

Simon Oddershede Gregersen

joint work with Morten Krogh-Jespersen, Amin Timany, Marit Edna Ohlenbusch, and Lars Birkedal





This work

- An ML-like language, AnerisLang, with higher-order store, node-local concurrency and datagram-like network sockets
- A separation logic, **Aneris**, for modular reasoning about partial correctness properties of **implementations** of distributed systems while allowing
 - Vertical composition
 - Horizontal composition
- All theory and examples are mechanised on top of the Iris framework in the Coq proof assistant

In separation logic, propositions denote ownership of resources, e.g., $\ell \mapsto v$. Specifications only talk about the footprint of the program.

$$\{P\} e \{v.Q\}$$

These specifications can be lifted through framing and binding.

$$\frac{\{P\} e \{v.Q\}}{\{P*R\} e \{v.Q*R\}} \qquad \frac{\{P\} e \{v.Q\} \qquad \forall v.\{Q\} K[v] \{w.R\}}{\{P\} K[e] \{w.R\}}$$

In concurrent separation logic, threads are considered one at a time and we need not reason about inter-leavings of threads explicitly.

$$\frac{\{P_1\} e_1 \{v_1.Q_1\}}{\{P_1 * P_2\} e_1 \mid\mid e_2 \{v_1, v_2.Q_1 * Q_2\}}$$

Through resource invariants, we can express protocols on shared state.

$$\exists v. \ell \mapsto v * even(v)$$

In Aneris, a distributed separation logic, we extend these reasoning principles to allow reasoning about nodes one at a time.

$$\frac{\{P_1 * \mathsf{FreePorts}(ip_1, A)\} \, e_1 \, \{\mathsf{True}\}}{\{P_1 * P_2 * \mathsf{FreeIp}(ip_1) * \mathsf{FreeIp}(ip_2)\} \, e_1 \, |_{ip_1} ||_{ip_2} \, e_2 \, \{\mathsf{True}\}}$$

If
$$A \cap B = \emptyset$$
 then

 $\mathsf{FreePorts}(ip,A) * \mathsf{FreePorts}(ip,B) \dashv \mathsf{FreePorts}(ip,A \cup B)$

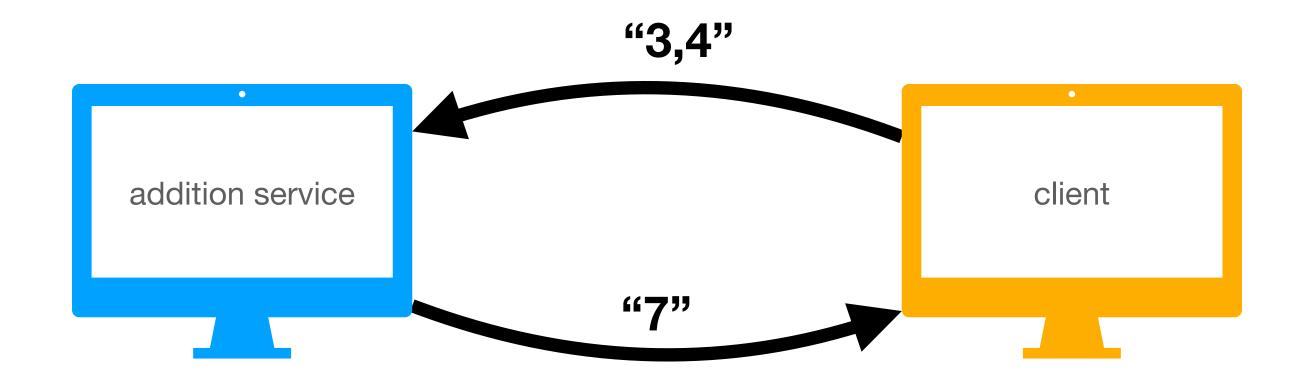
To express protocols on communication, we introduce socket protocols.

$$\Phi: Message \rightarrow iProp$$

With each socket address (pair of an ip an a port) we associate a protocol.

$$a \mapsto \Phi$$

An addition service



```
rec server a =
  let skt = socket () in
  socketbind skt a;
  listen skt (rec handler msg from =
     let (n, m) = deserialize msg in
     let res = serialize (n + m) in
     sendto skt res from;
  listen skt handler)
```

```
rec client x y srv a =
  let skt = socket () in
  socketbind skt a;
  let m = serialize (x, y) in
  sendto skt m srv;
  let res = listenwait skt in
  deserialize res
```

We primordially fix a socket protocol for the server.

$$\begin{split} \Phi_{add}(m) &\triangleq \exists \Psi : \textit{Message} \rightarrow \textit{iProp}, (x,y:\mathbb{N}). \\ &\text{body}(m) = serialize(x,y) * \\ &\text{from}(m) \mapsto \Psi * \\ &\forall m'. \, \text{body}(m') = serialize(x+y) \twoheadrightarrow \Psi(m') \end{split}$$

```
\{(ip,p) \mapsto \Phi_{add} * \text{FreePorts}(ip,\{p\})\}

\text{server}(ip,p)

\{False\}
```

```
\{(ip, p) \Rightarrow \Phi_{add} * FreePorts(ip, \{p\})\}
  (rec server a =
    let skt = socket () in
    socketbind skt a;
    listen skt (rec handler msg from =
      let (n, m) = deserialize msg in
      let res = serialize (n + m) in
      sendto skt res from;
      listen skt handler)) (ip, p)
 False
```

```
(rec server a =
  let skt = socket () in
  socketbind skt a;
  listen skt (rec handler msg from =
     let (n, m) = deserialize msg in
     let res = serialize (n + m) in
     sendto skt res from;
  listen skt handler)) (ip, p)

[False]
```

 $(ip, p) \Rightarrow \Phi_{add}$ FreePorts $(ip, \{p\})$

```
let skt = socket () in
socketbind skt (ip, p);
listen skt (rec handler msg from =
  let (n, m) = deserialize msg in
  let res = serialize (n + m) in
  sendto skt res from;
  listen skt handler)
```

False

 $(ip, p) \Rightarrow \Phi_{add}$ FreePorts $(ip, \{p\})$

```
\{h \hookrightarrow \mathsf{None}\}
  let skt = h in
  socketbind skt (ip, p);
  listen skt (rec handler msg from =
     let (n, m) = deserialize msg in
     let res = serialize (n + m) in
     sendto skt res from;
     listen skt handler)
 False
```

 $(ip, p) \mapsto \Phi_{add}$ FreePorts $(ip, \{p\})$

```
let skt = h in socketbind skt (ip, p); listen skt (rec handler msg from = let (n, m) = deserialize msg in let res = serialize (n + m) in sendto skt res from; listen skt handler)

False
```

 $(ip, p) \Rightarrow \Phi_{add}$ FreePorts $(ip, \{p\})$ $h \hookrightarrow \mathsf{None}$

```
socketbind h (ip, p);
listen h (rec handler msg from =
  let (n, m) = deserialize msg in
  let res = serialize (n + m) in
  sendto h res from;
  listen h handler)
False
```

```
(ip,p) \mapsto \Phi_{add}
FreePorts(ip,\{p\})
h \hookrightarrow \mathsf{None}
```

```
\{h \hookrightarrow \mathsf{Some}\ (ip,p)\}

listen h (rec handler msg from = let (n, m) = deserialize msg in let res = serialize (n + m) in sendto h res from; listen h handler)
```

$$(ip,p) \Rightarrow \Phi_{add}$$

```
listen h (rec handler msg from = let (n, m) = deserialize msg in let res = serialize (n + m) in sendto h res from; listen h handler)

False
```

$$(ip,p) \Rightarrow \Phi_{add}$$
 $h \hookrightarrow \mathsf{Some}\ (ip,p)$

```
\{ \varPhi_{add}(m) \} let (n, m) = deserialize body(m) in let res = serialize (n + m) in sendto h res from(m)
```

$$(ip,p) \mapsto \Phi_{add}$$
 $h \hookrightarrow \mathsf{Some}\ (ip,p)$

```
let (n, m) = deserialize serialize(x, y) in let res = serialize (n + m) in sendto h res from(m)
```

```
\begin{array}{c} \Psi: \textit{Message} \rightarrow \textit{iProp} \\ x,y: \mathbb{N} \\ \\ (ip,p) \mapsto \Phi_{add} \\ h \hookrightarrow \mathsf{Some} \ (ip,p) \\ \\ \mathsf{from}(m) \mapsto \Psi \\ \\ \forall m'. \, \mathsf{body}(m') = serialize(x+y) \\ \\ \twoheadrightarrow \Psi(m') \end{array}
```

sendto h serialize(x+y) from(m)

$$\begin{array}{c} \Psi: \textit{Message} \rightarrow \textit{iProp} \\ x,y: \mathbb{N} \\ \\ (ip,p) \mapsto \Phi_{add} \\ h \hookrightarrow \mathsf{Some} \ (ip,p) \\ \\ \mathsf{from}(m) \mapsto \Psi \\ \forall m'. \, \mathsf{body}(m') = serialize(x+y) \\ \\ \twoheadrightarrow \Psi(m') \end{array}$$

```
 \{ srv \mapsto \Phi_{add} * \mathsf{FreePorts}(ip, \{p\}) \}   \mathsf{client} \ x \ y \ srv \ (ip, p)   \{ v.v = x + y \}
```

```
let skt = socket() in socketbind skt (ip, p); let m = serialize(x, y) in sendto skt m srv; let res = listenwait skt in deserialize res
```

$$\{v.v = x + y\}$$

 $srv \mapsto \Phi_{add}$ $\mathsf{FreePorts}(ip, \{p\})$

```
let m = serialize (x, y) in sendto h m srv; let res = listenwait h in deserialize res
```

$$\{v.v = x + y\}$$

$$srv \mapsto \Phi_{add}$$
 $h \hookrightarrow \mathsf{Some}\ (ip,p)$

```
sendto h serialize(x, y) srv;
let res = listenwait h in
deserialize res
```

$$\{v.v = x + y\}$$

$$\begin{split} \Phi_{add}(m) \triangleq &\exists \Psi : \textit{Message} \rightarrow \textit{iProp}, (x,y:\mathbb{N}). \\ & \text{body}(m) = serialize(x,y) * \\ & \text{from}(m) \mapsto \Psi * \\ & \forall m'. \, \text{body}(m') = serialize(x+y) \twoheadrightarrow \Psi(m') \end{split}$$

$$srv \mapsto \Phi_{add}$$
 $h \hookrightarrow \mathsf{Some}\ (ip, p)$

sendto h serialize(x, y) srv; let res = listenwait h in deserialize res

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$$(ip, p) \Rightarrow \Phi_{client}$$
 $srv \Rightarrow \Phi_{add}$ $h \hookrightarrow \mathsf{Some}\ (ip, p)$

$$\Phi_{client}(m) \triangleq \mathsf{body}(m) = serialize(x+y)$$

let res = listenwait h in deserialize res

$$\{v.v = x + y\}$$

$$(ip, p) \Rightarrow \Phi_{client}$$
 $srv \Rightarrow \Phi_{add}$
 $h \hookrightarrow \mathsf{Some}\ (ip, p)$

$$\Phi_{client}(m) \triangleq \mathsf{body}(m) = serialize(x+y)$$

```
\{\Phi_{client}(m)\} let res = body(m) in deserialize res
```

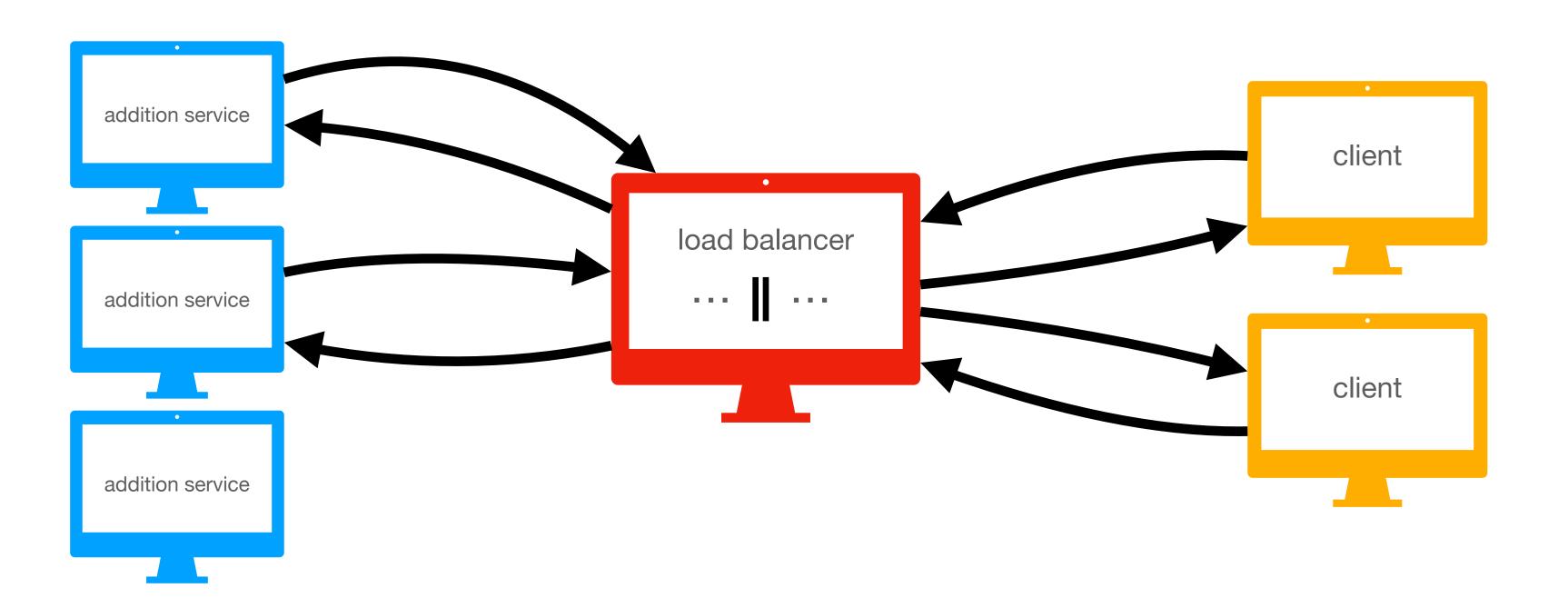
 $\{v.v = x + y\}$

$$(ip, p) \Rightarrow \Phi_{client}$$
 $srv \Rightarrow \Phi_{add}$ $h \hookrightarrow \mathsf{Some}\ (ip, p)$

$$\Phi_{client}(m) \triangleq \mathsf{body}(m) = serialize(x+y)$$

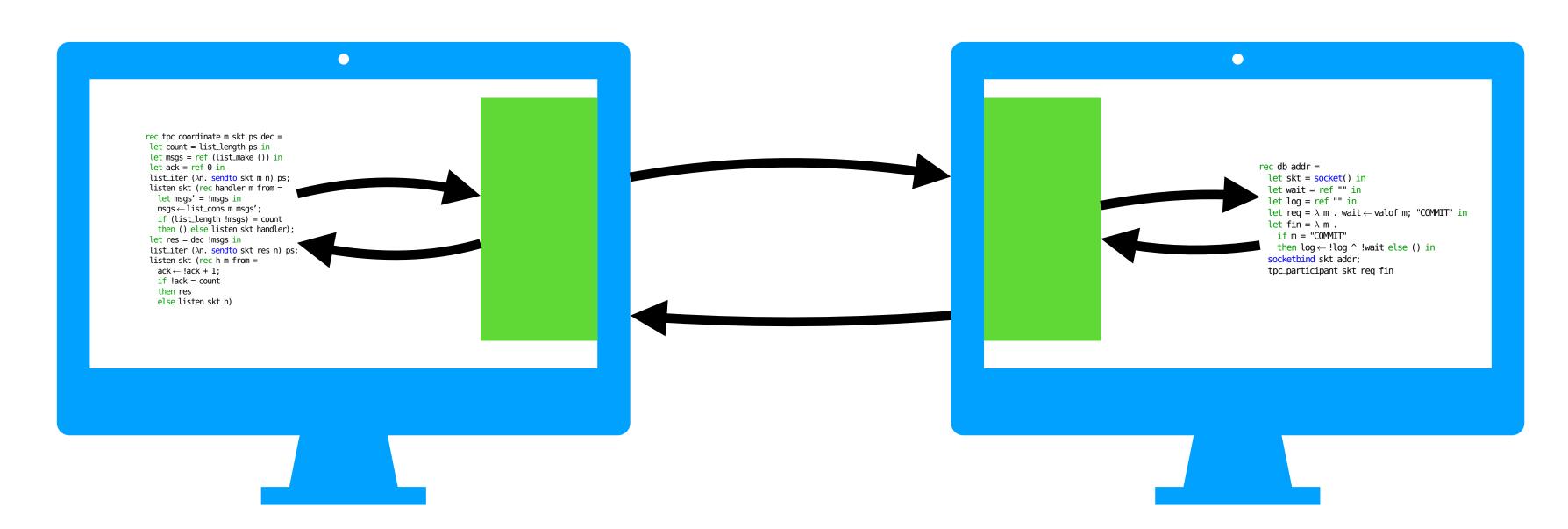
Horizontal composition

- The addition service showcases horizontal compositional reasoning
- In the paper we also introduce a load balancer, but use the existing specifications for the server and client to verify the system



Vertical composition

- In the paper we showcase vertical compositional reasoning by implementing and verifying the two-phase commit protocol
- We use the two-phase commit implementation and specification as a library to implement and verify a replicated logging system



Distributed Causal Memory @ POPL'21

Modular Specification and Verification in Higher-Order Distributed Separation Logic

An implementation, modular specification, and verification of a causally-consistent distributed database and its clients in Aneris.

Léon Gondelman, Simon Oddershede Gregersen, Abel Nieto, Amin Timany, and Lars Birkedal. 2021. Distributed Causal Memory: Modular Specification and Verification in Higher-Order Distributed Separation Logic. Proc. ACM Program. Lang. 5, POPL, Article 42 (January 2021), 29 pages. https://doi.org/10.1145/3434323







Distributed Causal Memory: Modular Specification and Verification in Higher-Order Distributed Separation Logic

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We present the first specification and verification of an implementation of a causally-consistent distributed database that supports modular verification of full functional correctness properties of clients and servers. We specify and season about the causally-consistent distributed database in Aneris, a higher-order distributed separation logic for an MI-like programming language with network primitives for programming distributed systems. We demonstrate that our specifications are useful, by proving the correctness of small, but tricky, synthetic examples involving causal dependency and by varifying a session manager library implemented on top of the distributed database. We use Ameris's facilities for modular specification and verification to obtain a highly modular development, where each component is verified in isolation, solying only on the specifications (not the implementations) of other components. We have used the Coq formalization of the Aneris logic to formalize all the results presented in the paper in the Coq proof assistant.

 $CCS\ Concepts: Theory\ of\ computation \rightarrow Program\ verification;\ Distributed\ algorithms;\ Separation$

Additional Key Words and Phrases Distributed systems, causal consistency, separation logic, higher-order logic, concurrency, formal verification

ACM Reference Format:

Léon Gondelman, Simon Oddershede Gregorsen, Abel Nieto, Amin Timany, and Lars Borkedal. 2021. Distributed Causal Memory: Modular Specification and Verification in Higher-Order Distributed Separation Logic. Proc. ACM Program, Lang. 5, POFL, Article 42 (January 2021), 29 pages, https://doi.org/10.1145/3434323

The abiquitous distributed systems of the present day internet often require highly available and scalable distributed data storage solutions. The CAP theorem [Gilbert and Lynch 2000] states that a distributed database cannot at the same time provide consistency, availability, and partition (failure) tolerance. Hence, many such systems choose to sacrifice aspects of data consistency for the sake of availability and foult tolerance [Bailis et al. 2013; Chang et al. 2008; Lloyd et al. 2011; Tyulenev et al. 2019]. In those systems different replicas of the database may, at the same point in time, observe different, inconsistent data. Among different notions of weaker consistency guarantees, a popular one is causal consistency. With causal consistency different replicas can observe different data, yet, it is guaranteed that data are observed in a causally related order: if a node n observes an operation Authors' addresses: Léan Gondelman, Aarhus University. Denmark, gandelman@es.au.dig Simon Oddershede Gregersen

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Conclusion

- A programming language and a separation logic, Aneris, for modular reasoning about partial correctness properties of implementations of distributed systems
- Scalable development and verification of distributed systems through vertical and horizontal compositional reasoning
- Extensive case studies showcasing our reasoning principles
- All theory and examples are mechanised on top of the Iris separation logic framework in the Coq proof assistant



Thank you for watching

Contact

Paper

Coq development

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https://cs.au.dk/~gregersen/papers/2020-esop-aneris-final.pdf

https://github.com/logsem/aneris