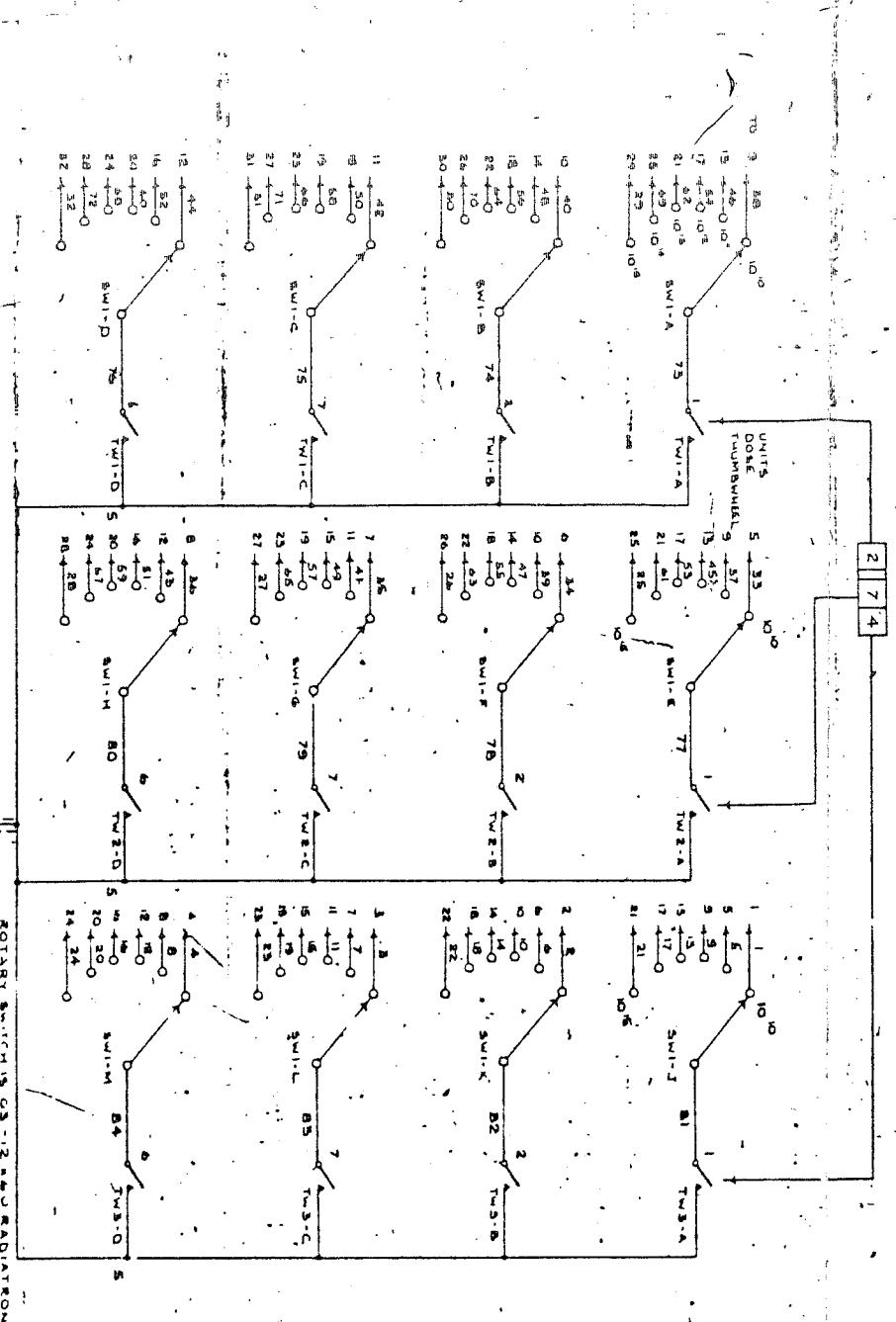
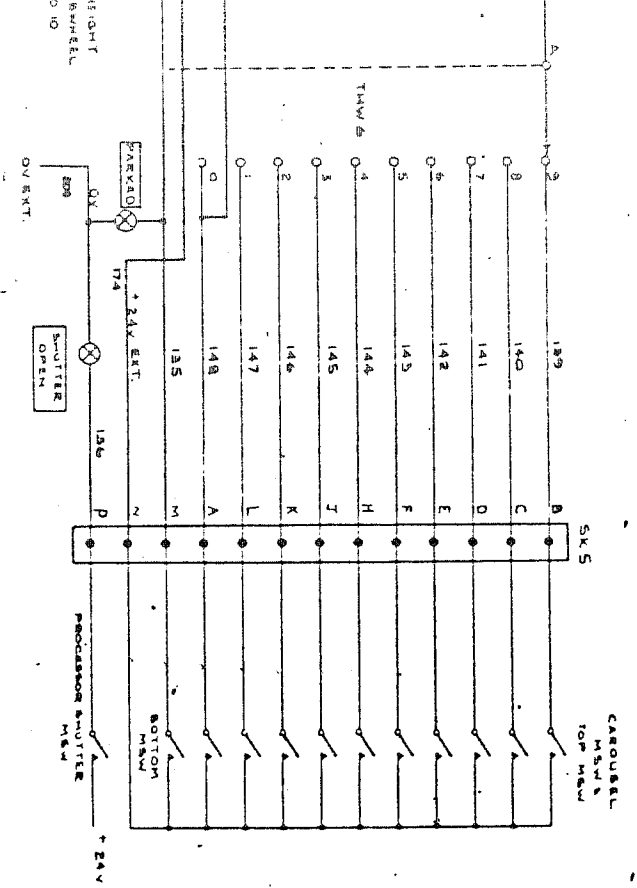


DRAWING

IN DOUBT - ASK



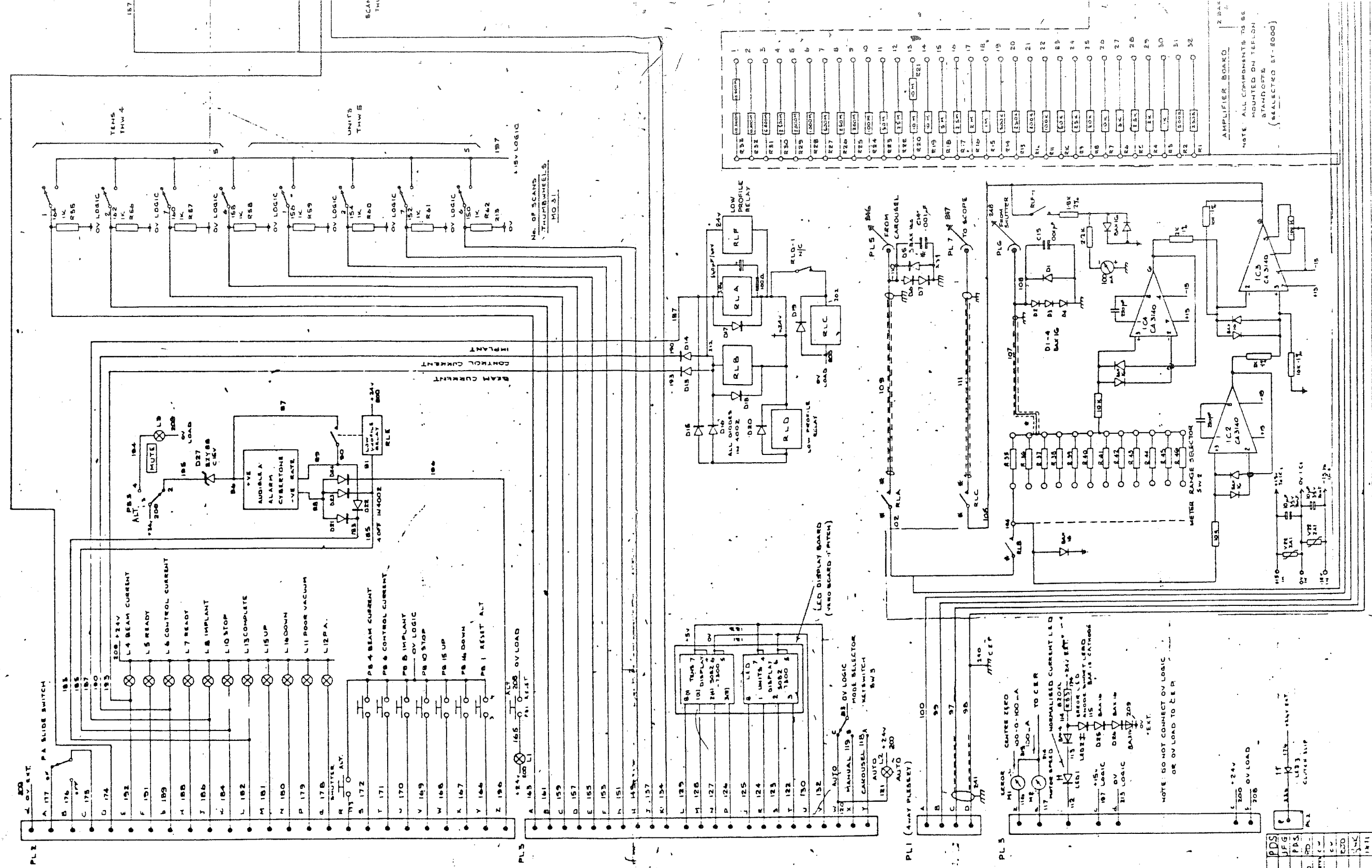
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267	Q
268	R
269	S
270	T
271	U
272	V
273	W
274	X
275	Y
276	Z

THIS DES IS THE MODIFIED MASTER WITH
BLANK VALUES ON WAFER SWITCH - WAS
USED TO MAKE CURRENT SEC MASTERS FOR

DIMENSIONS	MATERIAL	TOLERANCES	PROTECTIVE FINISH	UNIT
DRAWN <i>gwk</i>				PROCESSOR CONTROL
CHICKEN	SURFACE ROUGHNESS	UNLESS OTHERWISE	SCALE	UNIT
1001 N°	ESTIMATED WEIGHT	STATED		

PL. 6
BY WAFER CONVERSION
PL. A

DATE	BY	REV
11/11/11		
11/11/11		
11/11/11		



NOTE: ALL CONNECTION POINTS MARKED * SHOULD BE MOUNTED ON TELEPHON STAND-OFFS AND KEPT CLEAR OF OTHER WIRING

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AMPLIFIER BOARD
NOTE: ALL COMPONENTS TO BE MOUNTED ON TELEPHON STAND-OFFS (SELECTED BY 2000)

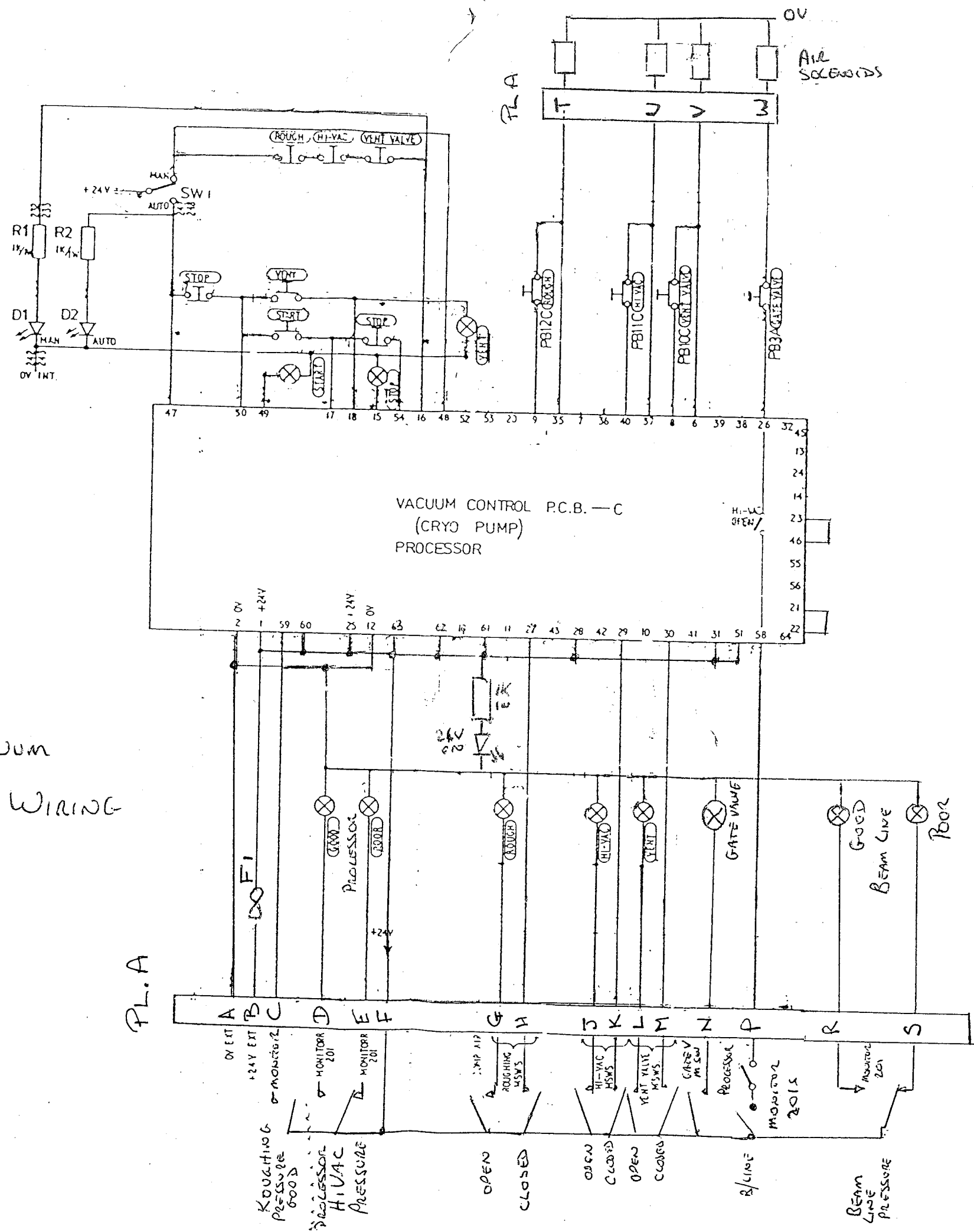


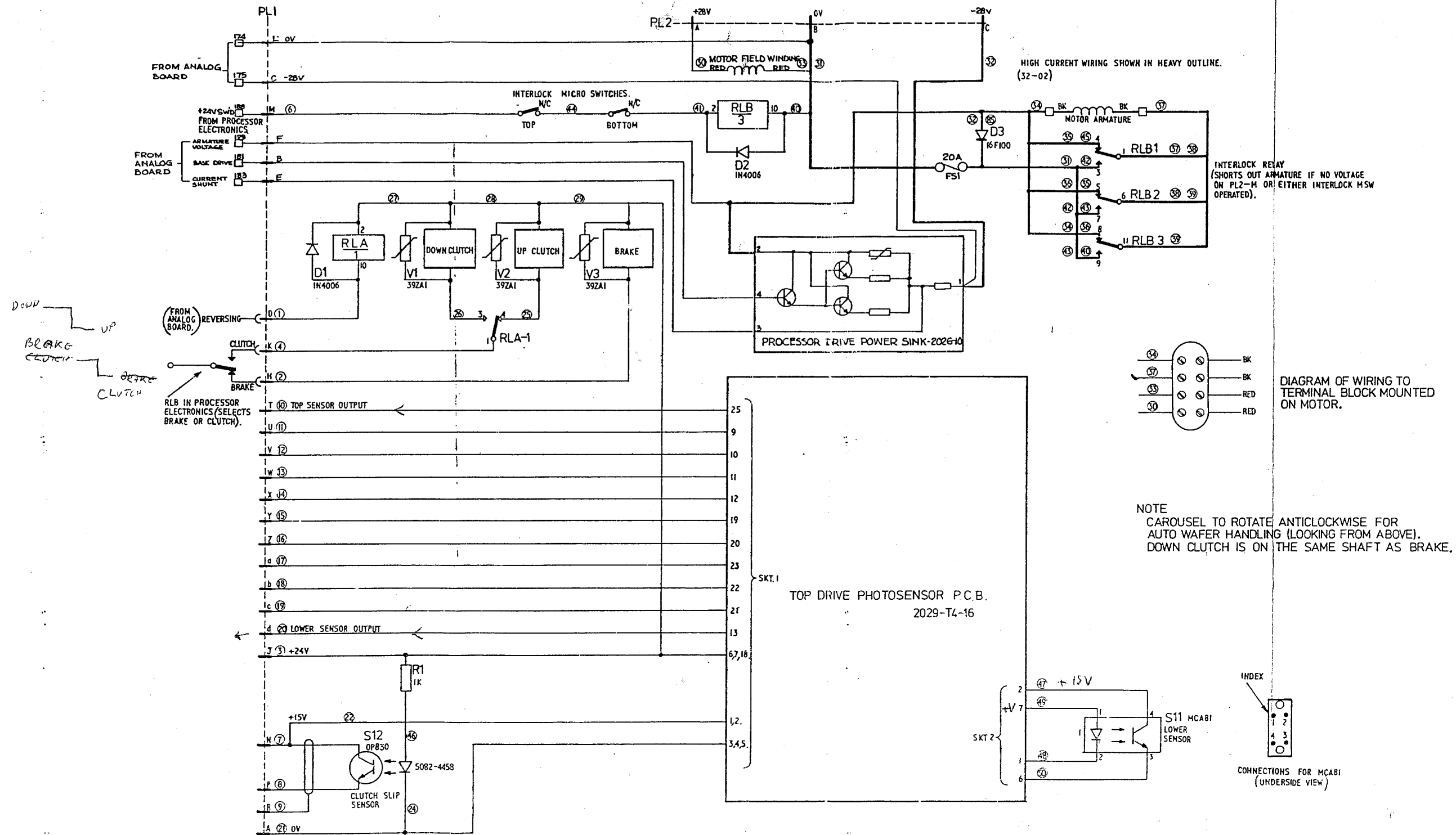
**** NOTE** IC 7 IN CAROUSEL REVERSING RELAY CIRCUIT DRAWING
ABOVE IS REPLACED BY IC 12 ON CLC 1 ~~CIRCUIT~~ BOARD. 7-31

Ques 2+ SPARE BOARD STILL USE IC7 AS PER DRAWING

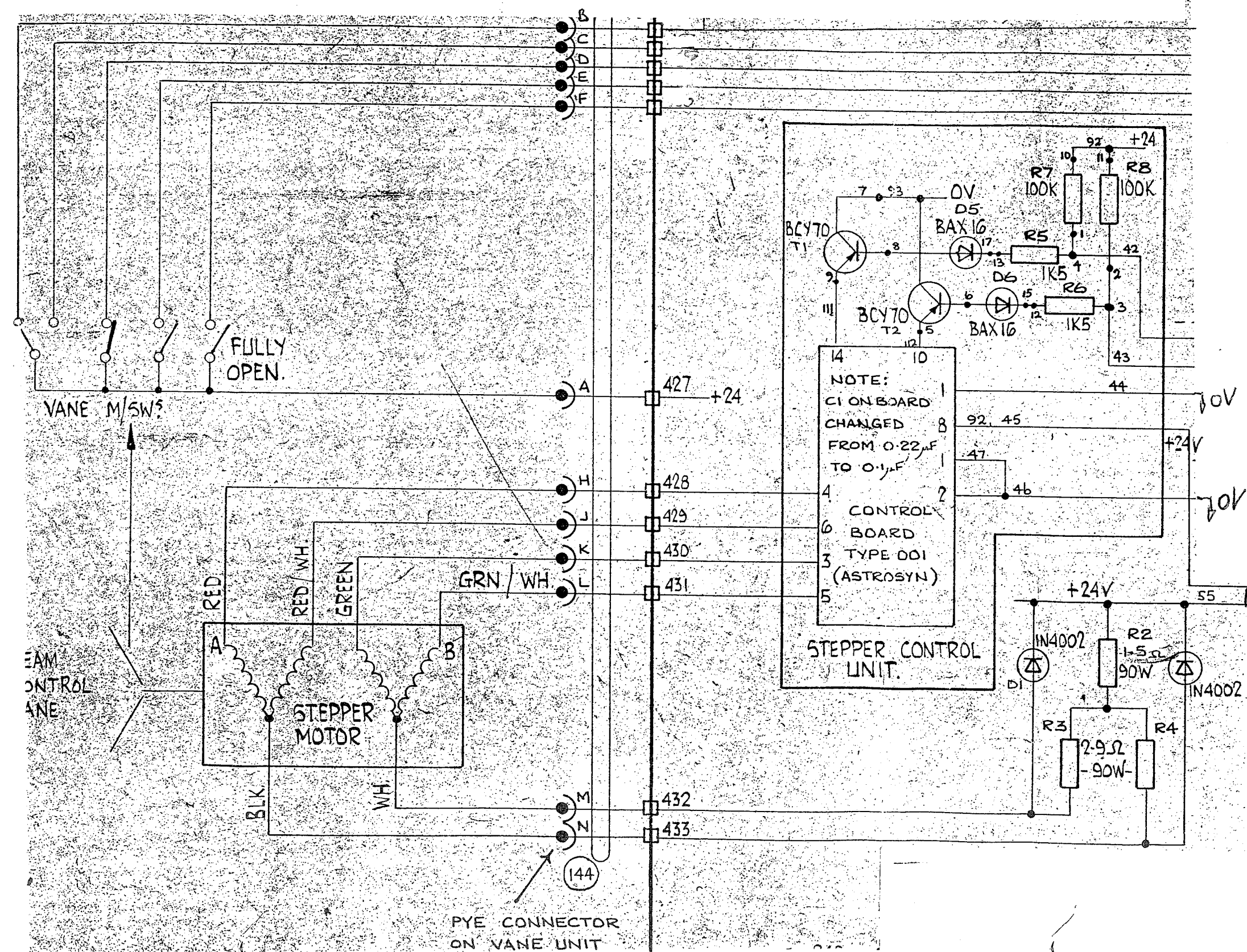


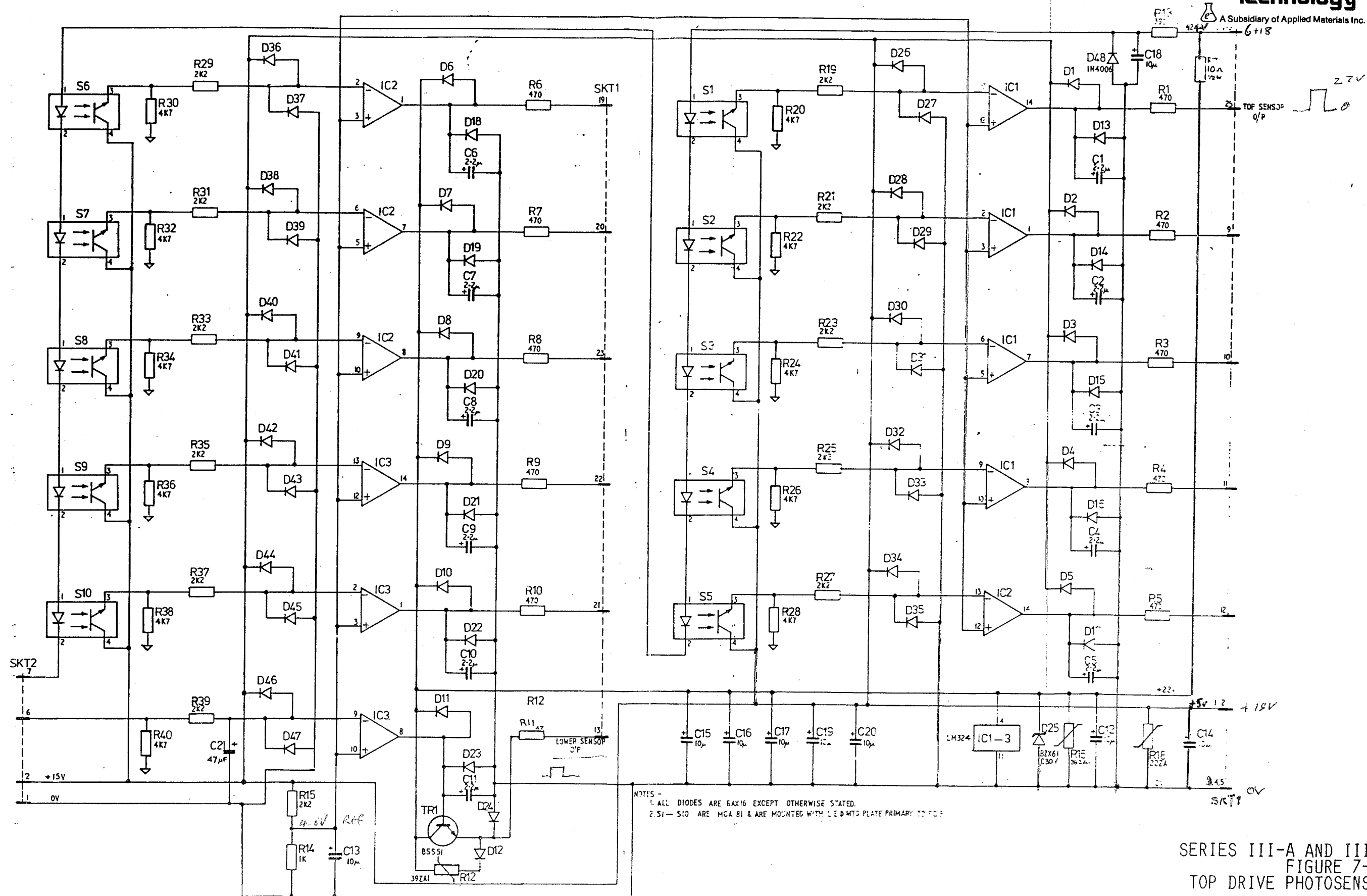
VACUUM CONTROL WIRING



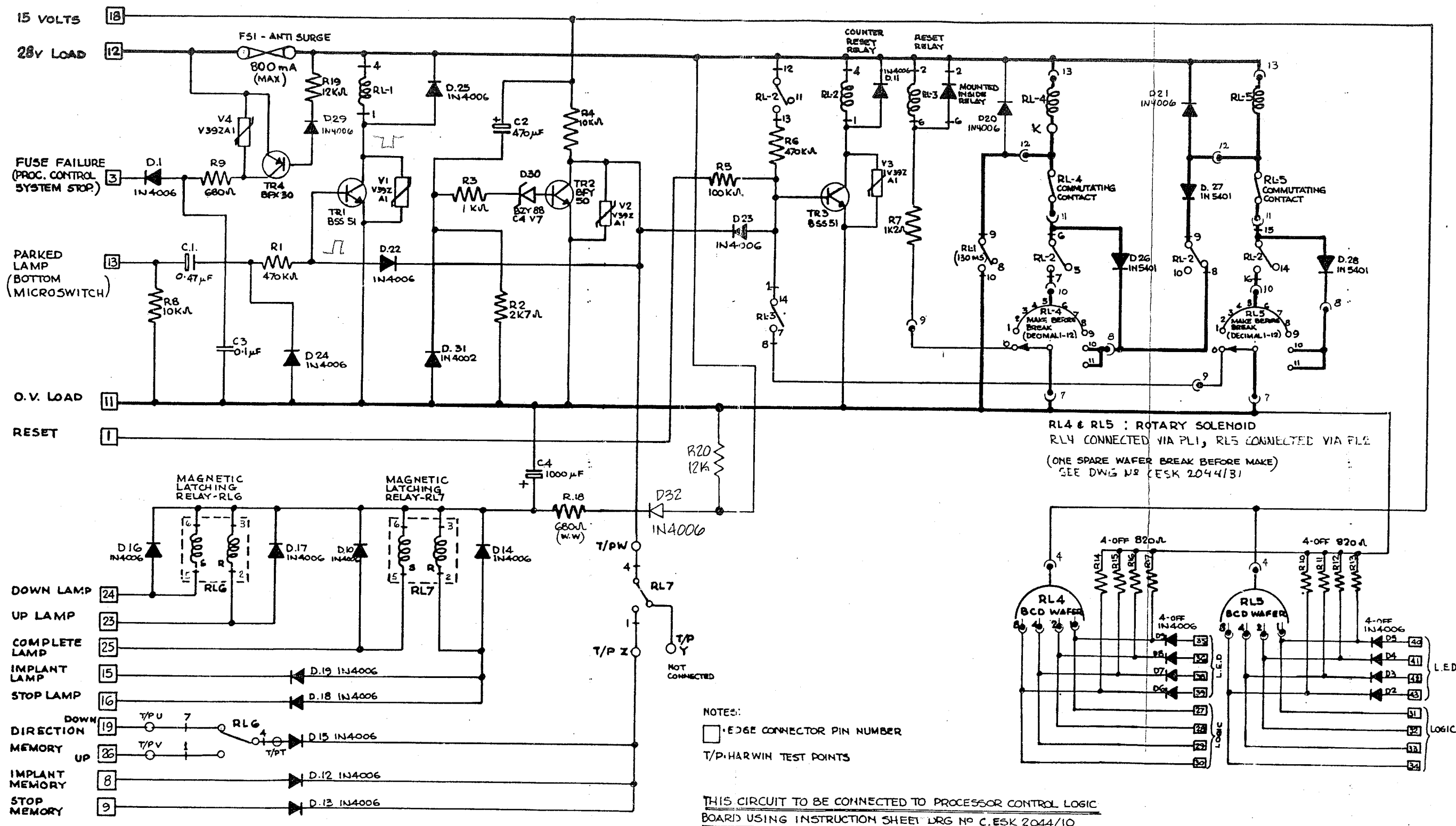


SERIES III-A AND III-X
FIGURE 7-31
PROCESSOR TOP DRIVE WIRING





SERIES III-A AND III-X
FIGURE 7-32
TOP DRIVE PHOTSENSOR



SERIES III-A AND III-X
FIGURE 7-30
ELECTROMECHANICAL COUNTER CIRCUIT

