Experiment 6: Kirchhoff's Circuit Laws

PHYS LAB 2240

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1 Abstract

The objective for this particular experiment was to demonstrate the use of Kirchhoff's Laws in solving electrical circuits. In this case, a "Y" circuit was analyzed using Kirchhoff's Laws. As is with every physics lab, another objective was to familiarize oneself with using laboratory equipment like the DMM to measure current and voltages, and the use of the DC power supply in setting up circuits.

The results matched our hypothesis: indeed Kirchhoff's Laws proved an invaluable tool in calculating the theoretical currents, and the percent differences between the measured and theoretical results further support our case. The average percent difference between the theoretical and measured values was 1.74%, showing consistency of the measured results with Kirchhoff's Laws. The significance of this experiment lies in the fact that it serves as an example to one of the fundamental (sets of) laws in Electromagnetism, teaches students valuable skills like plugging numbers into an RREF calculator, and allows for hands on experience in the lab, something that is quite important I believe.

2 Introduction

We begin by stating by stating Kirchhoff's Laws.

Theorem 1 (Junction Law). Let A be a junction point of an electrical circuit. Then the sum of the current flowing in and out of A must equal zero, that is,

$$\sum_{k=1}^{n} I_k = 0,$$

where n denotes the number of branches of currents that flow in or out of the node.

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Proof. Follows from the conservation of energy.

Theorem 2 (Loop Law). Let V_k denote voltages around a closed loop. Then the sum of the voltages must equal zero for n the number of voltages measured, that is,

$$\sum_{k=1}^{n} V_k = 0.$$

Proof. Left to the reader as an exercise.¹

The sample circuit as shown in the lab manual has been omitted since circuit diagrams take a long time to generate. You can consider the visualization of the sample circuit as yet another exercise left to the reader. Now we move on

¹Refer to this link for more information: https://www.feynmanlectures.caltech.edu/II_ 22.html

to analyze the particular circuit setup that this experiment concerns itself with. Refer to Figure 1 and Figure 2 to see the "Y" circuit setup and the Voltage Divider from which we will calculate V_f respectively.

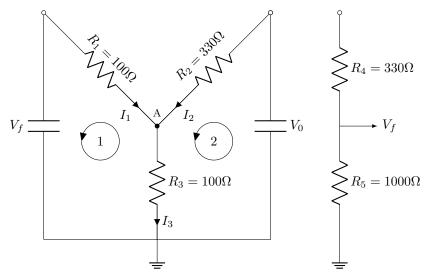


Figure 1: "Y" Circuit.

Figure 2: Voltage Divider.

From here a simple application of Kirchhoff's Laws will generate a complete set of equations that can be solved. This result is non-trivial and is also of mathematical interest, a general idea for the proof comes from the mapping of a circuit to a graph and from a graph to a matrix whos rows are vertices and columns are edges. Then after defining some vector spaces for the currents and voltages, our result follows from some neat linear algebra. For the interested reader, a link to the full paper containing the proof can be found below. ² Now in the case of our particular setup, a simple application of Theorem 1 to node A in the "Y" circuit yields the equation

$$I_1 + I_2 - I_3 = 0, (1)$$

definiting positive current as flowing into the node and negative current as flowing out of the node. We apply Theorem 2 to the closed loops 1 and 2 respectively to get the equations

$$R_1 I_1 + R_3 I_3 - V_f = 0 (2)$$

$$-R_2I_2 - R_3I_3 + V_0 = 0. (3)$$

Note that in this case, we define the voltage as negative when it flows from a higher voltage to a lower one. A simple application of Ohm's Law yields the

²An archive of the paper can be found by clicking here.

following equations for calculating the experimental values of I_1 and I_2 , given by

$$I_1 = \frac{V_f - V_B}{R_1} \tag{4}$$

and

$$I_2 = \frac{V_0 - V_B}{R_2},\tag{5}$$

respectively. It can be seen that I_1 and I_2 are calculated by measuring the voltage drops between V_f and V_B , and V_0 and V_B , respectively.

One final note: the "A" in Figure 1 refers to the node at which I_1 and I_2 flow in, and I_3 flows out, not to be confused with the point B at the same location which is used to measured the voltages above and set up the actual circuit in lab. Notice that B is not shown, along with its friends A, C, D, E, F, and G. This is a purely stylistic choice, it has nothing to do with the fact that I forgot to add them in when generating the figure. Please use your imagination to visualize them. Thank you in advance.

3 Apparatus

The apparatus used included an AC/DC Electronics Lab, a DC Power Supply, a DMM, a set of Resistors, and a set of eight Short Patch Cords.

The resistors came in a set of five, with resistances of 100Ω , 330Ω , 100Ω , 330Ω , and 1000Ω respectively, corresponding to the resistor's respective index number. The electronics lab was used to set up the entire experiment, and the DC Power supply was used to provide power so we could measure the voltages and determine the currents. The DMM was used to measure the resistance of the resistors and the voltages of the circuit. Quite frankly, I forgot what a short patch cord was. I think they were the things that connected the points to the power supply. Yeah, let's go with that.

4 Experimental Procedure

Part A: DC Analyis of Kirchhoff's Laws

Before the experiment began, the DMM was used to measure the resistance of the resistors. The aforementioned difficult of generating circuit diagrams unfortunately results in a lack of circuit to emulate. An effort was done to construct the circuit according the lab manual, shaped like a "T" with points A, B, and C across the upper portion of the "T", and the point D and the base of the "T". R_1 constituted \overline{AB} , R_2 constituted \overline{BC} , and R_3 constituted \overline{BD} . Similarly, the voltage divider was constructed similar to the shape of a backwards "L" (L for my grade on the last physics test), with points G, F, and E at the left most point, connector point, and topmost point respectively. R_5 constituted \overline{GF} and R_4 constituted \overline{FE} .

Now we connect the voltage divider to the "Y" ("T"?) circuit. The two were connected by connection the following points: C to E, D to G, and A to F, using connecting wires. Using banana plugs and alligator clips, the DC Power Supply was then connected to point E by the positive terminal and point G by the ground terminal. This completes the setup of the circuits.

The power supply was then activated and the voltage set to 15.00V. Then, a miniscule current was provided to keep the voltage, about 0.060A. The DMM was used to measure the voltage V_0 by touching the black lead to some ground connection, and the red lead to point E. Then, the DMM was used to measure the voltages V_f and V_B by keeping the black lead stationary from the previous step, and touching the red lead to their respective points on Figure 1. The DC Power supply was then shut off.

 I_3 was then measured in series with the DMM via the resistor R_3 . The wire that connected points D and G was removed and the DMM was inserted in its place by touching the black lead to point G and the read lead to point D. It was noted that the DMM was set to mA. This gave the current going through R_3 . The DC Power supply was then, once again, shut off. A similar process was repeated to measure I_1 and I_2 . For I_1 , the black lead was connected to point A and the red lead to point E. For E0, the black lead was connected to point E1 and E2 and the red lead to point E3. Note that Equations 4 and 5 could have been used as well to calculate E1 and E3, respectively. Turn off the power supply for the final time.

Finally, the system of linear equations given by Equations 1, 2, and 3 were solved using methods from linear algebra to yield the theoretical values, and the percent differences between the theoretical and measured values were calculated using the handy formula

Percent Difference =
$$\frac{|theoretical - measured|}{\frac{|theoretical + measured|}{2}} \cdot 100\%.$$
 (6)

See the Calculations section of this report for more information.

Part B: There is none

Why is there a "Procedure A" in the lab manual if there is no "Procedure B" to follow it?

5 Data

Table 1: Data obtained from measuring the resistance of the resistors with the DMM.

$R_1 = 100\Omega$	$R_2 = 330\Omega$	$R_3 = 100\Omega$	$R_4 = 330\Omega$	$R_5 = 1000\Omega$
99.6Ω	326Ω	99.8Ω	326Ω	985Ω

Table 2: Data obtained from measuring the voltages and currents experimentally.

V_0	V_B	V_f	I_1	I_2	I_3
14.97V	4.91V	6.75V	18.14mA	30.65mA	47.7mA

Table 3: Percent differences between the theoretical and measured values of the currents.

I_1 theoretical	I_2 theoretical	I_3 theoretical	$\%$ Diff I_1	$\%$ Diff I_2	$\%$ Diff I_3
18.4mA	30.8mA	49.3mA	1.4%	3.3%	3.3%

6 Calculations

Story time: I took a course in Linear Algebra several years back (or at least it feels that way). The theoretical part came to me extremely quickly, determinants and eigenvalues made perfect sense to me. However, on every test, I would always lose 5-10 points no matter what. The reason is, without fail, I would always mess up my arithematic when row reducing. Almost certainly I would flip the signs or add two numbers incorrectly. Since then, I have developed an avid hatred of row reduction by hand, and if there exists a way to avoid it, I will. Luckily for us, Linear Algebra fills up our toolbox with plenty of tools, some of which include LU Decomposition, Cramer's Rule, and in the case of a square matrix with a non-zero determinant (this is the only kind of matrix mathematicians are interested in anyway), matrix inversion. The last method is the one we choose for this matrix generated from the system of linear equations that follow from Kirchhoff's Laws. Behold the beautiful mess that is inverting a matrix by hand— now you have the pleasure of experiencing what Dr. Tran went through when she graded my tests!

Time for some Linear Algebra!

Note: don't ask me why I choose to torture myself by doing this. I have plenty of free time (update: I spoke too soon) and got a good grade on the last lab report. It's the end of the year, I'm about to graduate from TAMS, and society is on the cusp of complete collapse. I, quite literally, have nothing to lose. Without further ado, let us begin.

We can arrange Equations 1, 2, and 3 in this fashion:

From here it is clear that we can rewrite this system as the matrix equation $A\vec{x} = \vec{b}$, where

$$A = \begin{bmatrix} 1 & 1 & -1 \\ R_1 & 0 & R_3 \\ 0 & -R_2 & -R_3 \end{bmatrix}, \quad \vec{x} = \begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix}, \quad \vec{b} = \begin{bmatrix} 0 \\ V_f \\ -V_0 \end{bmatrix}$$

Since we know the values of all the unknowns except for the currents, we rewrite the matrices and vectors with real number entries as such:

$$A = \begin{bmatrix} 1 & 1 & -1 \\ 99.6 & 0 & 99.8 \\ 0 & -326 & -99.8 \end{bmatrix}, \quad \vec{x} = \begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix}, \quad \vec{b} = \begin{bmatrix} 0 \\ 6.75 \\ -14.97 \end{bmatrix}$$

A trivial calculation shows that det(A) is non-zero and is equal to 74944.48. Therefore, we can solve for \vec{x} using the equation

$$\vec{x} = A^{-1}\vec{b}$$
,

since it follows from the invertible matrix theorem that A is invertible. We want to find the exact value of A^{-1} . We have

$$A^{-1} = \frac{1}{\det(A)} adj(A),$$

and we already know the value of det(A). To find adj(A), we first solve for the matrix of minors, utilize the "checkerboard" pattern (cofactor matrix), then take the resulting matrix's transpose to obtain the adjugate matrix. Let us do this now. To find the matrix of minors, it's a fairly trivial computation that involves taking 9 determinants. We omit the work for this step and present the matrix of minors here:

$$\begin{pmatrix}
32524.8 & -9940.08 & -32469.6 \\
-425.8 & -99.8 & -326 \\
99.8 & 199.4 & -99.6
\end{pmatrix}$$

Now we "multiply" by the "checkerboard" matrix to obtain the matrix of cofactors. Clearly it is not actual matrix multiplication, hence the quotations around the word "multiply". We present the "checkerboard" matrix below:

$$\begin{pmatrix} + & - & + \\ - & + & - \\ + & - & + \end{pmatrix}$$

We multiply each respective entry a_{ij} of A by the respective b_{ij} in the checker-board matrix to get

$$\begin{pmatrix}
32524.8 & 9940.08 & -32469.6 \\
425.8 & -99.8 & 326 \\
99.8 & -199.4 & -99.6.
\end{pmatrix}$$

Now we take the transpose of this matrix to find the adjugate matrix, so we have adj(A) =

$$\begin{pmatrix}
32524.8 & 425.8 & 99.8 \\
9940.08 & -99.8 & -199.4 \\
-32469.6 & 326 & -99.6
\end{pmatrix}$$

Finally, keeping in mind that the determinant is a scalar, we divide the adjugate matrix by the determinant, so

$$\begin{pmatrix} 1 & 1 & -1 \\ 99.6 & 0 & 99.8 \\ 0 & -326 & -99.8 \end{pmatrix}^{-1} = \begin{pmatrix} 0.434119 & 0.00568154 & 0.00133165 \\ 0.132633 & -0.00133165 & -0.00266064 \\ -0.433249 & 0.00434989 & -0.00132898. \end{pmatrix}$$

Now since

$$\vec{x} = A^{-1}\vec{b},$$

we have

$$\vec{x} = \begin{bmatrix} 0.434119 & 0.00568154 & 0.00133165 \\ 0.132633 & -0.00133165 & -0.00266064 \\ -0.433249 & 0.00434989 & -0.00132898 \end{bmatrix} \begin{bmatrix} 0 \\ 6.75 \\ -14.97 \end{bmatrix} = \begin{bmatrix} 0.0184156 \\ 0.0308411 \\ 0.0492566 \end{bmatrix}$$

Taking note that the values obtained are in Amps, we conclude that

$$\vec{x} = \begin{bmatrix} I_1 \\ I_2 \\ I_3 \end{bmatrix} = \begin{bmatrix} 18.4mA \\ 30.8mA \\ 49.3mA \end{bmatrix},$$

so $I_1 = 18.4mA$, $I_2 = 30.8mA$, and $I_3 = 49.3mA$.

You thought the calculations section was over, but it was me DIO! In this case, DIO represents the percent differences. Some quick applications of Equation 6 yields

Percent Difference =
$$\frac{|18.4 - 18.14|}{\frac{|18.4 + 18.14|}{2}} \cdot 100\% = 1.42\%$$

for I_1 ,

Percent Difference =
$$\frac{|30.8 - 30.65|}{\frac{|30.8 + 30.65|}{2}} \cdot 100\% = 0.49\%$$

for I_2 , and

Percent Difference =
$$\frac{|49.3 - 47.7|}{\frac{|49.3 + 47.7|}{2}} \cdot 100\% = 3.30\%$$

for I_3 . Our fruitless toil finally comes to an end.

7 Discussion of Results and Error Analysis

There are several possible sources of error for this experiment— we begin by discussing the resistors. The theoretical values were calculated with the measured values of the resistors which reduced the error, however they will always fall somewhere below a $\pm 5\%$ error due to the nature of resistors. The gold band on the resistor indicated the error bound of $\pm 5\%$. Another source of error includes the equipment used, unfortunately we don't have access to professional tier research equipment. As a result, there will be many small sources of error accumulating throughout the experiment, such as the DMM measuring values slightly off, the quality of the circuit board affecting the voltages, the quality of the resistors, and so on.

However, even with all these sources of error, our results were quite splendid. Truly, we live in the best of all possible worlds. The average percent error of 1.74% indicates that our results stayed quite true to the theory, a concept which I will re-iterate in the following section.

8 Conclusion

Finally this lab report comes to an end. We have done many things, like proving some interesting theorems, doing some neat calculations, and looking at some cool circuits. We used Kirchhoff's Laws to analyze such aforementioned cool circuit(s), and measured the voltages and calculated the current values by such measured voltages. Our results seem to support our claim. The average percent error of 1.74% indicates that our results stayed quite true to the theory. Thus, we have supported the amazing claim that Kirchhoff's Laws seem to apply in the case of this particular circuit. 3

³Note how I didn't say anything about how this result supports the validity of Kirchhoff's Laws. Indeed, we could verify it manually for 999,999 circuits and if one failed to hold, the entire claim was false from the premise. Truly, if we were to show the validity of Kirchhoff's Laws, we would have to provide some form of formal proof that works for an arbitrary circuit. Wherever could that be...

⁴Please excuse this mathematician's dry sense of humor, my sincerest apologies for making you read this (this statement applies to the entire lab report). Thanks in advance.