GR-MANGO(mbed-RZ/A2M) Circuit schematic [X71A-M01-A]

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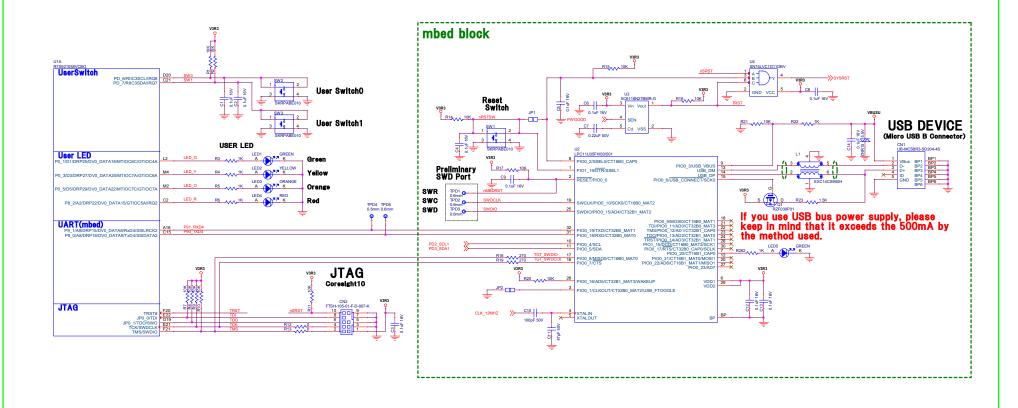
GR-MANGO Design by Core Corporation

SHT.1 Title Sheet SHT.2 mbed-IF / Switch / LED / JTAG Block SHT.3 Strage Block SHT.4 USB Block SHT.5 Ethernet Block SHT.6 HDMI / LCD IF / Audio CODEC Block SHT.7 Expansion I/F / MIPI / Camera IF Block SHT.8 Clock / BootMode / Power Block

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GR-MANGO(mbed-RZ/A2M) Board Circui

SHT.2 mbed-IF / Switch / LED / JTAG Block



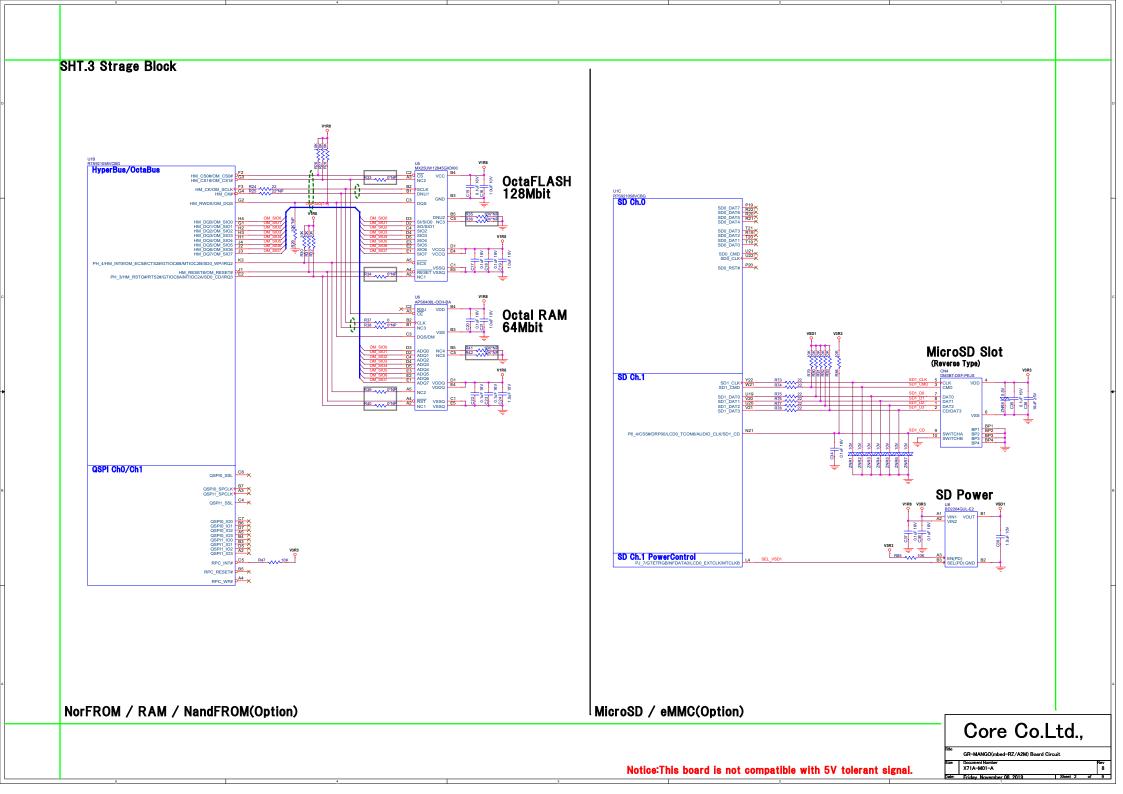
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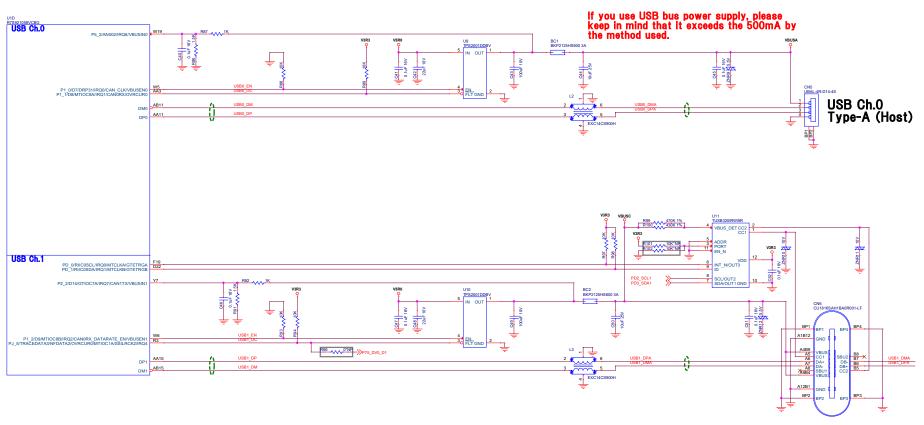
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Notice: This board is not compatible with 5V tolerant signal.

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SHT.4 USB Block



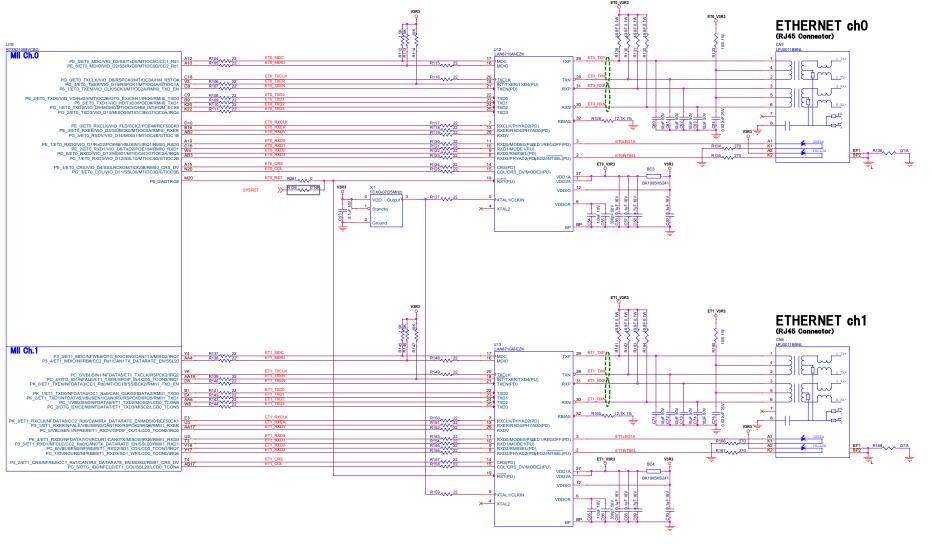
USB Ch.1 Type-C (Host/Device)

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SHT.5 Ethernet Block



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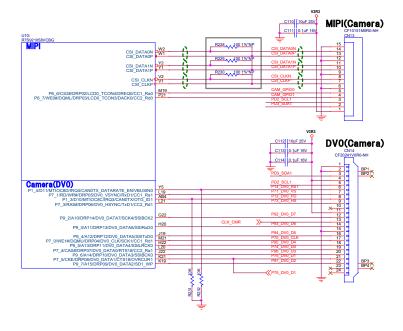
GR-MANGO(mbed-RZ/A2M) Board Circuit

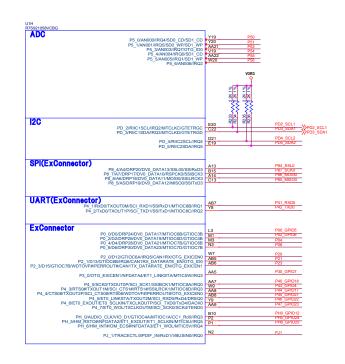
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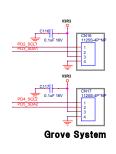
SHT.6 HDMI / LCD IF / Audio CODEC Block **HDMI Transmitter** UIF RYSECUSIONICOS ROBBS (HDMI/LCD) P. 7/CTET RICEDIOVI, DATASALCIO DATAGANTICAGUROT PP. 7/CTET RICEDIOVI, DATASALCIO DATAGANTICAGUROT PP. 6/TET SERIOU DATAGATICAD DATAMATICAGURISTICA PP. 6/TET SERIOU DATAGATICAGURA DATAMATICAGURISTICAGUROT PP. 6/TET SERIOU DATAMATICAGURISTICAGUR | P7_7/RDI/I/DV0_HSYNC/LCD0_TCON0/GTIOC3B/Rx00 | P7_6/RAH/I/DV0_VSYNC/LCD0_TCON1/GTIOC3A/SCK0 | P7_2/CS4/I/DV0_CLK/LCD0_TCON2/TEN00/CC2_Rx00 | P1_6/GTETRGC/NFCEH/LCD0_CLK/MTCLKC//RQ0 SSI(HDMI/CODEC) PJ_4/TRACEDATA PJ_3/TRACEDATA PJ_2/TRACE R205 4.3K 1% 11 MSEN/PO1 P6_5/CS3#/DRP01/LCD0_TCON5/AUDIO_XOUT/CC1_Rd0 N19 PH_1/AUDIO_XOUT/VIO_D0/GTIOC4B/MTIOC1B/CC2_Rd0/IRQ2 PK_5/GTETRGA/NFDATA1/WDTOVF#/PERROUT#/MTCLKA R202 10K **Audio CODEC** Backlight for LCD I/F LCD_DISP P8 1/A1/DRP23/DV0 DATA16/GTIOC5B/IRQ3 Core Co.Ltd., GR-MANGO(mbed-RZ/A2M) Board Circuit

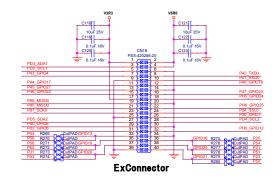
Notice: This board is not compatible with 5V tolerant signal.

SHT.7 Expansion I/F / Camera I/F(MIPI/DV0) Block









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