# Spurningar úr Stýriskipanir bókini

Setur

October 6, 2025

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1	
1.	1
1.	is a set of software frameworks that provide additional services to application developers.
	1. □ System programs
	2. □ Virtualization
	3. □ Cloud computing
	4. ✓ Middleware
2.	The operating system kernel consists of all system and application programs in a computer.
	• 🗖 True
	• 🗸 False
3.	All computer systems have some sort of user interaction.
	• 🗖 True
	• ✓ False
1.	2
1.	The most common secondary storage device is
	1. $\square$ random access memory
	2. □ solid state disks
	3. □ tape drives
	4. ✓ magnetic disk
2.	Flash memory is slower than DRAM but needs no power to retain its contents.
	• 🗸 True
	• □ False
3.	Interrupts may be triggered by either hardware of software
	• ✓ True
	• □ False
1.	3
1.	Which of the following would lead you to believe that a given system is an SMP-type system?
	1. $\square$ Each processor is assigned a specific task.
	2. $\square$ There is a boss-worker relationship between the processors.
	3. $\checkmark$ Each processor performs all tasks within the operating system.
	4. $\square$ None of the above
2.	A clustered system
	1. $\checkmark$ gathers together multiple CPUs to accomplish computational work
	2. $\square$ is an operating system that provides file sharing across a network
	3. $\square$ is used when rigid time requirements are present
	4. $\square$ can only operate one application at a time
3.	A dual-core system requires each core has its own cache memory.
	• $\square$ True

•  $\checkmark$  False

1.	<b>4</b>
1.	A can be used to prevent a user program from never returning control to the operating system.
	1. □ portal
	2. □ program counter
	3. $\square$ firewall
	4. ✓ timer
2.	What is another term for kernel mode?
	1. □ supervisor mode
	2. □ system mode
	3. $\square$ privileged mode
	4. ✓ All of the above
3.	What statement concerning privileged instructions is considered false?
	1. $\square$ They may cause harm to the system.
	2. $\square$ They can only be executed in kernel mode.
	3. $\checkmark$ They cannot be attempted from user mode.
	4. $\square$ They are used to manage interrupts.
1.	$oldsymbol{5}$
1.	Two important design issues for cache memory are
	1. □ speed and volatility
	2. ✓ size and replacement policy
	3. □ power consumption and reusability
	4. □ size and access privileges
2.	A(n) is the unit of work in a system.
	1. ✓ process
	2. □ operating system
	3. $\square$ timer
	4. □ mode bit
3.	Program counter specifies the current instruction to execute.
	• 🗖 True
	• ✓ False
1.	6
1.	UNIX does not allow users to escalate privileges to gain extra permissions for a restricted activity.
	• $\square$ True
	• ✓ False
2.	Security is equivalent to protection with no difference.
	• 🗖 True
	• 🗸 False
3.	Effective UID is the same as user identifiers (user IDs).
	• 🗖 True
	• ✓ False

1	-	7
	- 1	•

1. Virtualization software is one member of a class that differs from emulation.
• 🗖 True
• ✓ False
2. A virtual machine can be switched like a process in the operating system.
• 🗸 Yes
• 🗖 No
3. VMware application was a virtual machine manager (VMM).
• 🗸 Yes
• 🗖 No
1.8
1. Bluetooth and 802.11 devices use wireless technology to communicate over several feet, in essence creating a
1. □ local-area network
2. □ wide-area network
3. ✓ small-area network
4. □ metropolitan-area network
2. There are a fixed number of network protocols.
• 🖵 Yes
• 🗸 No
3. Internet is a network of networks.
• 🗸 Yes
• 🗖 No
1.9
1. A stack is a sequentially ordered data structure that uses the first in, first out (FIFO) principle for adding and removing items.
• 🖵 Yes
• 🗸 No
2. In a binary tree, a parent may have at most two children.
• 🗸 Yes
• 🖵 No
3. Hash collision refers to the phenomenon of hash function in which two unique inputs result in the same output value.
• ✓ Yes
• □ No

1.	10
1.	Embedded computers typically run on a operating system.
	1. $\checkmark$ real-time
	2. ☐ Windows XP
	3. □ network
	4. □ clustered
2.	Which of the following is a property of peer-to-peer systems?
	1. $\checkmark$ Clients and servers are not distinguished from one another.
	2. $\square$ Separate machines act as either the client of the server but not both.
	3. $\square$ They do not offer any advantages over traditional client-server systems.
	4. $\square$ They suffer from the server acting as the bottleneck in performance.
3.	A provides a file-system interface which allows clients to create and modify files.
	1. $\square$ compute-server system
	2. ✓ file-server system
	3. □ wireless network
	4. □ network computer
1.	11
1.	Which of the following statements concerning open source operating systems is true?
	1. □ Solaris is open source.
	2. ✓ Source code is freely available.
	3. $\square$ They are always more secure than commercial, closed systems.
	4. $\square$ All open source operating systems share the same set of goals.
2.	Which of the following operating systems is not open source?
	1. ✓ Windows
	2. □ BSD UNIX
	3. □ Linux
	4. □ PCLinuxOS
3.	A is a custom build of the Linux operating system
	1. ☐ LiveCD
	2. $\square$ installation

3.  $\checkmark$  distribution

4. 

Ware Player

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2.1
1. System call interface is the boundary between user programs and operating system services.
• ✓ Yes
• 🗖 No
2. Graphical User Interface (GUI) is the most common user interface.
• 🗸 Yes
• 🗖 No
3. Touch screen is a user interface on mobile systems.
• 🗸 Yes
• 🗖 No
2.2
1. KDE and GNOME desktops are available under open-source licenses.
• ✓ True
• 🖵 False
2. There is only a single flavor of shells for users to choose.
• 🖵 True
• 🗸 False
2.3
1 is not one of the major categories of system calls.
1. □ Process control
2. □ Communications
3. □ Protection
4. ✓ Security
<ul> <li>4. ✓ Security</li> <li>2 is/are not a technique for passing parameters from an application to a system call.</li> </ul>
2 is/are not a technique for passing parameters from an application to a system call.
<ul><li>2 is/are not a technique for passing parameters from an application to a system call.</li><li>1. ✓ Cache memory</li></ul>
<ul> <li>2 is/are not a technique for passing parameters from an application to a system call.</li> <li>1. ✓ Cache memory</li> <li>2. □ Registers</li> </ul>
<ul> <li>2 is/are not a technique for passing parameters from an application to a system call.</li> <li>1. ✓ Cache memory</li> <li>2. □ Registers</li> <li>3. □ Stack</li> </ul>
<ol> <li>is/are not a technique for passing parameters from an application to a system call.</li> <li>✓ Cache memory</li> <li>□ Registers</li> <li>□ Stack</li> <li>□ Special block in memory</li> </ol>
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<ol> <li>is/are not a technique for passing parameters from an application to a system call.</li> <li>Cache memory</li> <li>Registers</li> <li>Stack</li> <li>Special block in memory</li> <li>System calls can be run in either user mode or kernel mode.</li> <li>□ True</li> </ol>

2.4	
1. A	is an example of a systems program.
1.	✓ command interpreter
2.	☐ Web browser
3.	☐ text formatter
4.	$\Box$ database system
2. Sy	ystem programs all run in kernel mode.
•	☐ True
•	✓ False
3. W	b browser is a system program
•	☐ True
•	✓ False
2.5	
	ource files are compiled into object file(s) which use absolute addresses.
	□ True
	✓ False
	statically-linked library is only linked and loaded if it is conditionally required during program runtime.
	□ True
	✓ False
2.6	
	pplications that are designed to work on one operating system will also work on another operating system ng as they provide the same APIs.
•	□ True
•	✓ False
2.7	
1. Po	olicy
1.	□ determines how to do something
2.	$\checkmark$ determines what will be done
3.	$\Box$ is not likely to change across places
4.	$\Box$ is not likely to change over time
2. A	policy should be defined together with the mechanism.
•	□ True
•	✓ False
3. A	An operating system is far easier to port-to move to some other hardware-if it is written in a lowerlevel uage.
•	□ True

• 🗸 False

2.8
1. The major difficulty in designing a layered operating system approach is
1. $\checkmark$ appropriately defining the various layers
2. $\square$ making sure that each layer hides certain data structures, hardware, and operations from higherlevel layers
3. □ debugging a particular layer
4. □ making sure each layer is easily converted to modules
2. A microkernel is a kernel
1. $\square$ containing many components that are optimized to reduce resident memory size
2. $\square$ that is compressed before loading in order to reduce its resident memory size
3. $\square$ that is compiled to produce the smallest size possible when stored to disk
4. $\checkmark$ that is stripped of all nonessential components
3 allows operating system services to be loaded dynamically.
1. □ Virtual machines
2. ✓ Modules
3. □ File systems
4. □ Graphical user interfaces
2.9
1. To the SYSGEN program of an operating system, the least useful piece of information is
1. $\Box$ the CPU being used
2. □ amount of memory available
3. $\checkmark$ what applications to install
4. $\square$ operating-system options such as buffer sizes or CPU scheduling algorithms
2. A boot block
1. $\checkmark$ typically only knows the location and length of the rest of the bootstrap program
2. $\square$ typically is sophisticated enough to load the operating system and begin its execution
3. $\square$ is composed of multiple disk blocks

4.  $\square$  is composed of multiple disk cylinders

□ True ✓ False

 $3.\ \,$  An initial bootstrap program is in the form of random-access memory (RAM).

	. If a program terminates abnormally, a dump of memory may be examined by a f the problem.	_ to determine the cause
	1. □ module	
	2. ✓ debugger	
	3. □ shell	
	4. □ control card	
2.	. Tools for operating system debugging must be system-wide.	
	• 🗖 True	
	• 🗸 False	
3.	. Debugging is the activity of finding and fixing errors in a system, only in software.	
	• 🗖 True	
	• 🗸 False	

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3.1	
1. Thevariables.	of a process contains temporary data such as function parameters, return addresses, and loca
1. □ text s	ection
2. 🗖 data s	section
3. □ progra	am counter
4. ✓ stack	
2. A process of	control block
1. ✓ includ	les information on the process's state
2. □ stores	the address of the next instruction to be processed by a different process
3. 🗖 determ	mines which process is to be executed next
4. □ is an	example of a process queue
3. A process r	may transition to the Ready state by which of the following actions?
1. <b>□</b> Comp	eletion of an I/O event
2. $\square$ Await	ing its turn on the CPU
3. □ Newly	v-admitted process
4. ✓ All of	the above
3.2	
	refers to the number of processes in memory.
1. □ proce	
2. □ long-t	erm scheduler
3. ✓ degree	e of multiprogramming
4. □ CPU	scheduler
2. The list of	processes waiting for a particular I/O device is called a(n)
1. □ stand	
2. ✓ device	e queue
3. □ ready	queue
4. □ interr	upt queue
3. A	saves the state of the currently running process and restores the state of the
next process t	o run.
1. □ save-a	and-restore
2. □ state	switch
3. ✓ context	kt switch
4. $\square$ none	of the above

•  $\checkmark$  False

1. When a child process is created, which of the following is a possibility in terms of the execution or addrespace of the child process?
1. $\square$ The child process runs concurrently with the parent.
2. $\square$ The child process has a new program loaded into it.
3. $\square$ The child is a duplicate of the parent.
4. ✓ All of the above
2. A process that has terminated, but whose parent has not yet called wait(), is known as aprocess.
1. ✓ zombie
2. □ orphan
3. $\square$ terminated
4. $\square$ init
3. The process is assigned as the parent to orphan processes.
1. □ zombie
2. <b>✓</b> init
3. ☐ main
4. □ renderer
3.4
1. A new browser process is created by the Chrome browser for every new website that is visited.
• 🗖 True
• 🗸 False
2. Which of the following statements is true?
1. $\checkmark$ Shared memory is typically faster than message passing.
2. $\square$ Message passing is typically faster than shared memory.
3. $\square$ Message passing is most useful for exchanging large amounts of data.
4. $\square$ Shared memory is far more common in operating systems than message passing.
3. Shared memory is a more appropriate IPC mechanism than message passing for distributed systems.
• 🗖 True
• 🗸 False
3.5
1. The shared buffer is implemented as a circular array with two logical pointers: in and out. The variable points to the next free position in the buffer; out points to the first full position in the buffer. The buffer is full when in $==$ out.
• 🗖 True

3.0	6
1.	A blocking send() and blocking receive() is known as a(n)
	1. □ synchronized message
	2. ✓ rendezvous
	3. □ blocked message
	4. □ asynchronous message
2.	In a(n) temporary queue, the sender must always block until the recipient receives the message.
	1. ✓ zero capacity
	2. □ variable capacity
	3. □ bounded capacity
	4. □ unbounded capacity
	Under indirect communication, each process that wants to communicate must explicitly name the recipient sender of the communication.
	• 🗖 True
	• ✓ False
3.	7
	A(n) allows several unrelated processes to use the pipe for communication.
	1. ✓ named pipe
	2. □ anonymous pipe
	3. □ LIFO
	4. □ ordinary pipe
	Which of the following statements is true?
	1. □ Named pipes do not allow bi-directional communication.
	2. □ Only the parent and child processes can use named pipes for communication.
	3. ✓ Reading and writing to ordinary pipes on both UNIX and Windows systems can be performed like ordinary file I/O.
	4. □ Named pipes can only be used by communicating processes on the same machine.
3.	Named pipes continue to exist in the system after the creating process has terminated.
	• ✓ True
	• 🗅 False
3.8	R
	Solution Imagine that a host with IP address 150.55.66.77 wishes to download a file from the web server at IP address
Τ.	imagine that a nost with it address 190.99.00.11 wishes to download a me from the web server at IP address

- SS 202.28.15.123. Select a valid socket pair for a connection between this pair of hosts.
  - 1.  $\square$  150.55.66.77:80 and 202.28.15.123:80
  - 2.  $\square$  150.55.66.77:150 and 202.28.15.123:80
  - 3.  $\checkmark$  150.55.66.77:2000 and 202.28.15.123:80
  - 4.  $\square$  150.55.66.77:80 and 202.28.15.123:3500
- 2. When communicating with sockets, a client process initiates a request for a connection and is assigned a port by the host computer. Which of the following would be a valid port assignment for the host computer?

- 1. 🗖 21
- 2. 🗆 23
- 3. 🗆 80
- 4. **✓** 1625
- 3. A socket is identified by an IP address concatenated with a port number.
  - 🗸 True
  - $\square$  False

4
4.1
1. A traditional (or heavyweight) process has a single thread of control.
• ✓ True
• 🖵 False
2. A thread is composed of a thread ID, program counter, register set, and heap.
• $\square$ True
• ✓ False
3. Each thread has its own register set and stack.
• 🗸 True
• 🖵 False
4.2
1 involves distributing tasks across multiple computing cores.
1. □ Concurrency
2. ✓ Task parallelism
3. □ Data parallelism
4. □ Parallelism
2. According to Amdahl's Law, what is the speedup gain for an application that is 60
1. <b>1</b> 1.82
2. 🗆 .7
3. □ .55
4. <b>✓</b> 1.43
3. It is possible to have concurrency without parallelism.
• ✓ True
• □ False
4.3
1. The multithreading model multiplexes many user-level threads to a smaller or equal number of kerne

1. The	_ multithreading me	odel multiplexes	many user-level	threads to a	a smaller or	equal	number	of kerne
threads.								

- 1.  $\square$  many-to-one model
- 2.  $\square$  one-to-one model
- 3.  $\checkmark$  many-to-many model
- 4.  $\square$  many-to-some model
- 2. The \_\_\_\_\_ model maps many user-level threads to one kernel thread.
  - 1.  $\square$  many-to-many
  - 2.  $\square$  two-level
  - 3.  $\square$  one-to-one
  - 4. ✓ many-to-one

	1.	□ many-to-many
	2.	✓ two-level
	3.	□ one-to-one
	4.	□ many-to-one
4.	4	
1.	A	provides an API for creating and managing threads.
	1.	□ set of system calls
	2.	□ multicore system
	3.	✓ thread library
	4.	☐ multithreading model
2.	Pt	threads refers to
	1.	$\Box$ the POSIX standard.
	2.	$\hfill \Box$ an implementation for thread behavior.
	3.	$\checkmark$ a specification for thread behavior.
	4.	$\square$ an API for process creation and synchronization.
3.	It	is possible to create a thread library without any kernel-level support.
	•	✓ True
	•	□ False
4.	5	
1.	A	uses an existing thread - rather than creating a new one - to complete a task.
	1.	□ lightweight process
	2.	$\checkmark$ thread pool
	3.	$\square$ scheduler activation
	4.	$\square$ asynchronous procedure call
2.	W	hen OpenMP encounters the #pragma omp parallel directive, it
	1.	$\square$ constructs a parallel region
	2.	$\Box$ creates a new thread
	3.	$\checkmark$ creates as many threads as there are processing cores
	4.	□ parallelizes for loops
3.	Gı	rand Central Dispatch handles blocks by
	1.	$\checkmark$ placing them on a dispatch queue
	2.	$\Box$ creating a new thread
	3.	$\Box$ placing them on a dispatch stack
	4.	□ constructing a parallel region

3. The \_\_\_\_\_ model allows a user-level thread to be bound to one kernel thread.

1.	Which of the following would be an acceptable signal handling scheme for a multithreaded program?
	1. $\square$ Deliver the signal to the thread to which the signal applies.
	2. $\square$ Deliver the signal to every thread in the process.
	3. $\square$ Deliver the signal to only certain threads in the process.
	4. ✓ All of the above
2.	Cancellation points are associated with cancellation.
	1. □ asynchronous
	2. ✓ deferred
	3. □ synchronous
	4. $\square$ non-deferred
3.	Thread-local storage is data that
	1. $\square$ is not associated with any process
	2. $\square$ has been modified by the thread, but not yet updated to the parent process
	3. $\square$ is generated by the thread independent of the thread's process
	4. $\checkmark$ is unique to each thread
4.	7
1.	Windows uses the
	1. $\checkmark$ one-to-one model
	2. $\square$ many-to-one model
	3. $\square$ one-to many-model
	4. □ many-to-many model
2.	Linux distinguishes between processes and threads.
	• 🗆 True
	• ✓ False
	The flags like CLONE FS, CLONE VM, CLONE SIGHAND, and CLONE FILES defines which sharing wilke place in the function clone().
	• ✓ Yes
	• 🗅 No

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J.	1	
1.	W	Thich of the following is true of cooperative scheduling?
	1.	$\Box$ It requires a timer.
	2.	$\checkmark$ A process keeps the CPU until it releases the CPU either by terminating or by switching to the waiting state.
	3.	$\Box$ It incurs a cost associated with access to shared data.
	4.	$\square$ A process switches from the running state to the ready state when an interrupt occurs.
2.	In	preemptive scheduling, the sections of code affected by interrupts must be guarded from simultaneous use.
	•	✓ True
	•	□ False
5.	<b>2</b>	
1.		is the number of processes that are completed per time unit.
	1.	□ CPU utilization
	2.	□ Response time
	3.	□ Turnaround time
	4.	✓ Throughput
5.	3	
1.	_	scheduling is approximated by predicting the next CPU burst with an exponential average of the ured lengths of previous CPU bursts.
	1.	□ Multilevel queue
	2.	□ RR
	3.	□ FCFS
	4.	✓ SJF
2.	T	he scheduling algorithm is designed especially for time-sharing systems.
	1.	$\square$ SJF
	2.	$\square$ FCFS
	3.	✓ RR
	4.	□ Multilevel queue
3.	W	Thich of the following is true of multilevel queue scheduling?
	1.	$\Box$ Processes can move between queues.
	2.	✓ Each queue has its own scheduling algorithm.
	3.	$\hfill \Box$ A queue cannot have absolute priority over lower-priority queues.
	4.	$\Box$ It is the most general CPU-scheduling algorithm.

Э,	4
1.	involves the decision of which kernel thread to schedule onto which CPU.
	1. □ Process-contention scope
	2. ✓ System-contention scope
	3. □ Dispatcher
	4. □ Round-robin scheduling
E	E
1.	allows a thread to run on only one processor.  1. ✓ Processor affinity
	2. □ Processor set
	3. \( \superstack \text{NUMA} \)
า	4. □ Load balancing With a thread executes on a processor until a long-latency event (i.e. a memory stall) occurs.
۷.	1. ✓ coarse-grained multithreading
	2. □ fine-grained multithreading
	3. □ virtualization
2	4. □ multicore processors  The two general approaches to load balancing are and
ე.	1. □ soft affinity, hard affinity
	2. □ coarse grained, fine grained
	3. □ soft real-time, hard real-time
	4. ✓ push migration, pull migration
	4. V push inigration, pun inigration
5.	.6
	The rate of a periodic task in a hard real-time system is, where p is a period and t is the processime.
	1. ✓ 1/p
	2. □ p/t
	3. □ 1/t
	4. <b>□</b> pt
2.	Which of the following is true of the rate-monotonic scheduling algorithm?
	1. $\square$ The task with the shortest period will have the lowest priority.
	2. $\square$ Fine-It uses a dynamic priority policy.
	3. $\checkmark$ CPU utilization is bounded when using this algorithm.
	4. $\square$ It is non-preemptive.
3.	Which of the following is true of earliest-deadline-first (EDF) scheduling algorithm?
	1. $\checkmark$ When a process becomes runnable, it must announce its deadline requirements to the system.
	2. $\square$ Deadlines are assigned as following: the earlier the deadline, the lower the priority; the later the deadline the higher the priority.
	3. $\square$ Priorities are fixed; that is, they cannot be adjusted when a new process starts running.

4.  $\square$  It assigns priorities statically according to deadline.

	$\overline{}$
റ	1

1. V	Which of the following statements are false with regards to the Linux CFS scheduler?
1	. $\square$ Each task is assigned a proportion of CPU processing time.
2	. $\square$ Lower numeric values indicate higher relative priorities.
3	. $\checkmark$ There is a single, system-wide value of vruntime.
4	. $\square$ The scheduler doesn't directly assign priorities.
	What is the numeric priority of a Windows thread in the HIGH_PRIORITY_CLASS with ABOVE_NORL relative priority?
1	. 🗖 24
2	. 🗖 10
3	. 🗖 8
4	. 🗸 14
3. Into?	Solaris, if an interactive thread with priority 15 uses its entire time quantum, what is its priority recalculated
1	. 🗅 51
2	. <b>√</b> 5
3	. 🗖 160
4	. $\square$ It remains at 15
5.8	
1. In	a Little's formula, $\lambda$ , represents the
1	. $\square$ average waiting time in the queue
2	. $\checkmark$ average arrival rate for new processes in the queue
3	. $\square$ average queue length
4	. □ average CPU utilization

6	
6.1	
1. A	race condition
1	. $\square$ results when several threads try to access the same data concurrently
2	. $\checkmark$ results when several threads try to access and modify the same data concurrently
3	. $\square$ will result only if the outcome of execution does not depend on the order in which instructions are executed
4	. $\square$ None of the above
2. In	nstructions from different processes can be interleaved when interrupts are allowed.
•	✓ True
•	□ False
6.2	
	.(n) refers to where a process is accessing/updating shared data.
	· ✓ critical section
2	. $\square$ entry section
	. umutex
	. $\Box$ test-and-set
	solution to the critical section problem does not have to satisfy which of the following requirements?
	. $\square$ mutual exclusion
2	. $\square$ progress
3	. 🗸 atomicity
4	. $\square$ bounded waiting
3. A	nonpreemptive kernel is safe from race conditions on kernel data structures.
•	✓ True
•	□ False
6.3	
	Peterson's solution, the variable indicates if a process is ready to enter its critical section.
	turn
	. □ lock
	. ✔ flag[i]
	. □ turn[i]
	is not a technique for handling critical sections in operating systems.
	. □ Nonpreemptive kernels
2	□ Preemptive kernels
	. □ Spinlocks
	✓ Peterson's solution
	is/are not a technique for managing critical sections in operating systems.
	. ✓ Peterson's solution

2.  $\square$  Preemptive kernel

4.  $\square$  Semaphores

3.  $\square$  Nonpreemptive kernel

6.4	
1. An instruction that executes atomically	
1. $\square$ must consist of only one machine instruction	
2. $\checkmark$ executes as a single, uninterruptible unit	
3. $\square$ cannot be used to solve the critical section problem	
4. $\square$ All of the above	
2. Race conditions are prevented by requiring that critical region	ns be protected by locks.
• 🗸 True	
• 🗖 False	
3. Both the test _ and _set() instruction and compare _and _swa	ap() instruction are executed atomically.
• 🖵 False	
6.5	
1. A mutex lock	
1. $\square$ is exactly like a counting semaphore	
2. $\checkmark$ is essentially a boolean variable	
3. $\square$ is not guaranteed to be atomic	
4. $\square$ can be used to eliminate busy waiting	
2. What is the correct order of operations for protecting a critic	al section using mutex locks?
1. $\square$ release() followed by acquire()	
2. $\checkmark$ acquire() followed by release()	
3. $\square$ wait() followed by signal()	
4. $\square$ signal() followed by wait()	
3. Busy waiting refers to the phenomenon that while a process tries to enter its critical section must loop continuously in the calculations.	· · · · · · · · · · · · · · · · · · ·
• 🗸 Yes	
• □ No	
6.6	
1. A counting semaphore	
1. $\checkmark$ is essentially an integer variable	
2. $\square$ is accessed through only one standard operation	
3. $\square$ can be modified simultaneously by multiple threads	
4. $\square$ cannot be used to control access to a thread's critical se	ections
2 can be used to prevent busy waiting when implement	nting a semaphore.
1. □ Spinlocks	
2. ✓ Waiting queues	
3. □ Mutex lock	
4. $\square$ Allowing the wait() operation to succeed	
$3.\ \mathrm{Mutex}$ locks and binary semaphores are essentially the same	thing.
• 🗸 True	

• □ False

6.7
1. A type presents a set of programmer-defined operations that are provided mutual exclusion within it.
1. □ transaction
2. □ signal
3. □ binary
4. ✓ monitor
2. The local variables of a monitor can be accessed by only the local procedures.
• 🗸 True
• 🗖 False
3. Monitors are a theoretical concept and are not practiced in modern programming languages
• 🗖 True
• 🗸 False
6.8
1. A deadlock-free solution eliminates the possibility of starvation.
• 🗖 True
• 🗸 False
2. Another problem related to deadlocks is
1. $\square$ race conditions
2. □ critical sections
3. □ spinlocks
4. ✓ indefinite blocking
3 occurs when a higher-priority process needs to access a data structure that is current being accessed by a lower-priority process.
1. ✓ Priority inversion
2. □ Deadlock
3. □ A race condition
4. □ A critical section
6.9
1. Traditional synchronization is always much faster than CAS-based synchronization.
• 🗖 True
• 🗸 False

•	•
	What is the purpose of the mutex semaphore in the implementation of the bounded-buffer problem using maphores?
	1. $\square$ It indicates the number of empty slots in the buffer.
	2. $\square$ It indicates the number of occupied slots in the buffer.
	3. $\square$ It controls access to the shared buffer.
	4. ✓ It ensures mutual exclusion.
2.	The first readers-writers problem
	1. $\square$ requires that, once a writer is ready, that writer performs its write as soon as possible.
	2. $\square$ is not used to test synchronization primitives.
	3. $\checkmark$ requires that no reader will be kept waiting unless a writer has already obtained permission to use the shared database.
	4. $\square$ requires that no reader will be kept waiting unless a reader has already obtained permission to use the shared database.
3.	How many philosophers may eat simultaneously in the Dining Philosophers problem with 5 philosophers?
	1. 🗖 1
	2. <b>✓</b> 2
	3. 🗖 3
	4. <b>□</b> 5
7.	$\cdot 2$
1.	Which of the following statements is true?
	1. ✓ Operations on atomic integers do not require locking.
	2. $\square$ Operations on atomic integers do require additional locking.
	3. $\square$ Linux only provides the atomic_inc() and atomic_sub() operations.
	4. $\square$ Operations on atomic integers can be interrupted.
2.	A thread will immediately acquire a dispatcher lock that is the signaled state.
	• 🗸 True
	• 🗖 False
3.	Linux mostly uses atomic integers to manage race conditions within the kernel.
	• 🗖 True
	• 🗸 False

atomicity.

1. Pthreads can be implemented
1. $\square$ only inside the operating system kernel
2. $\square$ only at the user level
3. $\checkmark$ at the user level or inside the operating system kernel
4. □ only Windows OS
2. When the owner of a mutex lock invokes pthread mutex unlock(), all threads blocked on that mutex's lock are unblocked.
• 🗖 True
• 🗸 False
3. A call to pthread_cond_signal()
1. $\square$ releases the mutex lock and signals one thread waiting on the condition variable.
2. $\square$ releases the mutex lock and signals all threads waiting on the condition variable.
3. $\checkmark$ signals one thread waiting on the condition variable, but does not release the mutex lock.
4. $\square$ signals all threads waiting on the condition variable, but does not release the mutex lock.
7.4
1. In JAVA, when a thread calls notify(), an arbitrary thread T from the list of threads in the wait set
1. $\square$ is set to runnable state and T immediately regains the control of the lock.
2. $\square$ is set to runnable state and is moved from the wait set to entry set where it regains the control of the lock as soon as the thread calling notify() exits its method.
3. □ is set to runnable state and is moved from the wait set to entry set where it regains the control of the lock only after all other threads in the entry set have acquired and released the lock.
4. $\checkmark$ is set to runnable state and is moved from the wait set to entry set where it competes for the lock with the other threads in the entry set.
2. A reentrant lock allows for granting the lock to the longest-waiting thread when unlock() is called.
• ✓ True
• 🖵 False
3. When a thread invokes the signal() method of a condition variable, all threads blocked on that condition variable are unblocked.
• 🗖 True
• ✓ False
7.5
1. The OpenMP #pragma omp critical directive
1. ✓ behaves much like a mutex lock
2. □ does not require programmers to identify critical sections
3. □ does not guarantee prevention of race conditions
4. □ is similar to functional languages

2. An advantage of transactional memory system is that the developer doesn't have to worry about implementing

- 🗸 True
- $\square$  False
- 3. In a functional programming language,
  - 1.  $\square$  variables may be assigned different values over time.
  - 2.  $\square$  value of a variable can be changed only by the thread that created it.
  - 3.  $\checkmark$  value of a variable is immutable.
  - 4.  $\square$  developers have to implement atomicity.

8.1
1. A deadlocked state occurs whenever
1. $\square$ a process is waiting for I/O to a device that does not exist
2. $\square$ the system has no available free resources
3. $\checkmark$ every process in a set is waiting for an event that can only be caused by another process in the set
4. $\square$ a process is unable to release its request for a resource after use
2. Deadlock occurs when every thread in a set is blocked waiting for an event that can be caused only by another thread in the set, while livelock occurs when a thread continuously attempts an action that fails.
• 🗸 True
• 🗖 False
3. In the dining philosophers problem, there is a possibility of deadlock but not livelock.
• 🗖 True
• 🗸 False
8.2
1. One necessary condition for deadlock is, which states that a process must be holding one resource and waiting to acquire additional resources.
1. ✓ hold and wait
2. □ mutual exclusion
3. □ circular wait
4. □ no preemption
2. A cycle in a resource-allocation graph is
1. $\square$ a necessary and sufficient condition for deadlock in the case that each resource has more than one instance
2. ✓ a necessary and sufficient condition for a deadlock in the case that each resource has exactly one instance
3. $\square$ a sufficient condition for a deadlock in the case that each resource has more than once instance
4. □ is neither necessary nor sufficient for indicating deadlock in the case that each resource has exactly
one instance 3. If a resource-allocation graph has a cycle, the system must be in a deadlocked state.
• 🗖 True
• ✓ False
8.3  1. To be alle deadle sky an entire greaters most often
1. To handle deadlocks, operating systems most often
1. ✓ pretend that deadlocks never occur
2. □ use protocols to prevent or avoid deadlocks
3. □ detect and recover from deadlocks
4. □ None of the above
2. Both deadlock prevention and deadlock avoidance techniques ensure that the system will never enter a deadlocked state.

• 🗸 True

- $\square$  False
- 3. Most operating systems choose to ignore deadlocks, because
  - 1.  $\square$  handling deadlocks is expensive in terms of performance and resources.
  - 2.  $\square$  deadlocks occur infrequently.
  - 3.  $\square$  methods used to recover from livelock may be put to use to recover from deadlock.
  - 4. ✓ All of the above.

- 1. Assume there are three resources, R1, R2, and R3, that are each assigned unique integer values 15, 10, and 25, respectively. What is a resource ordering which prevents a circular wait?
  - 1.  $\square$  R1, R2, R3
  - 2.  $\square$  R3, R2, R1
  - 3.  $\square$  R3, R1, R2
  - 4. \(\nsigma\) R2, R1, R3
- 2. Deadlock prevention by denying the mutual-exclusion condition is the simplest way to prevent deadlocks.
  - 🗖 True
  - / False
- 3. In deadlock prevention by denying hold-and-wait condition,
  - 1.  $\square$  resource utilization may below.
  - 2.  $\square$  starvation is possible.
  - 3.  $\square$  whenever a thread requests a resource, it does not hold any other resources.
  - 4. ✓ All of the above.

- 1. Which of the following statements is true?
  - 1.  $\square$  A safe state is a deadlocked state.
  - 2.  $\square$  A safe state may lead to a deadlocked state.
  - 3.  $\square$  An unsafe state is necessarily, and by definition, always a deadlocked state.
  - 4.  $\checkmark$  An unsafe state may lead to a deadlocked state.
- 2. Suppose that there are ten resources available to three processes. At time 0, the following data is collected. The table indicates the process, the maximum number of resources needed by the process, and the number of resources currently owned by each process. Which of the following correctly characterizes this state?

Process	Maximum Needs	Currently Owned
$P_0$	10	4
$P_1$	3	1
$P_2$	6	4

- 1.  $\square$  It is safe.
- 2.  $\checkmark$  It is not safe.
- 3.  $\square$  The state cannot be determined.
- 4.  $\square$  It is an impossible state.

3. Suppose that there are 12 resources available to three processes. At time 0, the following data is collected. The table indicates the process, the maximum number of resources needed by the process, and the number of resources currently owned by each process. Which of the following correctly characterizes this state?

Process	Maximum Needs	Currently Owned
$P_0$	10	4
$P_1$	3	2
$P_2$	7	4

- 1. ✓ It is safe.
- 2.  $\square$  It is not safe.
- 3.  $\square$  The state cannot be determined.
- 4.  $\square$  It is an impossible state.

#### 8.6

- 1. The wait-for graph scheme is not applicable to a resource allocation system with multiple instances of each resource type.
  - / True
  - □ False
- 2. In a wait-for-graph,
  - 1.  $\square$  a resource may have multiple instances.
  - 2.  $\square$  a deadlock implies the existence of a cycle, but a cycle does not necessarily imply a deadlock.
  - 3.  $\square$  a cycle implies deadlock, but not every deadlock results in a cycle.
  - 4. ✓ None of the above.
- 3. The decision of when to invoke a detection algorithm depends on the likely frequency of deadlocks and the number of threads likely to be affected by a deadlock when it happens.
  - / True
  - □ False

- 1. A system can recover from a deadlock by
  - 1.  $\square$  aborting one process at a time until the deadlock cycle is eliminated.
  - 2.  $\square$  aborting all deadlocked processes.
  - 3.  $\square$  preempting some resources from one or more of the deadlocked threads.
  - 4. ✓ All of the above.
- 2. To recover from a deadlock using resource preemption,
  - 1.  $\square$  the order of resources and processes that need to be preempted must be determined to minimize cost.
  - 2.  $\square$  if a resource is preempted from a process, the process must be rolled back to some safe state and restarted from that state.
  - 3.  $\square$  ensure that starvation does not occur from always preempting resources from the same process.
  - 4. ✓ All of the above.

9	
9	.1
1.	Absolute code can be generated for
	1. ✓ compile-time binding
	2. □ load-time binding
	3. □ execution-time binding
	4. □ interrupt binding
2.	In a dynamically linked library,
	1. $\square$ loading is postponed until execution time
	2. $\square$ system language libraries are treated like any other object module
	3. $\square$ more disk space is used than in a statically linked library
	4. $\checkmark$ a stub is included in the image for each library-routine reference
3.	The mapping of a logical address to a physical address is done in hardware by the
	1. $\checkmark$ memory-management-unit (MMU)
	2. □ memory address register
	3. □ relocation register
	4. □ dynamic loading register
9	.2
1.	is the dynamic storage-allocation algorithm which results in the smallest leftover hole in memory
	1. □ First fit
	2. ✓ Best fit
	3.
	4. □ None of the above
2.	is the dynamic storage-allocation algorithm which results in the largest leftover hole in memory.
	1. ☐ First fit
	2. □ Best fit
	3. ✓ Worst fit
	4. ☐ None of the above
3.	Which of the following is true of compaction?

1.  $\square$  It can be done at assembly, load, or execution time.

3.  $\square$  It cannot shuffle memory contents.

2.  $\square$  It is used to solve the problem of internal fragmentation.

4.  $\checkmark$  It is possible only if relocation is dynamic and done at execution time.

<b>3</b>

1. Consider a logical address with a page size of 8 KB. How many bits must be used to represent the page offset in the logical address?
1. <b>□</b> 10
2. 🗖 8
3. <b>✓</b> 13
4. □ 12
2. Assume a system has a TLB hit ratio of 90%. It requires 15 nanoseconds to access the TLB, and 85 nanoseconds to access main memory. What is the effective memory access time in nanoseconds for this system?
1. <b>✓</b> 108.5
2. 🗖 100
3. 🗖 22
4. □ 176.5
3. Given the logical address 0xAEF9 (in hexadecimal) with a page size of 256 bytes, what is the page offset?
1. □ 0xAE
2. <b>✓</b> 0xF9
3. □ 0xA
$4. \ \square \ 0xF900$
9.4
1. A(n) page table one page entru for real page (or frame) of memory.
1. ✓ Inverted
2. □ Clusterd
3. ☐ Forward-mapped
4. □ Virtual
2. Consider a 32-bit address for a two-level paging system with an 8 KB page size. The outer page table has 1024 entries. How many bists are used to represent the secound-level page table?
1. <b>1</b> 0
2. 🗖 8
3. 🗖 12
4. <b>✓</b> 9
3. Which of the following statements are true with respect to hashed page tables?
1. $\square$ They only work for sparse address spaces.
2. $\square$ The virtual address is used to hash into the hash table.
3. $\checkmark$ A common approach for handling address spaces larger than 32 bits.
4. $\square$ Hash table collisions do not occur because of the importance of paging.

- 1. Which of the following is not a reason explaining why mobile devices generally do not support swapping?
  - 1.  $\square$  Limited space constraints of flash memory.
  - 2. 

    Small size of mobile applications do not require use of swap space.
  - 3.  $\square$  Limited number of writes of flash memory.
  - 4.  $\square$  Poor throughput between main memory and flash memory.
- 2. Standard swapping is generally not used in contemporary operating systems, because
  - 1.  $\square$  some processes are so large that they can't fit in backing store.
  - 2.  $\square$  memory in contemporary systems is large enough to store all processes.
  - 3.  $\checkmark$  the amount of time required to move entire processes between memory and the backing store is prohibitive.
  - 4.  $\square$  contemporary operating systems do not oversubscribe memory.
- 3. Standard swapping involves swapping in pages of processes instead of entire processes.
  - 🗖 True
  - / False

#### 9.6

- 1. The x86-64 bit architecture only uses 48 of the 64 possible bits for representing virtual address space.
  - 🗸 True
  - □ False
- 2. A page address extension (PAE) in IA-32
  - 1.  $\square$  introduces two-level paging scheme.
  - 2.  $\square$  increases address size to 64 bits.
  - 3.  $\checkmark$  allows 32-bit processors to access a physical address space larger than 4 GB.
  - 4.  $\square$  does not need any operating system support.
- 3. IA-32 page tables can be swapped to disk.
  - 🗸 True
  - 🖵 False

- 1. Which of the following statements regarding the ARM architecture are false?
  - 1.  $\square$  There are essentially four different page ranging from 4-KB to 16-MB in size.
  - 2.  $\square$  There are two different levels of TLB.
  - 3.  $\square$  One or two level paging may be used.
  - 4. ✓ The micro TLB must be flushed at each context switch.
- 2. The ARM architecture uses up to four-level paging.
  - 🗸 True
  - 🖵 False
- 3. In ARM architecture, address translation
  - 1.  $\checkmark$  begins at the micro-TLB level.
  - 2.  $\square$  begins at the main TLB-level.
  - 3.  $\square$  begins at the page table.
  - 4.  $\square$  can begin at any of micro, main or page table levels.

1. Which of the following is a benefit of allowing a program that is only partially in memory to execute?	
1. $\square$ Programs can be written to use more memory than is available in physical memory.	
2. $\square$ CPU utilization and throughput is increased.	
3. $\square$ Less I/O is needed to load or swap each user program into memory.	
4. ✓ All of the above	
2. In systems that support virtual memory,	
1. $\square$ virtual memory is separated from logical memory.	
2. □ virtual memory is separated from physical memory.	
3. $\square$ physical memory is separated from secondary storage.	
4. $\checkmark$ physical memory is separated from logical memory.	
3. In general, virtual memory decreases the degree of multiprogramming in a system.	
• 🗖 True	
• 🗸 False	
10.2	
1. On a system with demand-paging, a process will experience a high page fault rate when the process beging execution.	ns
• 🗸 True	
• 🖵 False	
2. A page fault occurs when	
1. $\square$ a page in memory get corrupted.	
2. $\square$ the size of a process is larger than the size of the physical memory.	
3. $\checkmark$ a process tries to access a page that is not loaded in memory.	
4. $\square$ the page table is not large enough to include all page table entries.	
3. If memory access time is 250 nanoseconds and average page fault service time 10 milliseconds, the probabile of page faults must be less to keep the performance degradation less than 20	ity
1. $\square 0.0000025$	
2. <b>✓</b> 0.000005	
3. □ 0.0000075	
4. □ 0.00001	
10.3	
1. The vfork() system call in UNIX	
1. $\checkmark$ allows the child process to use the address space of the parent	
2. $\square$ uses copy-on-write with the fork() call	
3. $\square$ is not intended to be used when the child process calls exec() immediately after creation	
4. □ duplicates all pages that are modified by the child process	

2 mod	allows the parent and child processes to initially share the same pages, but when either process lifies a page, a copy of the shared page is created.
1	. 🗸 copy-on-write
2	. $\square$ zero-fill-on-demand
3	. $\square$ memory-mapped
4	. $\square$ virtual memory fork
3. C	On systems that provide it, vfork() should always be used instead of fork().
•	☐ True
•	✓ False
10.	4
	uppose we have the following page accesses: 1 2 3 4 2 3 4 1 2 1 1 3 1 4 and that there are three frames within system. Using the LRU replacement algorithm, what is the number of page faults for the given reference ag?
1	. 🗖 14
2	. 🗅 13
3	. <b>√</b> 8
4	. 🗖 10
2. E	selady's anomaly states that
1	. $\square$ giving more memory to a process will improve its performance
2	. $\square$ as the number of allocated frames increases, the page-fault rate may decrease for all page replacement algorithms
3	. $\square$ for some page replacement algorithms, the page-fault rate may decrease as the number of allocated frames increases
4	. $\checkmark$ for some page replacement algorithms, the page-fault rate may increase as the number of allocated frames increases
	n the enhanced second chance algorithm, which of the following ordered pairs represents a page that would he best choice for replacement?
1	. <b>✓</b> (0,0)
2	. $\Box$ (0,1)
3	. $\square$ (1,0)
4	. $\square$ (1,1)
10.	5
1. T	The allocation algorithm allocates available memory to each process according to its size.
1	. $\square$ equal
2	. 🗖 global
3	. ✓ proportional
4	. □ slab
2. S	ystems in which memory access times vary significantly are known as
1	. □ memory-mapped I/O
2	. demand-paged memory

4.	□ copy-on-write memory
3. No	n-uniform memory access has little effect on the performance of a virtual memory system.
•	□ True
•	✓ False
10.6	
1	occurs when a process spends more time paging than executing.
1.	✓ Thrashing
2.	☐ Memory-mapping
3.	$\Box$ Demand paging
4.	□ Swapping
2. Th	e is an approximation of a program's locality.
1.	□ locality model
2.	✓ working set
3.	$\square$ page fault frequency
4.	$\square$ page replacement algorithm
3. If t	the page-fault rate is too high, the process may have too many frames.
•	□ True
•	✓ False
10.7	
1. Me	emory compression is commonly used in mobile systems instead of swapping.
•	✓ True
•	□ False
2. In	memory compression,
1.	$\square$ compression ratio needs to be balanced with the temporary memory needed for compression.
2.	$\square$ compression ratio needs to be balanced with the number of pages to be compressed.
3.	$\checkmark$ compression ratio needs to be balanced with the compression time.
4.	$\square$ compression ratio needs to be balanced with page size.
3. Me	emory compression is faster than swapping pages to hard disks, but slower than swapping pages to SSDs
•	□ True
•	✓ False

3.  $\checkmark$  non-uniform memory access

10.8
1. What size segment will be allocated for a $39~\mathrm{KB}$ request on a system using the Buddy system for kernel memory allocation?
1. □ 39 KB
2. □ 42 KB
3. <b>✓</b> 64 KB
4. $\square$ None of the above
2. Which of the following statements is false with regard to allocating kernel memory?
1. $\square$ Slab allocation does not suffer from fragmentation.
2. $\square$ Adjacent segments can be combined into one larger segment with the buddy system.
3. $\checkmark$ Because the kernel requests memory of varying sizes, some of which may be quite small, the system does not have to be concerned about wasting memory.
4. $\square$ The slab allocator allows memory requests to be satisfied very quickly.
3. Which of the following is considered a benefit when using the slab allocator?
1. $\square$ Memory is allocated using a simple power-of-2 allocator.
2. $\square$ It allows kernel code and data to be efficiently paged.
3. $\square$ It allows larger segments to be combined using coalescing.
4. ✓ There is no memory fragmentation.
10.9
1. The is the number of entries in the TLB multiplied by the page size.
1. ☐ TLB cache
2. □ page resolution
3. ✓ TLB reach
4. □ hit ratio
2. Only a fraction of a process's working set needs to be stored in the TLB.
• 🗖 True
• 🗸 False
3. Prepaging can be an advantage, because
1. $\checkmark$ the cost of using prepaging is less than the cost of servicing the corresponding page faults.
2. $\square$ the initial pages a process accesses are typically well known in advance.
3. $\square$ prepaging starts before a process is scheduled for execution.

4.  $\square$  All of the above.

- 1. Windows uses a local page replacement policy \_\_\_\_\_.
  - 1.  $\square$  when a process exceeds its working set minimum
  - 2.  $\checkmark$  when a process exceeds its working set maximum
  - 3.  $\square$  when the system undergoes automatic working set trimming
  - 4.  $\square$  under all circumstances
- 2. Which of the following statements is false with regard to Solaris memory management?
  - 1.  $\square$  The speed at which pages are examined (the scanrate) is constant.
  - 2.  $\square$  The pageout process only runs if the number of free pages is less than lotsfree.
  - 3.  $\square$  An LRU approximation algorithm is employed.
  - 4.  $\square$  Pages selected for replacement may be reclaimed before being placed on the free list.
- 3. In Linux,
  - 1.  $\square$  pages in active list may be moved to inactive list.
  - 2.  $\square$  pages in inactive list may be moved to active list.
  - 3.  $\square$  pages in inactive list are may be reclaimed for the free list.
  - 4. All of the above.

### 12.1 I/O Systems

A. The two conflicting trends of I/O devices are increasing diversity and the need for a uniform interfa	A.	The two	conflicting	trends of I	/O	devices	are in	creasing	diversity	and	the	need	for a	uniform	interfa	ace:
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- 🗸 True
- □ False
- B. A controller manages communication between a device and the CPU:
- 🗸 True
- □ False
- C. Most CPUs allow some interrupts to be ignored so that a critical instruction cannot be interrupted:
- 🗸 True
- □ False
- D. A given device may require different drivers for different operating systems:
- 🗸 True
- 🖵 False
- E. An alternative to a nonblocking system call is an asynchronous I/O operation:
- 🗸 True
- □ False
- F. Kernel I/O subsystem provides scheduling, buffering, and caching:
- 🗸 True
- $\square$  False
- G. The lifecycle of an I/O request does not require many CPU cycles:
- 🗖 True
- 🗸 False
- H. The STREAMS mechanism is designed for UNIX System V:
- 🗸 True
- □ False
- I. I/O operations do not have a significant impact on performance:
- 🗖 True
- 🗸 False

## 13.1 File-System Interface

A. Attributes of a file include name, type, and size:
• ✓ True
• 🖵 False
B. Operations that may be performed on a file include create, read, and write:
• ✓ True
• 🖵 False
C. The two fundamental ways of accessing a file are sequential and direct access:
• 🗸 True
• 🖵 False
D. A volume is a single storage unit that contains a file system:
• 🗸 True
• 🖵 False
E. The most common way of structuring directories is a tree structure:
• 🗸 True
• 🖵 False
F. UNIX systems allow acyclic-graph directories:
• 🗸 True
• 🗖 False
G. If a mount point is /home and the device jane/programs is mounted there, the full path to the programs directory is $/home/jane/programs$ :
• 🗸 True
• 🗖 False
H. One type of remote file system is NFS (Network File System):
• 🗸 True
• 🗖 False
I. One type of distributed information system is DNS (Domain Name System):
• 🗸 True
• 🗖 False
J. The three general classifications of users for each file are owner, group, and other:
• 🗸 True
• 🖵 False

### 18.1 Overview

A. Type 0 supervisors are most commonly applied in mainframes:
• 🗸 True
• 🖵 False
B. Paravirtualization does not affect the guest operating system:
• 🗖 True
• ✓ False
C. Application containment is one of the most efficient virtualization approaches:
• 🗖 True
• ✓ False
18.2 History
A. Microsoft launched first commercial virtual machines:
• □ True
• 🗸 False
B. The virtualization requirement postulate that virtual machine manager has full control over system resources
• 🗸 True
• 🖵 False
C. Intel 80 x 86 CPUs were popular base hardware for virtualization purposes in late 1970s:
• 🗖 True
• 🗸 False
18.3 Benefits and features
A. Host system is protected from a virtual machine, but several virtual machines running on the same host are not protected from each other:
• 🗖 True
• ✓ False
B. Virtual machine managers allow to make snapshots of host system:
• 🗖 True
• ✓ False
C. When a single image of a virtual machine is used as a source for multiple running guests, we call this templating
• 🗸 True
• 🖵 False

## 18.4 Building blocks

A. Guest virtual machines share information about their interrupts:

	□ True
•	✓ False
B. Th	he drawback of privileged instructions lies in:
1.	$\square$ sequential realization
2.	$\checkmark$ additional time overhead
3.	$\Box$ pointer issues
4.	$\Box$ difficult memory management
C. Ne	ested page tables allow for:
1.	$\square$ data corruption protection when creating new guests and their paging systems
2.	$\hfill \Box$ up of the virtual machine manager paging
3.	$\checkmark$ efficient management of page tables for guests and virtual machine manager
4.	$\square$ managing higher number of guests by virtual machine manager
18.5	Types of virtual machines and their management
A. W	Thich type of virtual machine manager offers purely hardware-based support for virtualization:
1.	✓ type 0 hypervisor
2.	□ type 1 hypervisor
3.	☐ type 2 hypervisor
4.	☐ type 3 hypervisor
В. Но	ow do you call a situation, where there are too many virtual machines being created:
1.	$\square$ virtual machine overload
2.	$\square$ virtual machine overflow
3.	✓ virtual machine sprawl
4.	$\square$ virtual machine sequencing
	That type of solution should be recommended for a bank that has updated its hardware architecture, but for lity reasons would like to maintain the old software:
1.	$\square$ application containment
2.	✓ emulation
3.	□ paravirtualization
	□ type 1 hypervisor

## 18.6 Virtualization and operating system components

A. In synchronous writes data is being stored in caches, with caller obtaining the control:
• 🗖 True
• 🗸 False
B. As guests may have multiple addresses, virtual machine manager must work as a virtual router for incoming packages:
• 🗸 True
• 🗖 False
C. What allows virtual machine manager to re-translate guest pages into real page tables:
1. □ virtual cache
2. ✓ nested page tables
3. □ virtual page tables
4. □ linked page tables