

EGE UNIVERSITY
LOGIC DESIGN LABORATORY
EXPERIMENT-2

Minimization of Boolean Functions

EXPERIMENTAL WORK

Minimize the following Boolean functions by using Karnaugh Map method.

- $F(a, b, c, d) = \sum (2, 3, 8, 9, 12, 13, 14)$

- $F(x, y, z, t) = \sum (0, 3, 5, 7, 11, 13), d(x, y, z, t) = \sum (4, 6, 14, 15)$

Implement these functions using 7408 AND, 7432 OR and 7404 NOT gates. Use switches for the inputs and connect the output to a led.

