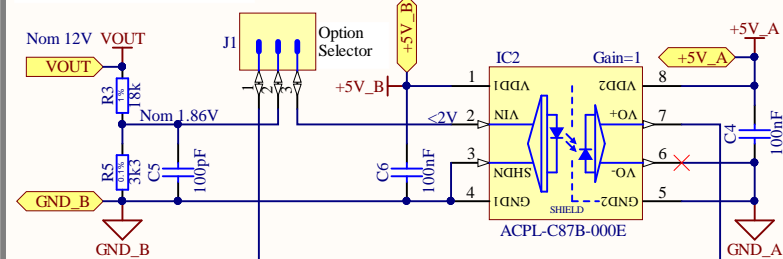
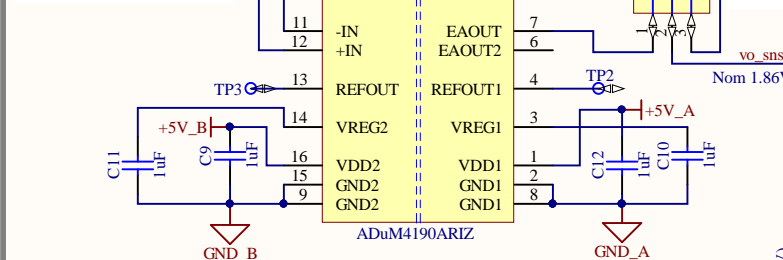


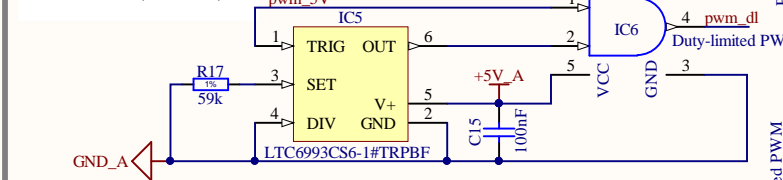
ISOLATED VOLTAGE SENSING: OPTION #1



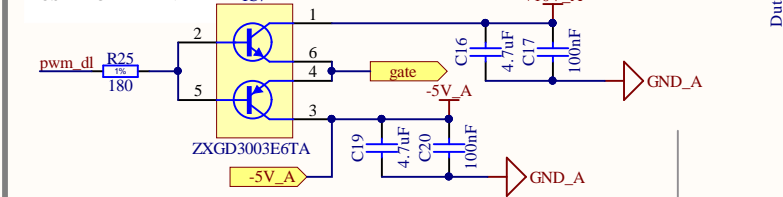
ISOLATED VOLTAGE SENSING: OPTION #2



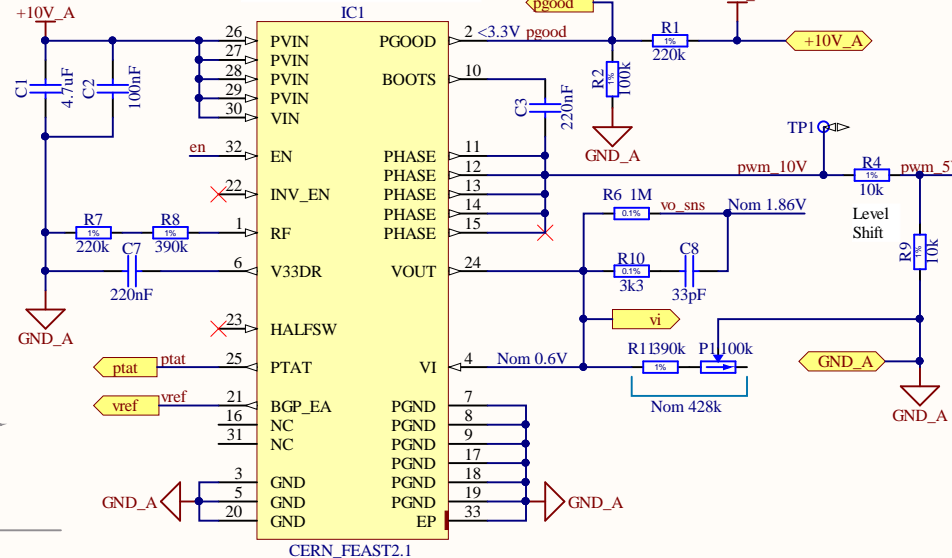
DUTY LIMITER (MAX 60%)



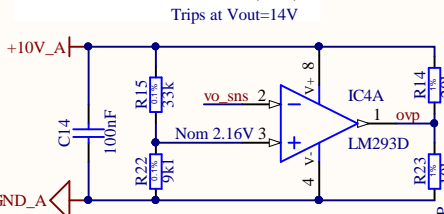
MOSFET GATE DRIVER



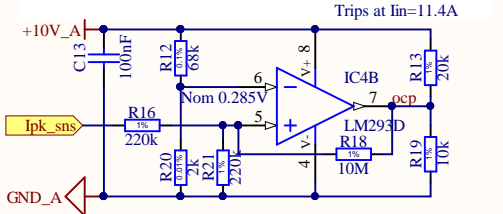
FEAST-BASED CONTROLLER



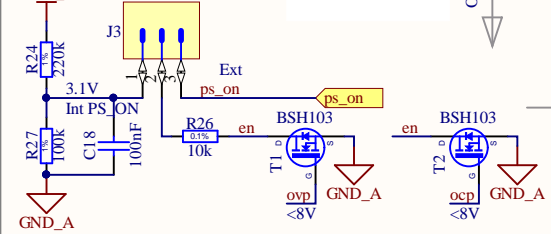
OVER-VOLTAGE PROTECTION (OVP)



OVER-CURRENT PROTECTION (OCP) WITH HYSTERESIS



ENABLE CIRCUIT



Gate pulses
To Power Stage:
power_stage.schdoc

Title **RaToPUS DC/DC: Controller**

Size: **A4**

Number:

Revision: **0.3**

Date: **03/04/2019**

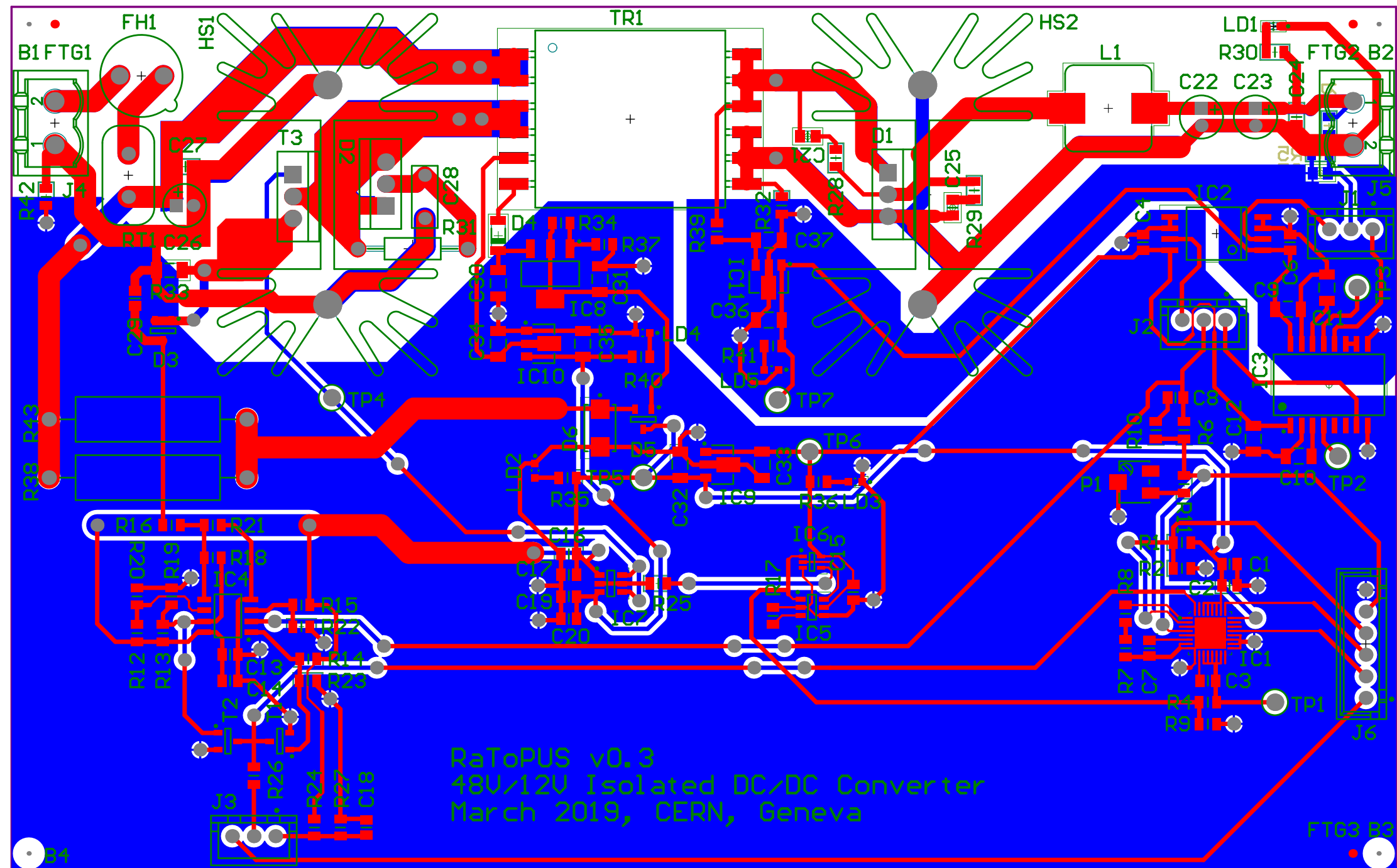
Time: **16:11:35**

Sheet **3** of **3**

File: **C:\DIOT_RTPS\Design\RaToPUS\controller.schdoc**

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Esplanade des Particules 1
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Altium



RaToPUS v0.3
48V/12V Isolated DC/DC Converter
March 2019, CERN, Geneva

FTG3 B3